

0.8 A sensitive gate SCR

Features

- I_{T(RMS)} = 0.8 A
- \blacksquare $V_{DRM} / V_{RRM} = 600 V$
- $I_{GT} = 30 \text{ to } 200 \mu A$

Applications

- Limited gate current topologies
- Ground fault circuit interrupters
- Overvoltage crowbar protection in power supplies
- Protection in electronic ballasts
- Capacitive discharge ignitions
- Ignitors (lighting, oven...)

Description

The X006 SCR can be used as on/off function in applications where topology does not offer high current for gate triggering.

This device is optimized in forward voltage drop and inrush current capabilities for reduced power losses and high reliability in harsh environments.

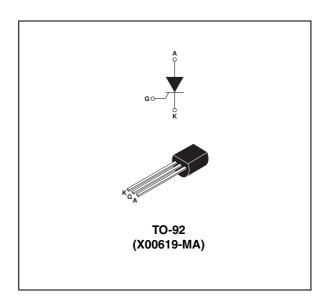


Table 1. Device summary

I _{T(RMS)}	0.8 A
V_{DRM} / V_{RRM}	600 V
I _{GT}	30 to 200 μA

Characteristics X00619

1 Characteristics

Table 2. Absolute ratings (limiting values, $T_J = 25$ °C unless otherwise specified)

Symbol	l Parameter			Value	Unit
I _{T(RMS)}	On-state rms current (180 °Conduction angle)	T _L = 85 °C	0.8	Α	
IT _(AV)	Average on-state current (180 °Conduction angle)		T _L = 85 °C	0.5	Α
	Non vanatitiva avvaa naak on atata avvaant	$t_p = 8.3 \text{ ms}$	T 05.00	10	^
ITSM	I _{TSM} Non repetitive surge peak on-state current		T _j = 25 °C	9	Α
l ² t	I ² t Value for fusing	$t_p = 10 \text{ ms}$	T _j = 25 °C	0.4	A ² s
di/dt	Critical rate of rise of on-state current $I_G = 2 \times I_{GT}$, $t_r \le 100 \text{ ns}$ $F = 60 \text{ Hz}$		T _j = 125 °C	50	A/µs
I _{GM}	Peak gate current $t_p = 20 \mu s$		T _j = 125 °C	1	Α
P _{G(AV)}	Average gate power dissipation $T_j = 125$ °C				W
T _{stg} T _j	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C

Table 3. Electrical characteristics ($T_J = 25$ °C unless otherwise specified)

Symbol	Test conditions			Value	Unit	
1			MIN.	30	шА	
I _{GT}	$V_D = 12 \text{ V}, R_L = 140 \Omega$		V_D = 12 V, R_L = 140 Ω	MAX.	200	μΑ
V_{GT}			IVIAA.	0.8	V	
V_{GD}	$V_D = V_{DRM,} \; R_L = 3.3 \; k\Omega$, $R_{GK} = 1 \; k\Omega$ $T_j = 125 \; ^{\circ}C$		MIN.	0.2	V	
V_{RG}	I _{RG} = 10 μA		MIN.	5	V	
I _H	$I_T = 50 \text{ mA}, R_{GK} = 1 \text{ k}\Omega$		MAX.	5	mA	
IL	$I_G = 1$ mA, $R_{GK} = 1$ k Ω		MAX.	6	mA	
dV/dt	$V_D = 67\% V_{DRM,} R_{GK} = 1 k\Omega$ $T_j = 125 °C$		MIN.	40	V/µs	

Table 4. Static electrical characteristics (per diode)

Symbol	Test conditions			Value	Unit
V_{TM}	$I_{TM} = 1 \text{ A, } t_p = 380 \mu\text{s}$	T _j = 25 °C		1.35	V
V _{TO}	Threshold voltage	T _ 105 °C		0.85	V
Rd	Dynamic resistance	T _j = 125 °C	MAX	245	mΩ
I _{DRM} I _{RRM}	$V_{DRM} = V_{RRM}, R_{GK} = 1 \text{ k}\Omega$	T _j = 25 °C		1	μΑ
		T _j = 125 °C		100	μΑ

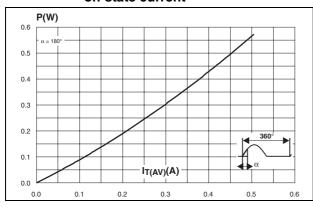
X00619 Characteristics

Table 5. Thermal resistances

Symbol	Parameter	Value	Unit
R _{th(j-a)}	Junction to ambient (DC)	150	°C/W
R _{th(j-l)}	Junction to lead (DC)	70	C/VV

Figure 1. Maximum average power dissipation vs. average on-state current

Figure 2. Average and DC on-state current vs. case temperature



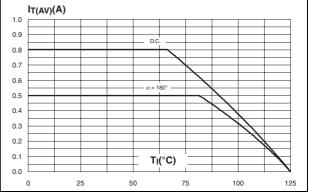
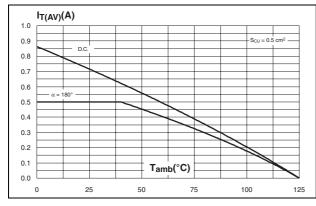
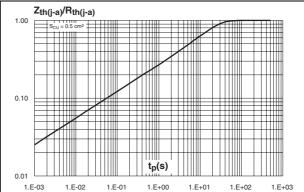


Figure 3. Average and DC on-state current vs. case temperature

Figure 4. Relative variation of thermal impedance junction to ambient vs. pulse duration

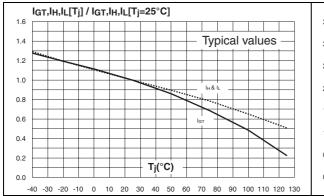




Characteristics X00619

Figure 5. Relative variation of gate trigger, holding and latching current vs. junction temperature

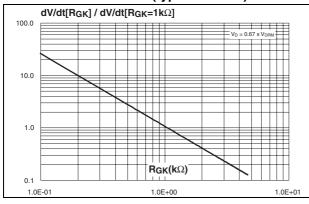
Figure 6. Relative variation of holding current vs. gate-cathode resistance (typical values)



I_H[R_{GK}] / I_H[R_{GK}=1kΩ]
3.5
3.0
2.5
2.0
1.5
1.0
0.5
0.0
1.E-02
1.E-01
1.E+00
1.E+01
1.E+02

Figure 7. Relative variation of dV/dt immunity vs. gate-cathode resistance (typical values)

Figure 8. Relative variation of dV/dt immunity vs. gate-cathode capacitance (typical values)



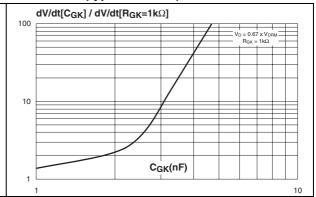
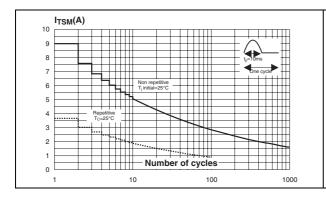
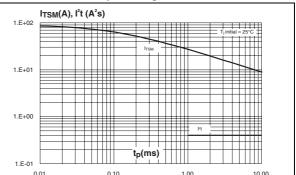


Figure 9. Surge peak on-state current vs. number of cycles

Figure 10. Non repetitive surge peak on state current for a sinusoidal pulse and corresponding value of I²T



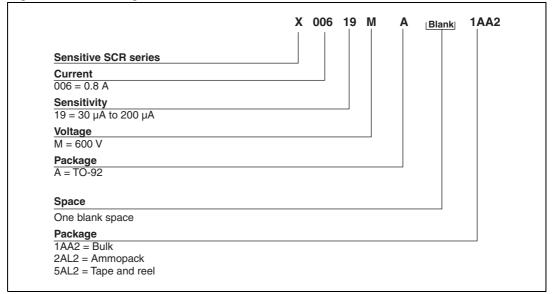


I_{TM}(A) 10.00 $0.0 \quad 0.2 \quad 0.4 \quad 0.6 \quad 0.8 \quad 1.0 \quad 1.2 \quad 1.4 \quad 1.6 \quad 1.8 \quad 2.0 \quad 2.2 \quad 2.4 \quad 2.6 \quad 2.8 \quad 3.0$

Figure 11. On-state characteristics (maximum values)

Ordering information scheme 2

Figure 12. Ordering information scheme



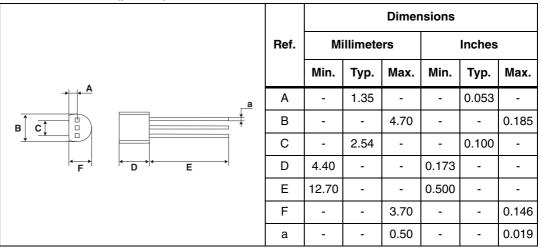
Package information X00619

3 Package information

Epoxy meets UL94, V0

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: www.st.com. ECOPACK[®] is an ST trademark.

Table 6. TO-92 (plastic) dimensions



4 Ordering information

Table 7. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
X00619MA 1AA2				2500	Bulk
X00619MA 2AL2	X0619 MA	TO-92	0.2 g	2000	Ammopack
X00619MA 5AL2				2000	Tape and reel

5 Revision history

Table 8. Document revision history

Date	Revision	Changes
26-May-2009	1	First issue

Please Read Carefully:

Information in this document is provided solely in connection with ST products. STMicroelectronics NV and its subsidiaries ("ST") reserve the right to make changes, corrections, modifications or improvements, to this document, and the products and services described herein at any time, without notice.

All ST products are sold pursuant to ST's terms and conditions of sale.

Purchasers are solely responsible for the choice, selection and use of the ST products and services described herein, and ST assumes no liability whatsoever relating to the choice, selection or use of the ST products and services described herein.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted under this document. If any part of this document refers to any third party products or services it shall not be deemed a license grant by ST for the use of such third party products or services, or any intellectual property contained therein or considered as a warranty covering the use in any manner whatsoever of such third party products or services or any intellectual property contained therein.

UNLESS OTHERWISE SET FORTH IN ST'S TERMS AND CONDITIONS OF SALE ST DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY WITH RESPECT TO THE USE AND/OR SALE OF ST PRODUCTS INCLUDING WITHOUT LIMITATION IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION), OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

UNLESS EXPRESSLY APPROVED IN WRITING BY AN AUTHORIZED ST REPRESENTATIVE, ST PRODUCTS ARE NOT RECOMMENDED, AUTHORIZED OR WARRANTED FOR USE IN MILITARY, AIR CRAFT, SPACE, LIFE SAVING, OR LIFE SUSTAINING APPLICATIONS, NOR IN PRODUCTS OR SYSTEMS WHERE FAILURE OR MALFUNCTION MAY RESULT IN PERSONAL INJURY, DEATH, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE. ST PRODUCTS WHICH ARE NOT SPECIFIED AS "AUTOMOTIVE GRADE" MAY ONLY BE USED IN AUTOMOTIVE APPLICATIONS AT USER'S OWN RISK.

Resale of ST products with provisions different from the statements and/or technical features set forth in this document shall immediately void any warranty granted by ST for the ST product or service described herein and shall not create or extend in any manner whatsoever, any liability of ST.

ST and the ST logo are trademarks or registered trademarks of ST in various countries.

Information in this document supersedes and replaces all information previously supplied.

The ST logo is a registered trademark of STMicroelectronics. All other names are the property of their respective owners.

© 2009 STMicroelectronics - All rights reserved

STMicroelectronics group of companies

Australia - Belgium - Brazil - Canada - China - Czech Republic - Finland - France - Germany - Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Philippines - Singapore - Spain - Sweden - Switzerland - United Kingdom - United States of America

www.st.com

