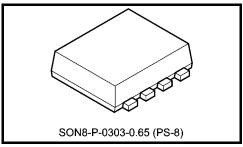
TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA48LS00F

300 mA Output Current, Variable Output Voltage and Low Dropout Voltage Regulator with ON/OFF Control Switch

The TA48LS00F consists of small-surface mount type low-dropout regulators with an output current of 300 mA (maximum) and an ON/OFF control switch. Control by an EN (ON/OFF) terminal enables the regulator to be operated only when required (output ON). The output voltage can be arbitrarily set by external resistance. Therefore, the TA48LS00F can be used for a wide range of applications. TA48LS00F is suitable for use in the power supply circuits of AV, OA and other digital devices equipped with a stand-by function, and of battery operated portable data devices of various types, where they will contribute to energy saving.



Weight: 0.017 g (Typ.)

Features

• Built-in ON/OFF control function (active high)

• Maximum output current : 300 mA

• Output voltage $V_{OUT} = 1.5 \text{ to } 5.0 \text{ V}$

Reference voltage accuracy : V_{REF} ± 2.3 % (@T_j = 25°C)
 Low quiescent current : 1 mA (Typ.) (@I_{OUT} = 0 A)
 Low standby current (output OFF mode): 0.2 µA (Typ.)

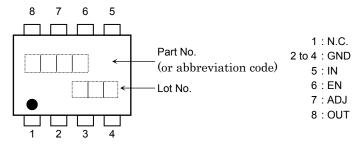
• Low-dropout voltage $: 0.5 \text{ V (Max) } (@V_{OUT} = 3.3 \text{ V, } I_{OUT} = 150 \text{ m A})$

• Protection function : Overcurrent / Thermal shutdown

• Package type : PS-8

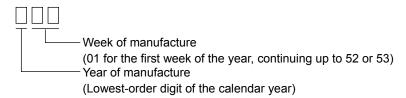
Marking / Pin Assignment

| Part No. | Part No. (abbreviation code) |
|-----------|---------------------------------|
| TA48LS00F | LS00 |



(•) on the lower left of the marking indicates Pin 1.

^{*} Lot No.: The lot no. consists of three digits. The first digit represents the last digit of the year of manufacture, and the following two digits indicates the week of manufacture between 01 and either 52 or 53.



The product(s) in this document ("Product") contain functions intended to protect the Product from temporary small overloads such as minor short-term overcurrent or overheating. The protective functions do not necessarily protect Product under all circumstances. When incorporating Product into your system, please design the system (1) to avoid such overloads upon the Product, and (2) to shut down or otherwise relieve the Product of such overload conditions immediately upon occurrence. For details, please refer to the notes appearing below in this document and other documents referenced in this document.

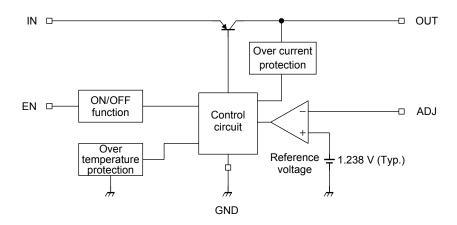
Pin Description

| Pin No. | Symbol | Description |
|---------|--------|---|
| 1 | N.C. | Non-connection |
| 2 to 4 | GND | Ground terminal |
| 5 | IN | Input terminal. Connected by capacitor (C _{IN}) to GND. |
| 6 | EN | Output ON/OFF control terminal. Output is ON when this pin is set to "High", OFF when this pin is open or set to "Low". |
| 7 | ADJ | Output voltage feedback to regulator. It is connected to an error amplifier with V _{REF} =1.238 V (Typ.). |
| 8 | OUT | Output terminal. Connected by capacitor (C _{OUT}) to GND. |

How to Order

| Part No. | Package Type and Capacity |
|---------------------|---------------------------|
| TA48LS00F(TE85L, F) | Tape (3000 pcs/reel) |

Block Diagram



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Absolute Maximum Rating (Ta = 25°C)

| Characteristic | | Symbol | Rating | Unit |
|------------------------------|-----------|-------------------|----------------------|------|
| Input voltage | | V _{IN} | 14 | V |
| EN Input voltage | | V _{EN} | 14 | ٧ |
| Output current | | lout | I _{OUT} 300 | |
| Operating junction temperate | ture | T _{jopr} | -40 to 150 | °C |
| Junction temperature | | Tj | 150 | °C |
| Storage temperature | | T _{stg} | -55 to 150 | °C |
| Power dissipation(Note 3) | Ta = 25°C | P _D | 1.2 | W |

Note 1: Do not apply current and voltage (including reverse polarity) to any pin that is not specified.

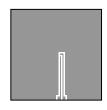
Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

| Characteristic | Symbol | Max | Unit |
|---|-----------------------|-----|------|
| Thermal resistance, junction to ambient | R _{th (j−a)} | 102 | °C/W |

Note 3: Glass epoxy board



Material: FR-4 $25.4\times25.4\times1.6$ Unit: (mm) Cu base thickness: 35 μ m

Operating Input Voltage Range

| Characteristic | Symbol | Min | Тур. | Max | Unit |
|----------------|-----------------|-------------|------|------|------|
| Input voltage | V _{IN} | 2.5(Note 4) | _ | 14.0 | V |

Note 4: This is the voltage at which the IC begins operating. V_D must be considered when determining the best input voltage for the application.

Output Voltage Range

| Characteristic | Symbol | Min | Тур. | Max | Unit |
|----------------|------------------|-----|------|-----|------|
| Output voltage | V _{OUT} | 1.5 | _ | 5.0 | V |

Protection Function (Reference)

| Characteristic | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-----------------------------------|----------------------|--|-----|------|-----|------|
| Thermal shutdown | T_{SD} | V _{IN} = 4.3 V | 150 | 170 | _ | °C |
| Thermal shutdown hysteresis width | T _{SD(hys)} | VIN = 4.3 V | _ | 15 | _ | °C |
| Peak circuit current | IPEAK | $V_{IN} = 5.3 \text{ V}, T_j = 25^{\circ}\text{C}$ | 300 | 500 | _ | mA |
| | | $V_{IN} = 8.3 \text{ V}, T_j = 25^{\circ}\text{C}$ | 300 | 500 | _ | IIIA |
| Short circuit current | I _{SC} | $V_{IN} = 5.3 \text{ V}, T_j = 25^{\circ}\text{C}$ | _ | 300 | _ | mA |
| | | $V_{IN} = 16 \text{ V} , T_j = 25^{\circ}\text{C}$ | _ | 300 | _ | IIIA |

Note 5: Protection features do not guarantee that the device will be kept below the absolute maximum rated conditions. Ensure that the devices operate within the limits of the maximum rating when in actual use.

TA48LS00F Electrical Characteristics (Unless otherwise specified, V_{EN} = V_{IN}, V_{OUT} = 3.3 V, C_{IN} = 0.33 μ F, C_{OUT} = 1 μ F, T_j = 25°C)

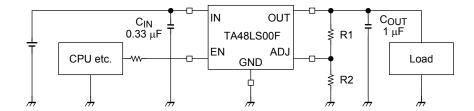
| Characteristic | Symbol | Test Condition | Min | Тур. | Max | Unit | |
|---|----------------------|--|-------|-------|-------|-------------------|--|
| Reference voltage | V _{REF} | V _{IN} = 5.3 V, I _{OUT} = 150 mA | 1.209 | 1.238 | 1.267 | V | |
| Line regulation | Reg·line | $4.3~V \leq V_{IN} \leq 8.3~V, \\ I_{OUT} = 150~mA$ | _ | 2 | 20 | mV | |
| Load regulation | Reg·load | $V_{IN} = 5.3 \text{ V}, \text{ 5 mA} \leq I_{OUT} \leq 300 \text{ mA}$ | _ | 3 | 20 | mV | |
| Quiescent current | 1_ | $4.3~V \leq V_{IN} \leq 8.3~V, \\ I_{OUT} = 0~A$ | _ | 1.0 | 1.7 | 0 | |
| | lΒ | $ 4.3 \text{ V} \leq \text{V}_{IN} \leq 8.3 \text{ V}, \\ \text{I}_{OUT} = 300 \text{ mA} $ | _ | 5 | 10 | mA | |
| Quiescent current (OFF mode) | I _{B(OFF)} | | _ | 0.2 | 5.0 | μА | |
| Ota-dia-a-a-di-a-a-a-d | I _{Bstart} | V _{IN} = 2.1 V, I _{OUT} = 0 A | _ | 1.4 | 4.0 | - mA | |
| Starting quiescent current | | $V_{IN} = 2.8 \text{ V}, I_{OUT} = 300 \text{ mA}$ | _ | 8.3 | 18.0 | | |
| Output noise voltage | V _{NO} | $\begin{split} V_{IN} = 5.3 \ V, \ I_{OUT} = 50 \ mA, \\ 10 \ Hz \le f \le 100 \ kHz \end{split}$ | _ | 70 | _ | μV _{rms} | |
| Ripple rejection | R.R. | $V_{IN} = 5.3 \text{ V}, I_{OUT} = 50 \text{ mA},$ $f = 120 \text{ Hz}$ | _ | 60 | _ | dB | |
| Dranaut valtage | \/- | I _{OUT} = 150 mA | _ | 0.2 | 0.5 | V | |
| Dropout voltage | V _D | I _{OUT} = 300 mA | _ | 0.3 | 0.6 | V | |
| Output control voltage (ON) | V _{EN(ON)} | _ | 2 | _ | _ | V | |
| Output control voltage (OFF) | V _{EN(OFF)} | _ | _ | _ | 0.8 | V | |
| Output control current (ON) | I _{EN(ON}) | V _{IN} = V _{EN} = 5.3 V | _ | 53 | 75 | μА | |
| Average temperature coefficient of output voltage | T _{CVO} | $\begin{split} &V_{IN}=5.3 \ V, \ I_{OUT}=5 \ mA, \\ &0^{\circ}C \leq T_{j} \leq 125^{\circ}C \end{split}$ | _ | 0.3 | _ | mV/°C | |

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Electrical Characteristics Common to All Products

• $T_j = 25$ °C in the measurement conditions of each item is the standard condition when a pulse test is carried out, and any drift in the electrical characteristic due to a rise in the junction temperature of the chip may be disregarded.

Application Circuit Example



• Be sure to connect a capacitor near the input terminal and output terminal between both terminals and GND. The use of a monolithic ceramic capacitor (B Characteristic or X7R) of low ESR (equivalent series resistance) is recommended. The IC may oscillate due to external conditions (output current, temperature, or the type of the capacitor used). The type of capacitor required must be determined by the actual application circuit in which the IC is used.

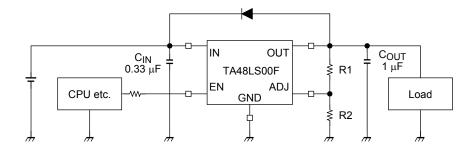
Setting Output Voltage

• The output voltage is determined by the equation shown below. When you control the output voltage with R1, a recommended value to use for R2 is 5 k Ω . R1 and R2 must be placed as close as possible to each other, and the board trace to the ADJ terminal must be kept as short as possible.

$$V_{\text{OUT}} = V_{\text{REF}} \times \left(1 + \frac{R1}{R2} \right)$$

Usage Precautions

• The IC might be destroyed if a voltage greater than the input terminal voltage is applied to the output terminal, or if the input terminal is connected to GND during operation. To prevent such an occurrence, connect a diode as in the following diagram.



- There is a possibility that internal parasitic devices may be generated when momentary transients cause a terminal's potential to fall below that of the GND terminal. In such case, that the device could be destroyed. The voltage of each terminal and any state must therefore never fall below the GND potential.
- Depending on the load conditions, a steep increase in the input voltage applied (V_{IN}) may cause a momentary rise in output voltage (V_{OUT}) even if the EN (enable) pin is Low. Treat with care.

· Low voltage

Do not apply voltage to the Product that is lower than the minimum operating voltage, or the Product's protective functions will not operate properly and the Product may be permanently damaged.

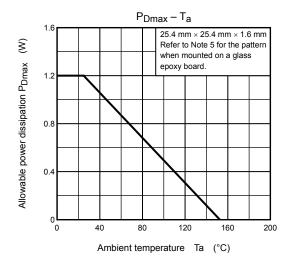
• Overcurrent Protection

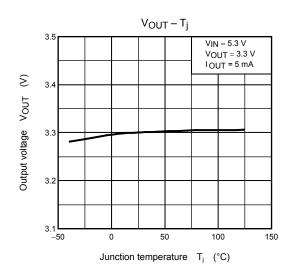
The overcurrent protection circuits in the Product are designed to temporarily protect Product from minor overcurrent of brief duration. When the overcurrent protective function in the Product activates, immediately cease application of overcurrent to Product. Improper usage of Product, such as application of current to Product exceeding the absolute maximum ratings, could cause the overcurrent protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

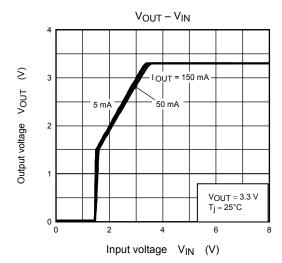
• Thermal shutdown Protection

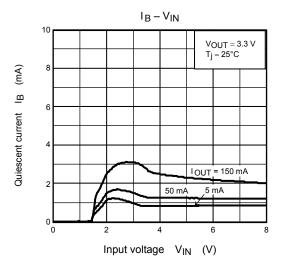
The thermal shutdown circuits in the Product are designed to temporarily protect Product from minor overheating of brief duration. When the thermal shutdown protective function in the Product activates, immediately correct the overheating situation. Improper usage of Product, such as the application of heat to Product exceeding the absolute maximum ratings, could cause the thermal shutdown protection circuit not to operate properly and/or damage Product permanently even before the protection circuit starts to operate.

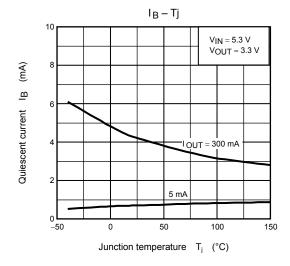
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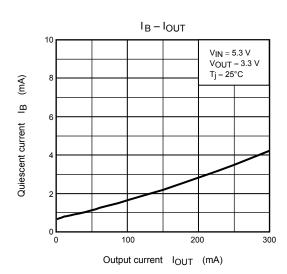


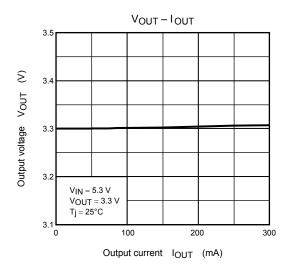


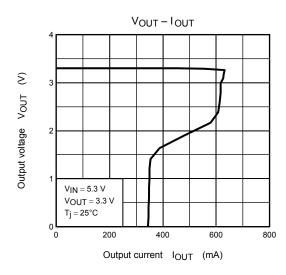


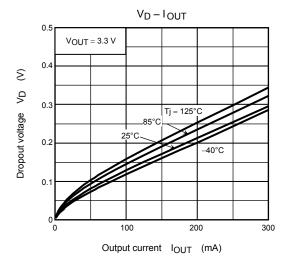


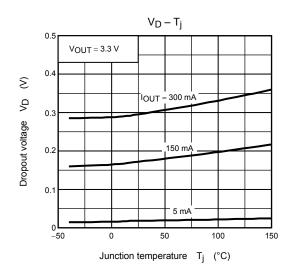


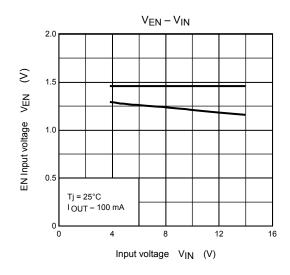


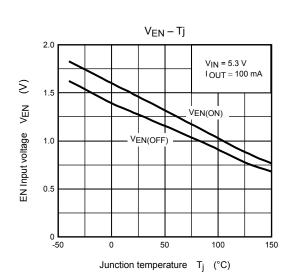








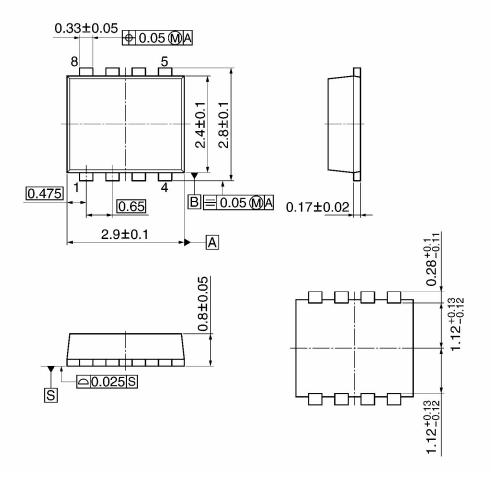




Package Dimensions

SON8-P-0303-0.65 (PS-8)

Unit: mm



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Weight: 0.017 g (Typ.)

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