TOSHIBA Thyristor Silicon Planar Type

# SF5G49, SF5J49, USF5G49, USF5J49

#### **Medium Power Control Applications**

Repetitive peak off-state voltage: VDRM = 400V, 600 V
 Repetitive peak reverse voltage: VRRM = 400V, 600 V

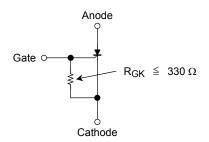
• Average on-state current: I<sub>T</sub> (AV) = 5 A

• Gate trigger current: IGT = 70 μA max

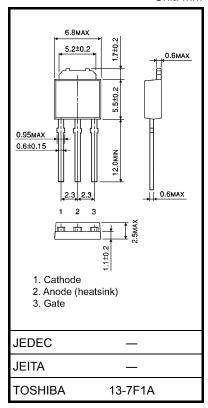
### **Maximum Ratings**

Characteristics		Symbol	Rating	Unit	
Repetitive peak off-state voltage and Repetitive	SF5G49 USF5G49	$V_{DRM}$	400	V	
peak reverse voltage $(R_{GK} = 330 \Omega)$	SF5J49 USF5J49	$V_{RRM}$	600	<b>v</b>	
Non-repetitive peak reverse voltage	SF5G49 USF5G49	\/	500	V	
(non-repetitive $<$ 5 ms, $T_j = 0\sim125^{\circ}\text{C}$ , $R_{GK} = 330 \Omega$ )	SF5J49 USF5J49	V <sub>RSM</sub>	720		
Average on-state current		I <sub>T (AV)</sub>	5	Α	
R.M.S on-state current		I <sub>T (RMS)</sub>	7.8	Α	
Peak one cycle surge on-state current (non-repetitive)		I <sub>TSM</sub>	65 (50 Hz)	А	
I <sup>2</sup> t limit value		l <sup>2</sup> t	20	A <sup>2</sup> s	
Peak gate power dissipation		P <sub>GM</sub>	0.5	W	
Average gate power dissipation		P <sub>G</sub> (AV)	0.05	W	
Peak forward gate voltage		$V_{FGM}$	5	V	
Peak reverse gate voltage		$V_{RGM}$	<b>-5</b>	V	
Peak forward gate current		I <sub>GM</sub>	200	mA	
Junction temperature		Tj	-40~125	°C	
Storage temperature range		T <sub>stg</sub>	-40~125	°C	

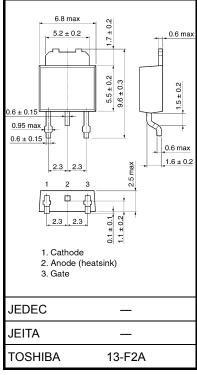
Note: Should be used with gate resistance as shown below:



Unit: mm



Weight: 0.36 g (typ.)



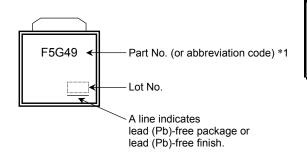
Weight: 0.28 g (typ.)



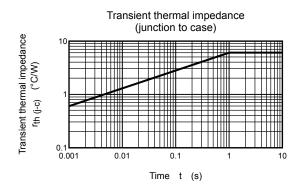
## **Electrical Characteristics (Ta = 25°C)**

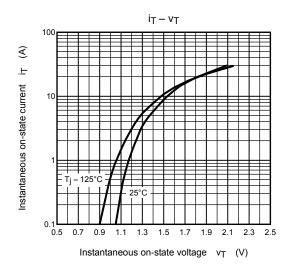
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Repetitive peak off-state current and Repetitive peak reverse current	I <sub>DRM</sub> I <sub>RRM</sub>	$V_{DRM} = V_{RRM} = Rated$ $R_{GK} = 330 \Omega$	_	_	20	μА
Peak on-state voltage	V <sub>TM</sub>	I <sub>TM</sub> = 12 A	_	_	1.6	V
Gate trigger voltage	V <sub>GT</sub>	$V_D = 6 \text{ V}, R_L = 100 \Omega$	_	_	0.8	V
Gate trigger current	I <sub>GT</sub>	$R_{GK} = 330 \Omega$	3	_	70	μА
Gate non-trigger voltage	$V_{GD}$	V <sub>D</sub> = Rated × 2/3, Tc = 125°C	0.2	_	_	V
Critical rate of rise of off-state voltage	dv/dt	$V_{DRM}$ = Rated × 2/3, Tc = 75°C R <sub>GK</sub> = 330 $\Omega$ , Exponential rise	_	50	_	V/μs
Holding current	lΗ	$R_L = 100 \Omega$ , $R_{GK} = 330 \Omega$	_	2.5	_	mA
Thermal resistance (junction to case)	R <sub>th (j-c)</sub>	DC	_	_	6.0	°C/W

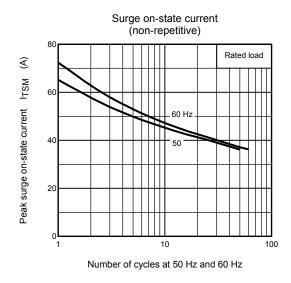
## Marking

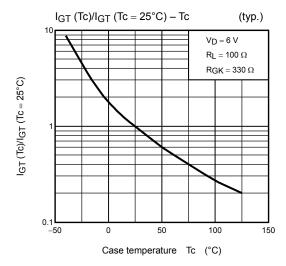


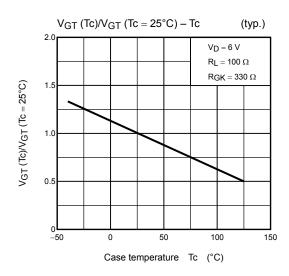
	Part No. (or abbreviation code)	Part No.		
*1	F5G49	SF5G49, USF5G49		
	F5J49	SF5J49, USF5J49		

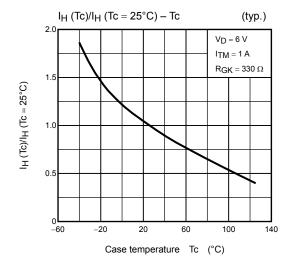


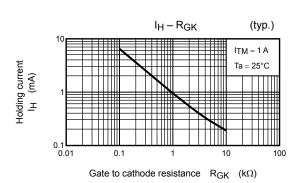


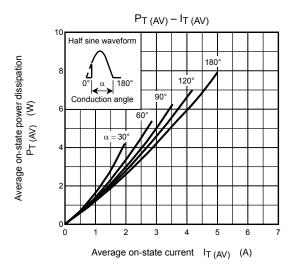


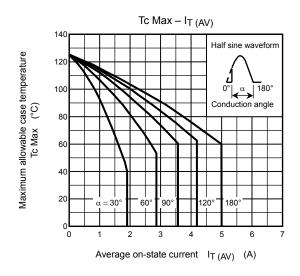


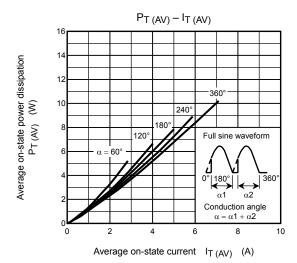


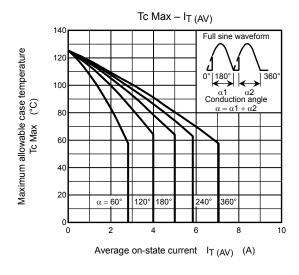


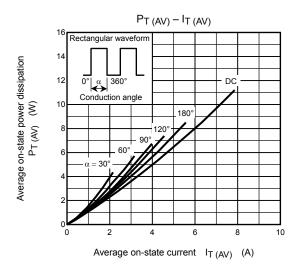


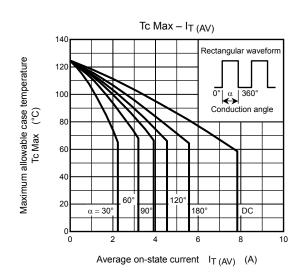




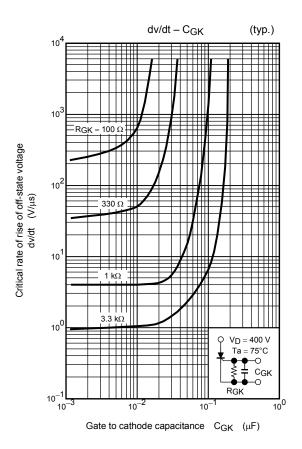


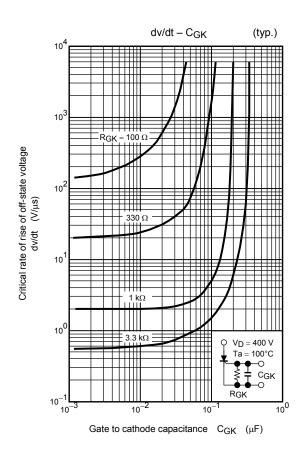


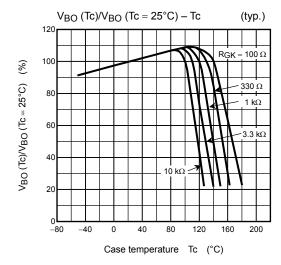




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