10BASE-TNETWORK COMPONENTS

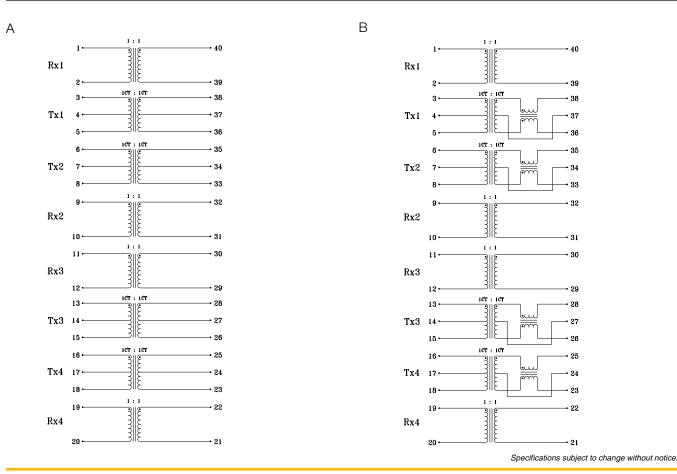


- Designed for use with AMD multi-port 10Base-T transceiver chipsets, such as the eIMR (79C984A), eIMR+ (79C985) and QuIET (79C988)/IMR2 (79C983) where digital filtering has been incorporated into the silicon
- Filterless quad, 4-port designs with common configuration offering choice of customized EMI performance and grounding techniques
- Quad design offers best space and cost efficiency per port
- Low profile, surface mount packaging, rated to 225°C peak IR reflow temperature
- 2000 Vrms isolation

ELECTRICALS AT 25°C

| Part No. | Insertion Loss (dB) Max 1-10MHz | Return Loss (dB) Min 5MHz-10MHz | Crosstalk (dB) Min 5MHz-10MHz | CM-CM Rej (dB) Min | | | | |
|--------------|---------------------------------------|---------------------------------------|-------------------------------------|-----------------------|-------|--------|--------|-----------|
| | | | | 5-10MHz | 50MHz | 100MHz | 200MHz | Schematic |
| S553-5999-18 | -1.0 | -20 | -50 | -50 | -35 | -25 | -20 | А |
| S553-5999-23 | -1.0 | -20 | -50 | -50 | -35 | -25 | -20 | В |
| S553-5999-24 | -1.0 | -20 | -50 | -50 | -35 | -25 | -20 | С |
| S553-5999-39 | -1.0 | -20 | -50 | -50 | -35 | -25 | -20 | С |

SCHEMATICS



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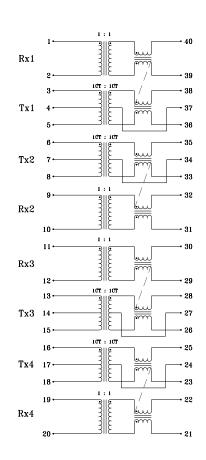


10BASE-TNETWORK COMPONENTS

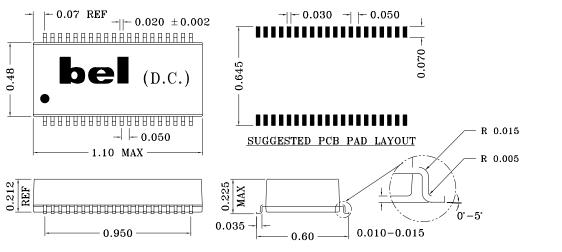
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SCHEMATICS (CONT'D)

С



MECHANICAL



Specifications subject to change without notice.

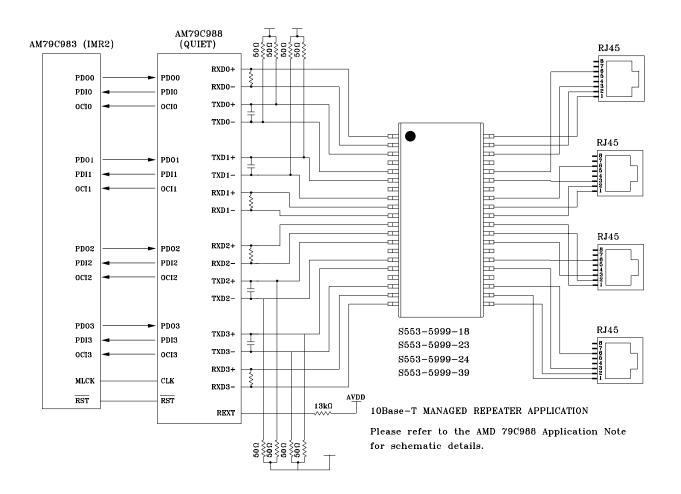
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APPLICATION CIRCUIT



Specifications subject to change without notice.



10BASE-TNETWORKCOMPONENTS

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APPLICATION NOTES

- Bel has developed these part types for use with AMD's product line of quad, 4-port PHY devices that incorporate digital filtering techniques within the silicon itself. Bel's "filterless magnetics" are optimized for this specific application and create a very cost efficient design solution. Each Bel part type contains 4 channels of transmit and receive transformers to provide for wave shaping, high voltage isolation and EMI noise suppression. In certain applications, additional differential filtering may be required to comply with IEEE 802.3 standards.
- Bel has designed these parts as a family of parts with common footprint and pinouts to enable the designer to customize the use of common mode choke for optimum system performance.
- In multi-port system applications, good PCB layout and proper grounding techniques are very critical to achieve FCC class A and B equipment approvals. Bel recommendations are available and can be provided by contacting our engineering department or your local sales representative.
- Bel's low profile, surface mount packaging is ideal for high speed pick and place machinery. Parts can be shipped on tape and reel for high speed placement. Construction processes have been implemented for thermal compatibility with high temperature IR reflow assembly processing. Post dipping of leads assist with PC board solderability. Each part is optically inspected to meet rigid coplanarity requirements.
- The specific configuration of these Bel parts (Rx-Tx-Tx-Rx-Rx-Tx-Rx) is optimal for use with stacked or "double decker" RJ-45 connectors. This configuration should provide short and straight traces to the output connectors.

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