TOSHIBA THYRISITOR SILICON PLANAR TYPE

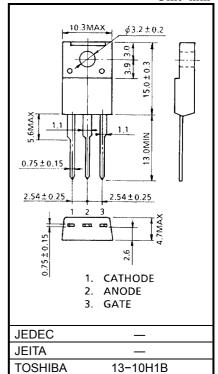
SF8GZ47,SF8JZ47

MEDIUM POWER CONTROL APPLICATIONS

- Repetitive Peak off-State Voltage : VDRM = 400, 600V Repetitive Peak Reverse Voltage
 - $: V_{RRM} = 400, 600V$
 - Average On–State Current
- $I_{T(AV)} = 8A$
- **Isolation Voltage**
- : $V_{Isol} = 1500V AC$



CHARACTERISTIC		SYMBOL	RATING	UNIT	
Repetitive Peak	SF8GZ47	\/	400	V	
Off-State Voltage and Repetitive Peak6 Reverse Voltage	SF8JZ47	V _{DRM} V _{RRM}	600		
Non-Repetitive Peak Reverse Voltage (Non-Repetitive <5ms, $T_j = 0~125^{\circ}C$)	SF8GZ47		500	V	
	SF8JZ47	V _{RSM}	720		
Average On-State Current (Half Sine Waveform Tc = 72°C)		I _{T (AV)}	8	А	
R.M.S On-State Current		I _{T (RMS)}	12.6	А	
Peak One Cycle Surge On-State Current (Non-Repetitive)		ITSM	120 (50 Hz)	A	
			132 (60 Hz)		
I ² t Limit Value		l ² t	72	A ² s	
Critical Rate of Rise of On-State Current (Note 1)		di / dt	100	Α / μs	
Peak Gate Power Dissipation		P _{GM}	5	W	
Average Gate Power Dissipation		P _{G (AV)}	0.5	W	
Peak Forward Gate Voltage		V _{FGM}	10	V	
Peak Reverse Gate Voltage		V _{RGM}	-5	V	
Peak Forward Gate Current		I _{GM}	2	А	
Junction Temperature		Tj	-40~125	°C	
Storage Temperature Range		T _{stg}	-40~125	°C	
Isolation Voltage (AC, t = 1 min.)		VISOL	1500	V	



Weight: 1.7g

Note 1: di / dt test condition,

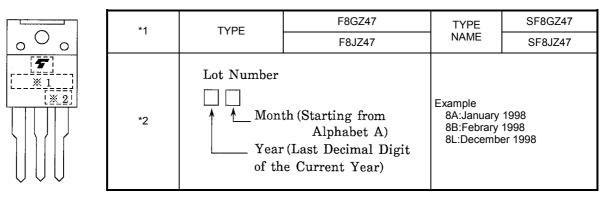
 $V_{DRM} = 0.5 \times Rated$, $I_{TM} \le 25A$, $t_{qw} \ge 10\mu s$, $t_{gr} \le 250$ ns, $i_{gp} = I_{GT} \times 2.0$

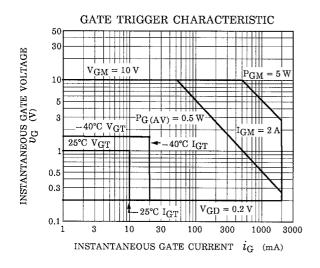
Unit: mm

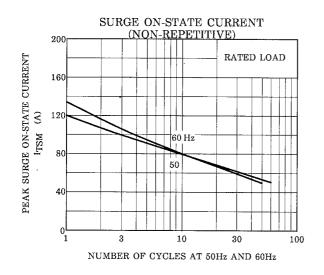
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

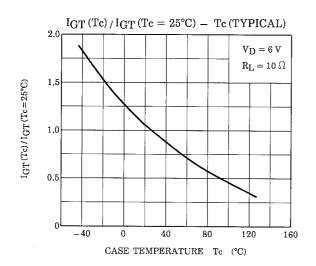
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse Current	I _{DRM} I _{RRM}	V _{DRM} = V _{RRM} = Rated	_	_	10	μA
Peak On-State Voltage	V _{TM}	I _{TM} = 25 A	_	_	1.5	V
Gate Trigger Voltage	V _{GT}	V _D = 6 V, R _I = 10 Ω	_	-	1.0	V
Gate Trigger Current	I _{GT}	$v_{\rm D} = 0 v, R_{\rm L} = 10 \Omega$		_	10	mA
Gate Non-Trigger Voltage	V _{GD}	V _D = Rated × 2 / 3, Tc = 125°C	0.2	_	_	V
Critical Rate of Rise of Off-State Voltage	dv / dt	V _{DRM} = Rated, Tc = 125°C Exponential Rise	_	50	_	V / µs
Holding Current	Ι _Η	V _D = 6 V, I _{TM} = 1 A	-	_	40	mA
Latching Current	١L	V _D = 6 V, f = 50Hz, t _{gw} = 50 µs, i _G = 30 mA	_	_	50	mA
Thermal Resistance	R _{th (j−c)}	Junction to Case	_	_	3.7	°C/W

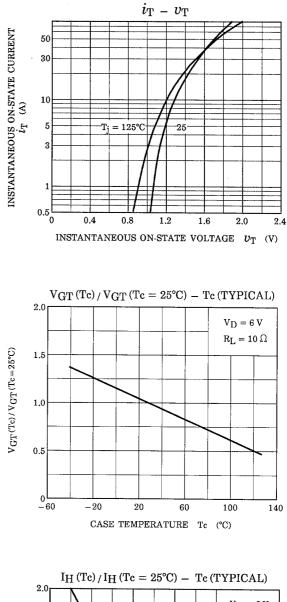
MARKING

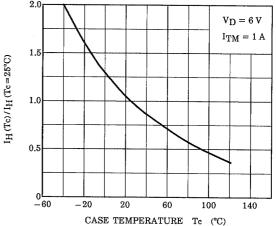




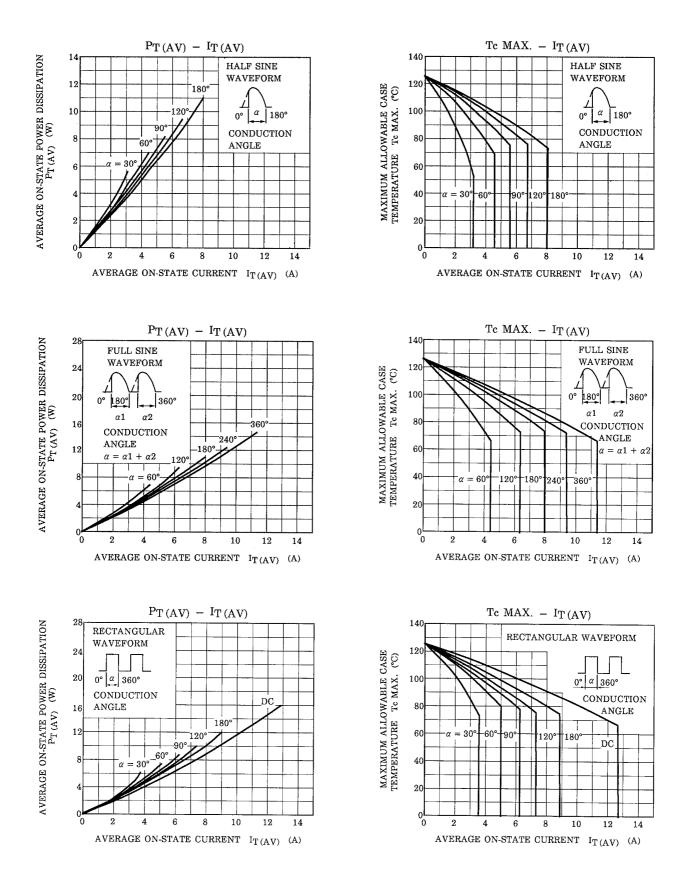




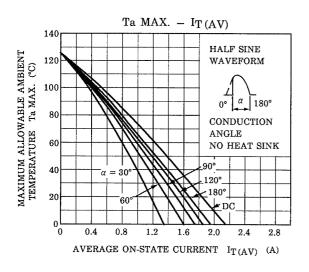


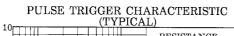


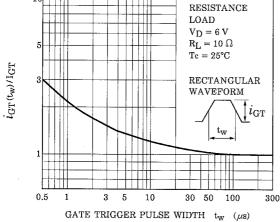
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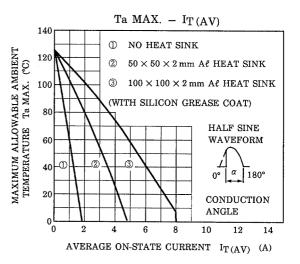


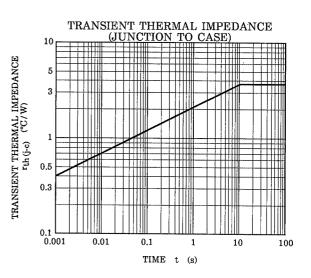
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