TOSHIBA BI-DIRECTIONAL TRIODE THYRISTOR SILICON PLANAR TYPE

SM16GZ47, SM16JZ47, SM16GZ47A, SM16JZ47A

AC POWER CONTROL APPLICATIONS

- Repetitive Peak Off-State Voltage: VDRM = 400V, 600V
- R.M.S On-State Current: IT (RMS) = 16A
- High Commutating (dv / dt)
- Isolation Voltage: VISOL = 1500V AC

ABSOLUTE MAXIMUM RATINGS

CHARACTER	ISTIC	SYMBOL	RATING	THAU
Repetitive Peak	SM16GZ47 SM16GZ47A	V_{DRM}	400	
Off-State Voltage	SM16JZ47 SM16JZ47A	V DRM	600	
R.M.S On-State Currer (Full Sine Waveform To	• •	I _{T (RMS)}	16	
Peak One Cycle Surge Current (Non-Repetitiv		I _{TSM}	150 (50Hz) 165 (60Hz)	A
I ² t Limit Value		I ² t	112.5	$\sqrt{\mathbb{A}^2}$ s
Critical Rate of Rise of Current	On-State (Note 1)	di / dt	50	A/µs
Peak Gate Power Dissi	pation	PGM	5 <	\ W
Average Gate Power D	issipation	P _G (AV)	0.5	//w
Peak Gate Voltage		VGM	10	\\
Peak Gate Current		I _{GM} 2		^^ A
Junction Temperature		\mathcal{T}_{j}	-40~125	°C
Storage Temperature R	ange	T _{stg}	-40~125	°C
Isolation Voltage (AC, t	= 1 min.)	VISOL	1500	V

Weight: 1.7 g (typ.)

Note 1: di / dt Test condition VDRM = 0.5 × Rated

I_{TM} ≤ 25A

tgw(≥(10µs)

t_{gr} ≤ 250ns

iGR = IGT × 2.0

Note 2: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

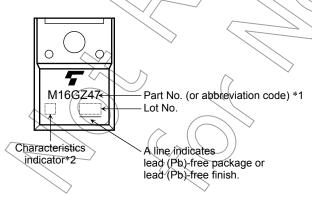
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



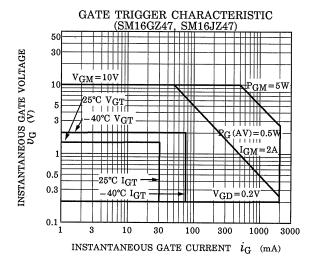
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

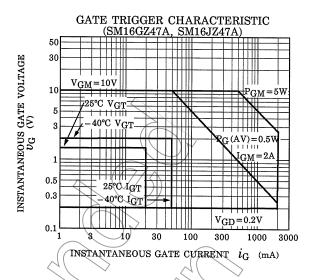
CHARACTERISTIC		SYMBOL	TEST CONDITION		MIN	TYP.	MAX	UNIT		
Repetitive Peak Off-State Current		I _{DRM}	V _{DRM} = Rated		_	_	20	μΑ		
Gate Trigger Voltage		I	V _{GT}	V _D = 12V, R _L = 20Ω	T2 (+) , Gate (+)	-	_	1.5	V	
		Ш			T2 (+) , Gate (-)	/	_	1.5		
		III			T2 (-) , Gate (-)		_	1.5		
		IV			T2 (-) , Gate (+)	(F) >-	_		
Gate Trigger Current SM16			I			T2 (+) , Gate (+)		_	30	
	SM16	SM16GZ47				T2 (+), Gate (-)	$\bigcirc))$	_	30	1
	SM16	SJZ47	III		$V_D = 12V$, $R_L = 20\Omega$	T2 (-) , Gate (-)	_	_	30	- mA
			IV	lgт		T2 (-), Gate (+)	_	_	_	
		SM16GZ47A SM16JZ47A	I			T2 (+), Gate (+)	_		20	
	SM16		II			T2 (+) , Gate (-)		4	20	
	SM16		III			T2 (¬) , Gate (¬)	-	(-/	> 20	
			IV			T2 (-) , Gate (+)		2)) —	
Peak On-State Voltage		V _{TM}	I _{TM} = 25A			90)	1.5	V		
Gate Non-Trigger Voltage		V_{GD}	V _D = Rated, Tc = 125°C		0.2	→ —	_	V		
Holding Current		lΗ	V _D = 12V, I _{TM} = 1A		(-)]	_	50	mA		
Thermal Resistance		R _{th (j-c)}	Junction to Case, AC		\ _	_	2.5	°C/W		
		SM16GZ47 SM16JZ47		dv / dt	V _{DRM} = Rated, T _j = 125°C Exponential Rise		_	300	_	- V / µs
	SM16GZ47 SM16JZ47		dv/dt	_			200	_		
Critical Rate of Rise of Off-State Voltage at Commutation		SM16GZ47 SM16JZ47			V _{DRM} = 400V, T _j = 125°C (di / dt) c = -8.7A / ms		10	_	_	- V/μs
		SM16GZ47 SM16JZ47		(dv / dt) c			4	_	_	

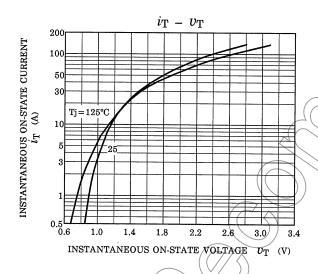


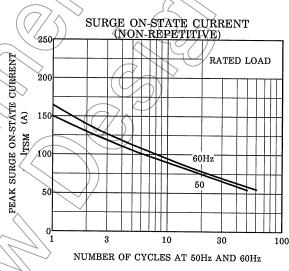


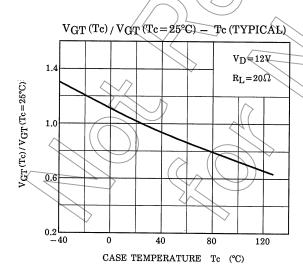
	Part No. (or abbreviation code)	Part No.		
*1	M16GZ47	SM16GZ47, SM16GZ47A		
,	M16JZ47	SM16JZ47, SM16JZ47A		
*2	Nothing	SM16GZ47, SM16JZ47		
٠٧	А	SM16GZ47A, SM16JZ47A		

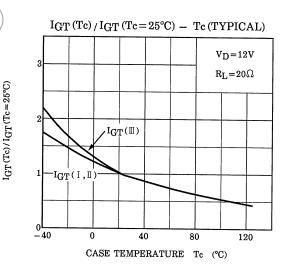




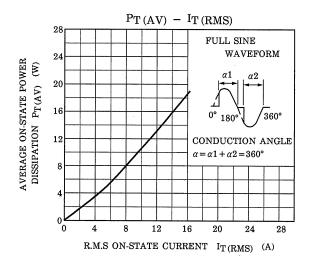


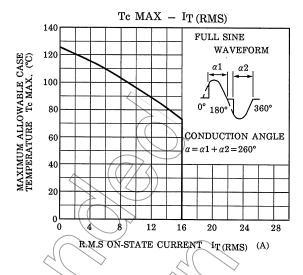


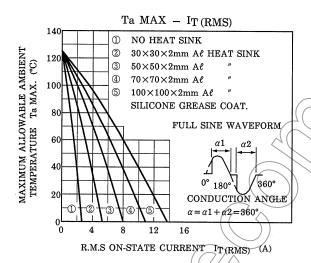


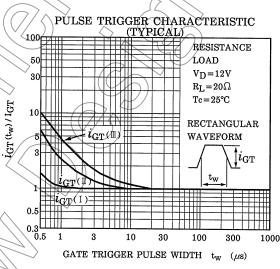


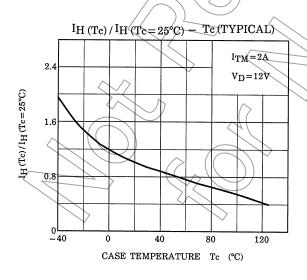
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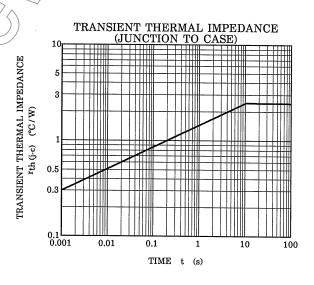




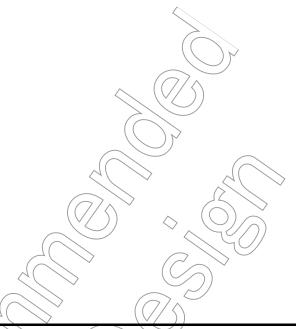








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