

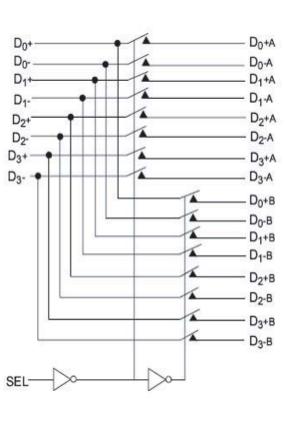
PI3HDMI412FT-B

2:1 Mux/DeMux High Bandwidth (> 5 Gbps) HDMI[™] 1.3 Compliant Signal Switch

Features

- 4-Differential Channel 2:1 Mux/DeMux
- HDMI[™] 1.1, 1.2, and 1.3 compatible
- Allowable Data Rate: > 5.0 Gbps
- Supports both AC coupled and DC coupled signals
- Switching speed: 4ns
- Isolation: -40dB @ 2.0 Gbps
- Crosstalk: -31dB @ 2.0 Gbps
- ESD: Data bits @ 8kV contact, select bit @ 2kV HBM
- Low bit-to-bit skew
- Enable/Disable Time: 9ns
- Bidirectional
- Packaging (Pb-free & Green): — 42-pin TQFN (ZH42)
 - 48-pin BOSOP (B48)

Block Diagram



Description

Pericom Semiconductor's PI3HDMI[™] series of switch circuits are targeted for high-resolution video networks that are based on DVI/HDMI[™] standards, and TMDS signal processing. The PI3HD-MI412FT-B is an 8- to 4-Channel Mux/DeMux Switch. The device multiplexes differential signals to one of two corresponding outputs. The switch is bidirectional and offers little or no attenuation of the high-speed signals at the outputs. It is designed for low bit-to-bit skew and high channel-to-channel noise isolation.

The allowable data rate of 5.0Gbps provides the resolution required by the next generation HDTV and PC graphics. Three differential channels are used for data (video signals for DVI or audio/video signals for HDMITM), and one differential channel is used for Clock for decoding the TMDS signals at the outputs.

Because of its passive bidirectional feature, this switch can be used either at the video drivers side or at the receiver side. For PC graphics applications, the device sits at the drivers side to switch between multiple display units such as LCD monitor, projector, TV, etc. For consumer video applications, the device sits at the receiver end to switch between the source components such as DVD, D-VHS, STB, etc.

Pin Description 42-pin TOFN **48-pin BOSOP** 8888 43 Vpo V_{BS} 42 41 40 39 Vod 2 47 Vss 38 🗖 D0+A 3 46 D0*A Vss 37 🗖 D0-A Ē 45 D0-A -4 Vod D1+A Ьз 36 🛄 D0+ 5 D1+A V_{BB} \square 44 D1-A 00- 20 4 35 🗖 43 В D0+ D2+A Vss 🗖 5 345 7 42 0 D0-D2-A 33 📖 D1+ 6 41 8 32 Vss D3+A 白7 D1- \square Q 40 D1+ **р**а 8 D3-A 31 Vod 39 10 D1-SEL 🛄 9 V_{SB} 30 🗖 Vpp 29 C C0+8 28 C C0-8 27 C D1+8 11 38 Vss 5 V_{BS} 10 D2+ 11 D2- 11 C 12 37 Vop 36 13 SEL C 35 613 614 26 🛄 D1-8 14Vss ٧ss 34 15 25 🛄 D2+B D2+ D3+ 33 C DS- 15 24 🗖 D2-B 16 D2-23 🗖 D3+B 32 D1-8 V_{DD} 16 17 V_{BS} 5 31 V_{SS} 🗖 17 18 22 🛄 D3-B Vss D3+ C.... 19 30 D2+B 19 20 21 D3-D2-8 D2-8 D3+8 D3+8 V55 V55 0000 29 20 Vss C 28 21 8888 Vod 22 27 Vss C 23 26 Vod 25 24 Vss

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Pin Description

Pin # (BQSOP)	Pin # (TQFN)	Pin Name	Description
2, 4, 12, 21, 23, 25, 36, 48	2, 8, 16, 18, 20, 30, 40, 42	V _{DD}	+ Power supply 3.30V
1, 3, 5, 8, 11, 14, 17, 20, 22, 24, 26, 31, 37, 42, 47	1, 5, 10, 13, 17, 19, 21, 39, 41	V _{SS}	- Power supply
13	9	SEL	Select pin, see truth table
6, 7, 9, 10, 15, 16, 18, 19, 27-30, 32-35, 38-41, 43-36	3, 4, 6, 7, 11, 12, 14, 15, 22-29, 31-38	$Dx^{+/-x} + CLK^{+/-x}$	Data + Clk bits for TMDS signal

Truth Table⁽¹⁾

Function	SEL
Dx to Dx ^A	L
Dx to Dx^{B}	Н

Note:

1. x=0,1,2,3

Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

65°C to +150°C
0.5V to +2.5V
0V to V _{DD} +0.5V
120mA

Note: Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Power Supply Characteristics

Paramenter	Description	Min.	Max.	Units
V _{DD}	Positive Power Supply	3.0	3.6	V
V _{SS}	Negative Power Supply	1.5	1.6	V

DC Electrical Characteristics for Switching over Operating Range $(T_A = -40^{\circ}C \text{ to } +85^{\circ}C)$

Paramenter	Description	Test Conditions (1)	Min.	Typ. ⁽²⁾	Max.	Units	
$V_{IH}^{(3)}$	Input HIGH Voltage	Guaranteed HIGH level	$0.65(V_{DD}-V_{SS})+V_{SS}$				
$V_{IL}^{(3)}$	Input LOW Voltage	Guaranteed LOW level	$-0.5 + V_{SS}$		$0.35(V_{DD}-V_{SS})+V_{SS}$	V	
V _{IK}	Clamp Diode Voltage	V _{DD} =Max., I _{IN} =-18mA		$-0.7+V_{SS}$	$-1.2+V_{SS}$		
I _{IH}	Input HIGH Current	V _{DD} =Max., V _{IN} =V _{DD}			±5		
I _{IL}	Input LOW Current	V _{DD} =Max., V _{IN} =V _{SS}			±5	μA	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at $T_A = 25^{\circ}C$ ambient and maximum loading.

3. V_{IH} & V_{IL} are for SEL input only.

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Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	$V_{DD} = Max., V_{IN} = V_{DD} \text{ or } V_{SS}$		300	400	μΑ

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.

2. Typical values are at $T_A = 25^{\circ}C$ ambient and maximum loading.

Switching Characteristics (T_A = -40° to +85°C, V_{SS} =0V, V_{DD} =1.8V ±10%)

Paramenter	Description	Min.	Max.	Units
tpZH, tpZL	Line Enable Time - SEL to N^A , $N^{B(1)}$	0.5	8.0	
tPHZ, tPLZ	Line Disable Time - SEL to N^A , $N^{B(1)}$	0.5	4.0	ns

Notes:

1. For measurement setup, please see "Test Circuit For Electrical Characteristic" on page 8, and "Switching waveforms" on page 9

Dynamic Electrical Characteristics Over the Operating Range (T_A= -40° to +85°C)

Parameter	Description	Test Conditions	Min.	Тур.	Max.	Units
X _{TALK}	Crosstalk	See Fig. 1 for Measurement Setup, @ 2.0 Gbps		-31		dB
O _{IRR}	OFF Isolation	See Fig. 2 for Measurement Setup, @ 2.0 Gbps		-40		uБ
BW	-3dB BW			2.7		GHz
		@ 1.65 Gbps		-0.87		
Insertion Loss		@ 2.0 Gbps		-0.95		dB
		@ 2.5 Gbps		-1.18		uБ
		@ 3.3 Gbps		-1.85		



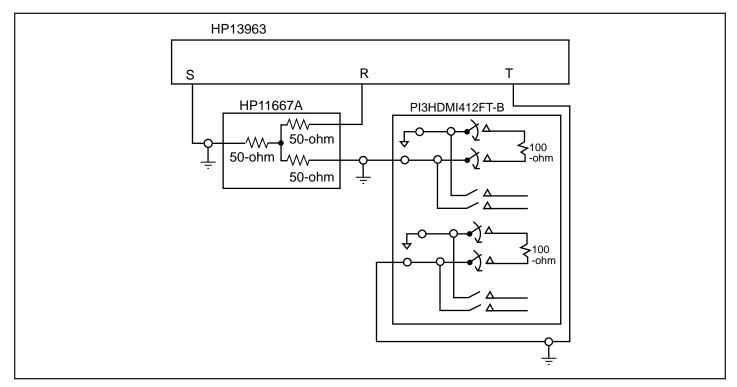


Fig 1. Crosstalk Setup

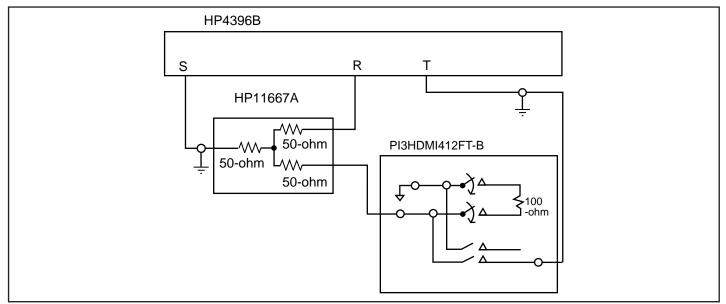


Fig 2. Off-isolation setup

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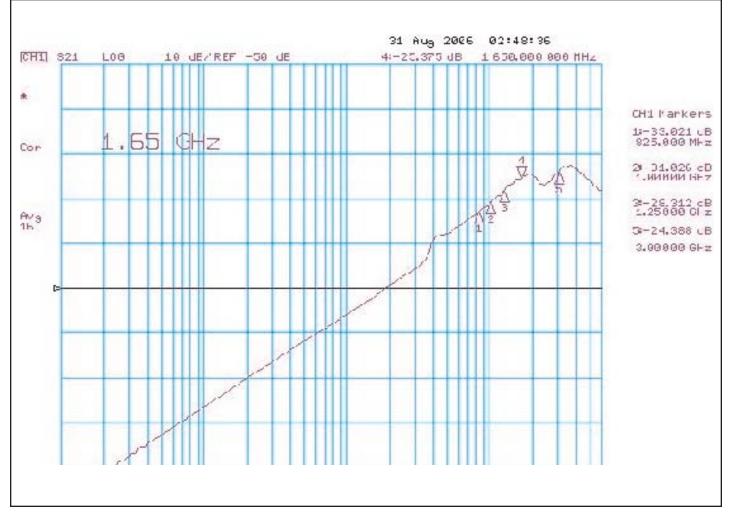


Fig 3. Crosstalk

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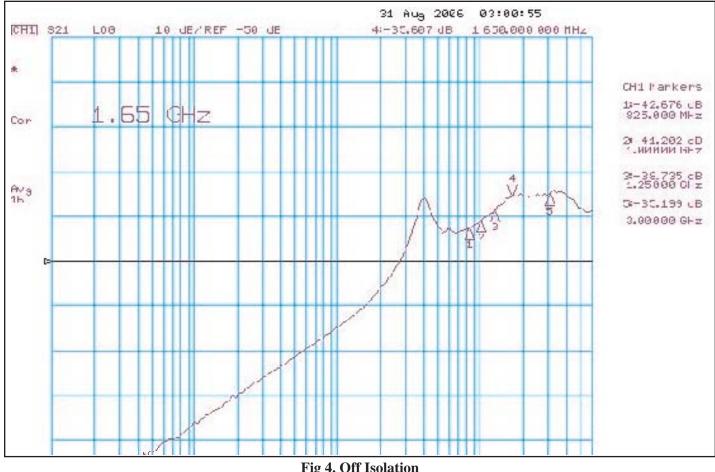


Fig 4. Off Isolation

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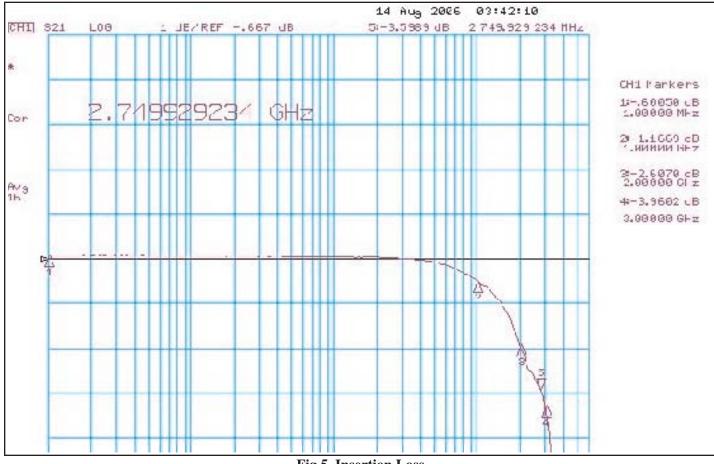


Fig 5. Insertion Loss

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Application Measurements

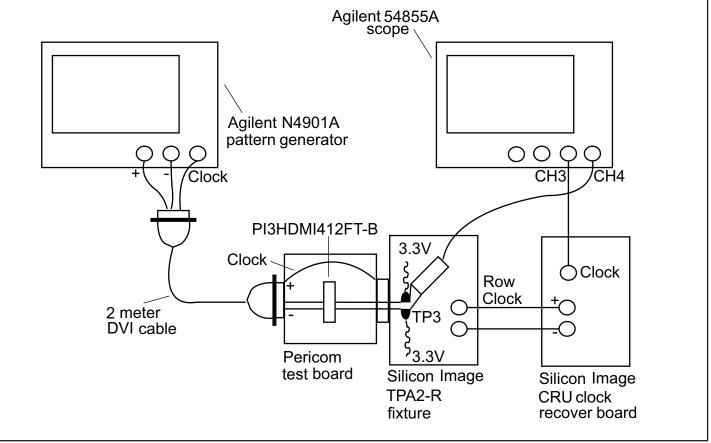


Fig 6. TMDS TP3 Rx eye compliance Test Setup

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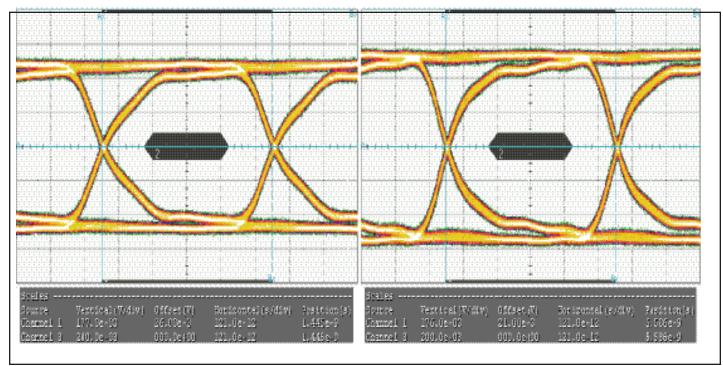
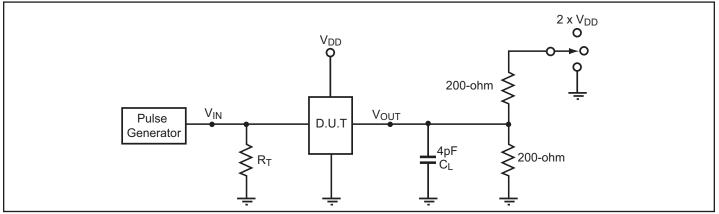


Fig 7. TP3 (Rx) with 2-meter cable eye measurements (Right eye is with no switch. Left eye is with switch.)

Test Circuit for Electrical Characteristics



Notes:

- 1. C_L = Load capacitance: includes jig and probe capacitance.
- 2. R_T = Termination resistance: should be equal to Z_{OUT} of the Pulse Generator
- 3. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
- Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- $\text{4.} \quad \text{All input impulses are supplied by generators having the following characteristics: } \text{PRR} \leq \text{MHz}, \text{Z}_{\text{O}} = 50\Omega, \text{t}_{\text{R}} \leq 2.5 \text{ns}, \text{t}_{\text{F}} \leq 2.5 \text{ns}. \ \text{MHz}, \text{Z}_{\text{O}} = 50\Omega, \text{t}_{\text{R}} \leq 2.5 \text{ns}. \ \text{MHz}, \text{MHz$
- 5. The outputs are measured one at a time with one transition per measurement.

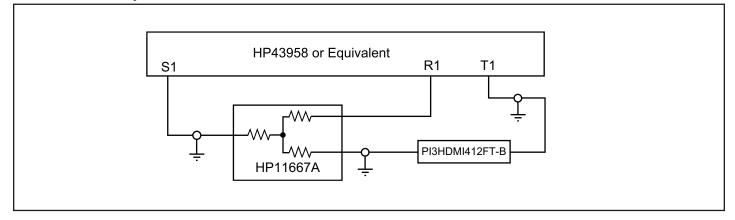
Switch Positions

Test	Switch
t _{PLZ} , t _{PZL}	2 x (V _{DD} - V _{SS})
t _{PHZ} , t _{PZH}	V _{SS}
Prop Delay	Open

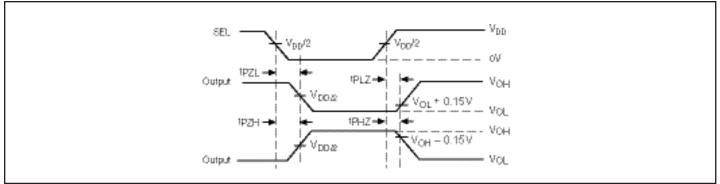
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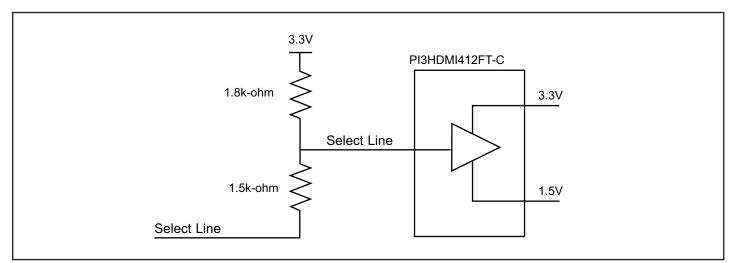
Test Circuit for Dynamic Electrical Characteristics



Switching Waveforms



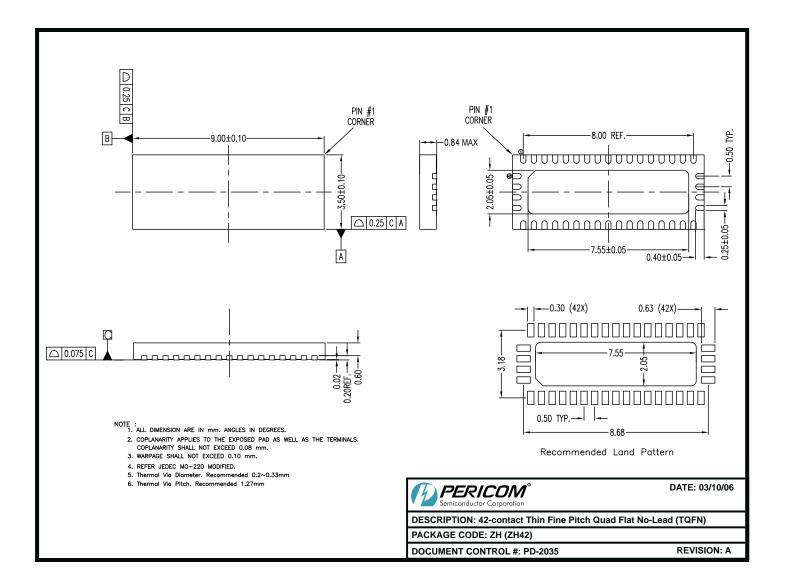
Voltage Waveforms Enable and Disable Times



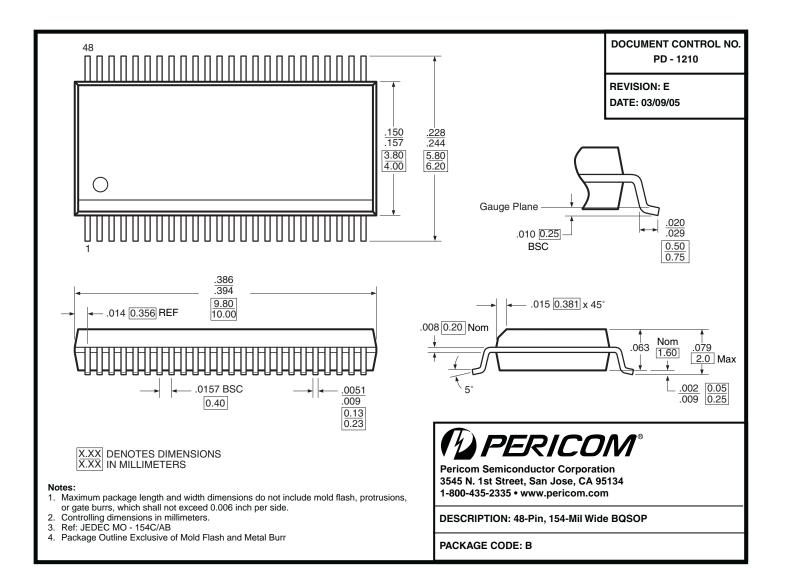
Example of a circuit that also needs to be connected to the select lines of the PI3HDMI412FT-B

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Ordering Information

Ordering Code	Package Code	Package Description
PI3HDMI412FT-BZHE	ZH	Pb-free & Green, 42-pin TQFN
PI3HDMI412FT-BBE	В	Pb-free & Green, 48-pin BQSOP

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free and Green
- Adding X suffix = Tape/Reel
- HDMI & Deep Color are trademarks of Silicon Image

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