PT6400 Series

3 Amp 5V Input Adjustable Integrated Switching Regulator



of product is conducting metal.

OVER TREN

SLTS032A

(Revised 6/30/2000)

allows easy integration of low-power Single-Device 5V to 3V Power C 3.3V logic IC's into existing 5V systems 85% Efficiency • without redesigning the central power • Small SIP Footprint supply. Only two external capacitors are Adjustable Output Voltage required for proper operation. The output voltage is easily adjustable with one The PT6400 is a high performance external resistor. The PT6406,7,8 can +5V to +3.3V, 3 Amp, 12-Pin SIP be used to terminate high-speed data (Single In-line Package) Integrated buses such as Futurebus (+2.1V) or the Switching Regulator (ISR) designed new GTL (+1.2V) logic buses. for stand alone (not parallelable) op-Please note that this product does FFFFFFFFFFFFFFFFFFFFFFFFFF eration. This high-performance ISR not include short circuit protection. **Pin-Out Information Ordering Information** PT Series Suffix (PT1234X) Pin Function **PT6404**□ = +1.5 Volts Case/Pin **PT6405**□ = +3.3 Volts **Configuration** 1 Do not connect **PT6406**□ = +1.8 Volts Vertical Through-Hole P 2 Vin **PT6407**□ = +2.1 Volts Horizontal Through-Hole D 3 **Standard Application** Vin **PT6408**□ = +1.2 Volts Horizontal Surface Mount Vin 4 **PT6409**□ = +2.5 Volts 5 GND PT6400 6 GND R1 7 GND 100 R2 GND 8 Pkg Style 300 COM 9 Vout PT6400 Note: Back surface

10

11

12

Vout

Vout

V_{out}Adjust

 C_1 = Required 100µF electrolytic C_2 = Required 100µF electrolytic

Specifications

Characteristics			PT6400 SERIES				
(T _a = 25°C unless noted)	Symbols	Conditions	Min	Тур	Max	Units	
Output Current	Io	$4.5V \le V_{in} \le 5.5V$	0.1*	_	3.0	А	
Current Limit	I_{cl}	$V_{in} = +5V$	_	3.6	5.0	А	
Input Voltage Range	Vin	$0.1A \le I_o \le 3.0A$	4.5	_	5.5	V	
Output Voltage Tolerance	ΔV_{o}		Vo-0.05	_	Vo+0.05	V	
Line Regulation	Reg _{line}	$4.5V \le V_{in} \le 5.5V$, $I_o = 3.0A$		±10	±25	mV	
Load Regulation	Reg _{load}	$V_{in} = +5V, 0.3 \le I_o \le 3.0A$	_	±10	±25	mV	
Vo Ripple/Noise	V_n	$V_{in} = 5V, I_o = 3.0A$	_	66	165	mV	
Transient Response with $C_2 = 100\mu F$	${f t_{tr}}{V_{os}}$	I _o step between 1.5A and 3.0A V _o over/undershoot	_	200 200	_	μSec mV	
Efficiency	η		 	85 74 77 63	 	% % %	
Switching Frequency	$f_{ m o}$	$\begin{array}{l} 4.5\mathrm{V} \leq \mathrm{V_{in}} \leq 5.5\mathrm{V} \\ 0.3\mathrm{A} \leq \mathrm{I_o} \leq 3.0\mathrm{A} \end{array}$	500	650	800	kHz	
Absolute Maximum Operating Temperature Range	Та		0	—	+85	°C	
Recommended Operating Temperature Range	Та	Free Air Convection (40-60 LFM) At Vin= 5V, Io=2.5A	0	_	+ 70**	°C	
Thermal Resistance	θ_{ja}	Free Air Convection (40-60 LFM)	_	25	_	°C/W	
Storage Temperature	Ts	—	-40	_	+125	°C	
Mechanical Shock		Per Mil-STD-883D, Method 2002.3, 1 msec, Half Sine, mounted to a fixture	_	500		G's	
Mechanical Vibration		Per Mil-STD-883D, Method 2007.2, 20-2000 Hz, Soldered in a PC board	_	15	_	G's	
Weight		_		6.5		grams	

*ISR will operate down to no load with reduced specifications

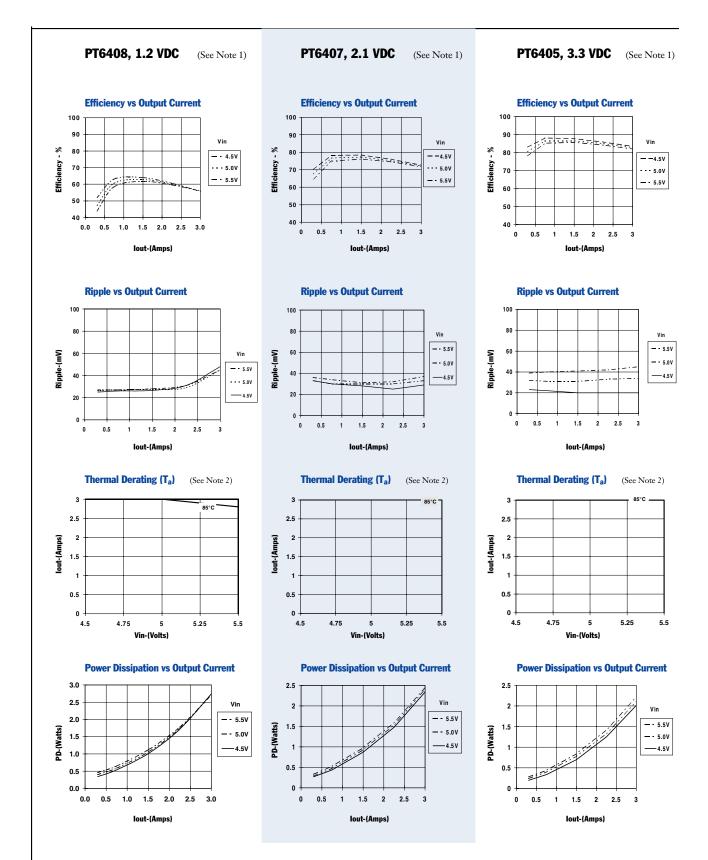
**See Thermal Derating chart.

Note: The PT6400 Series requires two 100µF electrolytic or tantalum capacitors for proper operation in all applications.



Typical Characteristics

3 Amp 5V Input Adjustable Integrated Switching Regulator



Note 1: All data listed in the above graphs, except for derating data, has been developed from actual products tested at 25°C. This data is considered typical data for the ISR. Note 2: Thermal derating graphs are developed in free air convection cooling of 40-60 LFM. (See Thermal Application Notes.)



PT6400 Series

Adjusting the Output Voltage of the PT6400 Series 3AMP 5V Bus Converters

The output voltage of the Power Trends PT6400 Series ISRs may be adjusted higher or lower than the factory trimmed pre-set voltage with the addition of a single external resistor. Table 1 accordingly gives the allowable adjustment range for each model in the series as V_a (min) and V_a (max).

Adjust Up: (See note 1) An increase in the output voltage is obtained by adding a resistor R1, between pin 12 (V_o adjust) and pins 9-11 (V_{out}).

Adjust Down: (See note 1) Add a resistor (R2), between pin 12 (V_0 adjust) and pins 5-8 (GND).

Refer to Figure 1 and Table 2 for both the placement and value of the required resistor; either R1 or (R2) as appropriate.

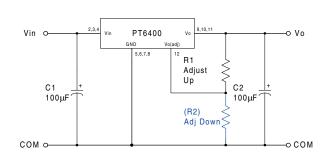
Notes:

- 1. The direction in which each resistor adjusts the output of the PT6400 series differs from many other Power Trends products. These output voltage adjustment notes are therefore specific only to the PT6400 models.
- Use only a single 1% resistor in either the R1 or (R2) location. Place the resistor as close to the ISR as possible.
- 3. Never connect capacitors from V_o adjust to either GND or V_{out}. Any capacitance added to the V_o adjust pin will affect the stability of the ISR.
- An increase in the output voltage may place additional limits on the input voltage range of the part. The revised minimum input voltage will be (V_{out} + 1.2) or 4.5V, whichever is higher. Do not exceed 5.5Vdc.

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PT6400 ADJUSTMENT RANGE							
PT6408	PT6404	PT6406	PT6407	PT6409	PT6405		
1.2	1.5	1.8	2.1	2.5	3.3		
1.1	1.3	1.5	1.8	2.1	2.8		
1.4	1.8	2.2	2.6	3.1	3.8		
	PT6408 1.2 1.1	PT6408 PT6404 1.2 1.5 1.1 1.3	PT6408 PT6404 PT6406 1.2 1.5 1.8 1.1 1.3 1.5	PT6408 PT6404 PT6406 PT6407 1.2 1.5 1.8 2.1 1.1 1.3 1.5 1.8	PT6408 PT6404 PT6406 PT6407 PT6409 1.2 1.5 1.8 2.1 2.5 1.1 1.3 1.5 1.8 2.1		





The values of R1 [adjust up], and (R2) [adjust down], can also be calculated using the following formulae.

R1 =
$$\frac{12.45 \text{ V}_{\text{o}}}{(\text{V}_{\text{a}} - \text{V}_{\text{o}})}$$
 - 49.9 k Ω

(R2) =
$$\frac{12.45 (2V_a - V_o)}{V_o - V_a}$$
 - 49.9 kΩ



PT6400 Series

Series Pt #	PT6408	PT6404	PT6406	PT6407	PT6409	PT6405
lo (nom)	1.2	1.5	1.8	2.1	2.5	3.3
/ _a (req'd)	1.2	1.5	1.0	2.1	2.5	3.3
1.1	(74.6)kΩ					
1.15	(224.0)kΩ					
1.13	(224.0)832					
1.25	249.0kΩ					
1.23	249.0kΩ 99.5kΩ	(18.6)kΩ				
1.35	49.7kΩ	(18.0)ks2 (49.7)kΩ				
1.55	24.8kΩ	(112.0)kΩ				
1.45	27.0832	(112.0)kS2 (299.0)kΩ				
1.5		(277.0)K22	(0.0)kΩ			
1.55		324.0kΩ	(0.0)ks2 (14.8)kΩ			
1.55		137.0kΩ	(17.3)kΩ			
1.65 1.7		74.6kΩ 43.5kΩ	(74.6)kΩ (149.0)kΩ			
1.75		24.8kΩ	(149.0)kΩ			
1.75		12.4kΩ	(373.0)832	(12.4)kΩ		
1.85		12.7K32	398.0kΩ	(12. 4)kS2 (29.8)kΩ		
1.85			174.0kΩ	(55.9)kΩ		
1.9						
			99.5kΩ	(99.5)kΩ		
2.0			62.2kΩ 39.7kΩ	(187.0)kΩ		
2.05				(448.0)kΩ	(2.0)1-0	
2.1			24.8kΩ 14.1kΩ	473.0kΩ	(3.0)kΩ	
2.15					(14.1)kΩ	
2.2			6.1kΩ	212.0kΩ	(29.0)kΩ	
2.25				124.0kΩ	(49.7)kΩ	
2.3				80.8kΩ	(80.8)kΩ	
2.35				54.7kΩ	(133.0)kΩ	
2.4				37.3kΩ	(236.0)kΩ	
2.45				24.8kΩ	(548.0)kΩ	
2.5				15.5kΩ	573.01.0	
2.55				8.2kΩ	573.0kΩ	
2.6				2.4kΩ	261.0kΩ	
2.65					158.0kΩ	
2.7					106.0kΩ	
2.75					74.6kΩ	(= 4)1 <
2.8					53.9kΩ	(7.4)kg
2.85					39.0kΩ	(16.5)ks
2.9					27.9kΩ	(27.9)ks
2.95					19.3kΩ	(42.6)kg
3.0					12.4kΩ	(62.2)ks
3.1					2.0kΩ	(131.0)ks
3.2						(336.0)ks
3.3						241.01.0
3.4						361.0kΩ
3.5						156.0kΩ
3.6						87.0kΩ
3.7 3.8						52.8kΩ 32.3kΩ

R1 = Black R2 = (Blue)

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PACKAGING INFORMATION

Orderable Device	Status ⁽¹⁾	Package Type	Package Drawing	Pins	Package Qty	Eco Plan ⁽²⁾	Lead/Ball Finish	MSL Peak Temp ⁽³⁾
PT6404D	NRND	SIP MOD ULE	ECA	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6404E	NRND	SIP MOD ULE	ECC	12	12	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM
PT6404P	NRND	SIP MOD ULE	ECD	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6405B	NRND	SIP MOD ULE	ECK	12	12	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM
PT6405D	NRND	SIP MOD ULE	ECA	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6405E	NRND	SIP MOD ULE	ECC	12	12	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM
PT6405ET	NRND	SIP MOD ULE	ECC	12	200	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM
PT6405P	NRND	SIP MOD ULE	ECD	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6405R	NRND	SIP MOD ULE	ECE	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6406D	NRND	SIP MOD ULE	ECA	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6406E	NRND	SIP MOD ULE	ECC	12	12	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM
PT6406P	NRND	SIP MOD ULE	ECD	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6407E	NRND	SIP MOD ULE	ECC	12	12	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM
PT6407P	NRND	SIP MOD ULE	ECD	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6409D	NRND	SIP MOD ULE	ECA	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type
PT6409E	NRND	SIP MOD ULE	ECC	12	12	Pb-Free (RoHS)	Call TI	Level-1-215C-UNLIM
PT6409P	NRND	SIP MOD ULE	ECD	12	12	Pb-Free (RoHS)	Call TI	N / A for Pkg Type

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame



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retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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