

# M5208 Evaluation Board Errata

by: Microcontroller Division

This document identifies implementation differences between the M5208EVB and the description contained in the M5208EVB User's Manual. Check <http://www.freescale.com> for the latest updates.

This errata applies to all revisions of the M5208EVB, but not the M5208EVBE.

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# 1 ASRAM

## 1.1 Description of Problem

The EVB has a footprint for one 64 Kbytes  $\times$  16 asynchronous SRAM device (IDT - IDT71T016SA12PH). This memory device (U2) may be populated for benchmarking purposes. However, the byte select connections to the SRAM are incorrect.  $\overline{BS0}$  is connected to  $\overline{BLE}$ , and  $\overline{BS1}$  is connected to  $\overline{BHE}$ . These connections should be reversed.

The incorrect byte select connections mean the byte accesses to the SRAM are to the incorrect half of the 16-bit port. Word accesses to the SRAM function correctly.

## 1.2 Solution

$\overline{BS0}$  should be connected to  $\overline{BHE}$ , and  $\overline{BS1}$  should be connected to  $\overline{BLE}$ .

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