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# H5N2503P

# Silicon N Channel MOS FET High Speed Power Switching

REJ03G1105-0200

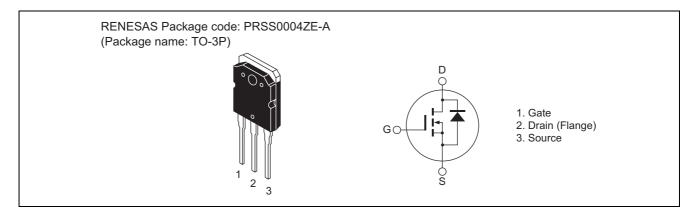
(Previous: ADE-208-1374A) Rev.2.00

Sep 07, 2005

#### **Features**

- Low on-resistance: R  $_{DS\ (on)}$  = 0.04  $\Omega$  typ.
- Low leakage current:  $I_{DSS} = 1 \mu A \text{ max (at } V_{DS} = 250 \text{ V)}$
- High speed switching:  $t_f = 190$  ns typ (at  $V_{GS} = 10$  V,  $V_{DD} = 125$  V,  $I_D = 25$  A)
- Low gate charge: Qg = 140 nC typ (at  $V_{DD} = 200 \text{ V}$ ,  $V_{GS} = 10 \text{ V}$ ,  $I_D = 50 \text{ A}$ )
- Avalanche ratings

#### **Outline**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Drain to source voltage	V <sub>DSS</sub>	250	V
Gate to source voltage	V <sub>GSS</sub>	±30	V
Drain current	I <sub>D</sub>	50	A
Drain peak current	I <sub>D (pulse)</sub> Note 1	200	A
Body-drain diode reverse drain current	I <sub>DR</sub>	50	A
Body-drain diode reverse drain peak current	I <sub>DR (pulse)</sub> Note 1	200	A
Avalanche current	I <sub>AP</sub> Note 3	50	A
Channel dissipation	Pch Note 2	150	W
Channel to case thermal Impedance	θ ch-c	0.833	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at Tc = 25°C

3. Tch  $\leq 150$ °C

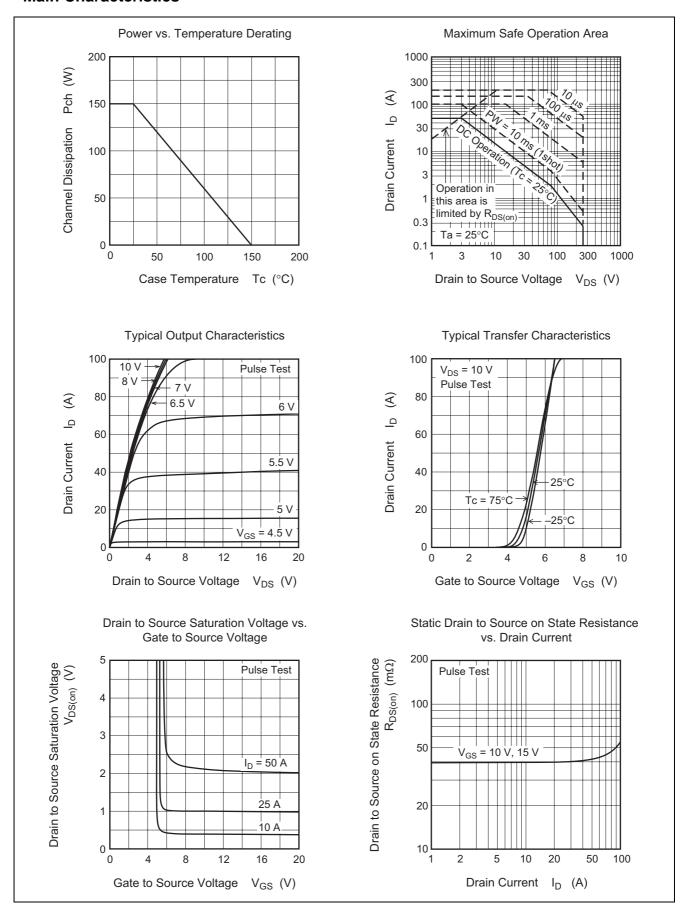
## **Electrical Characteristics**

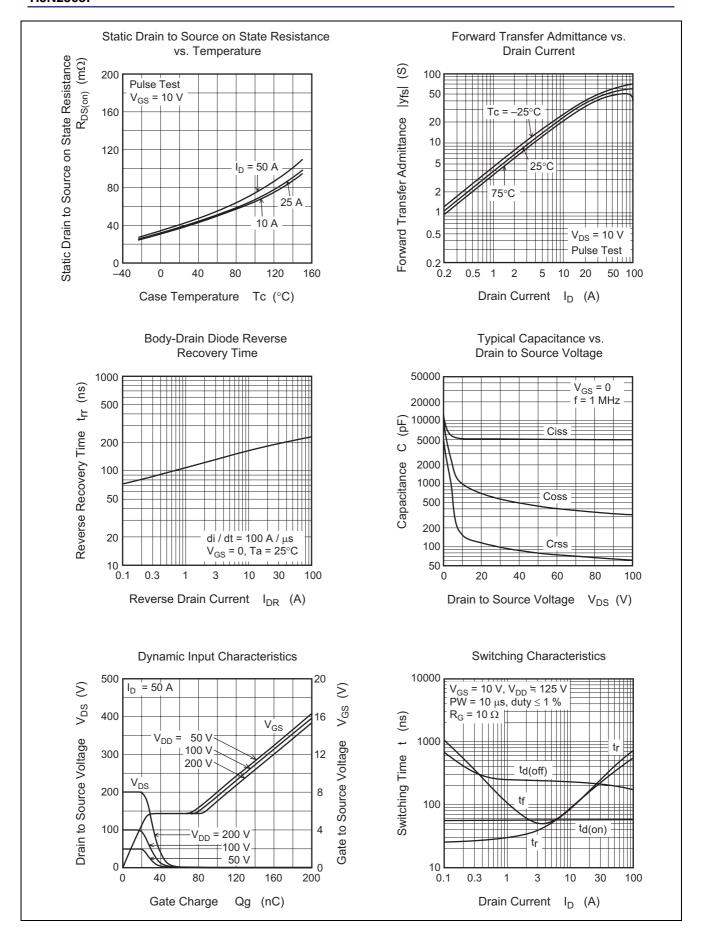
 $(Ta = 25^{\circ}C)$ 

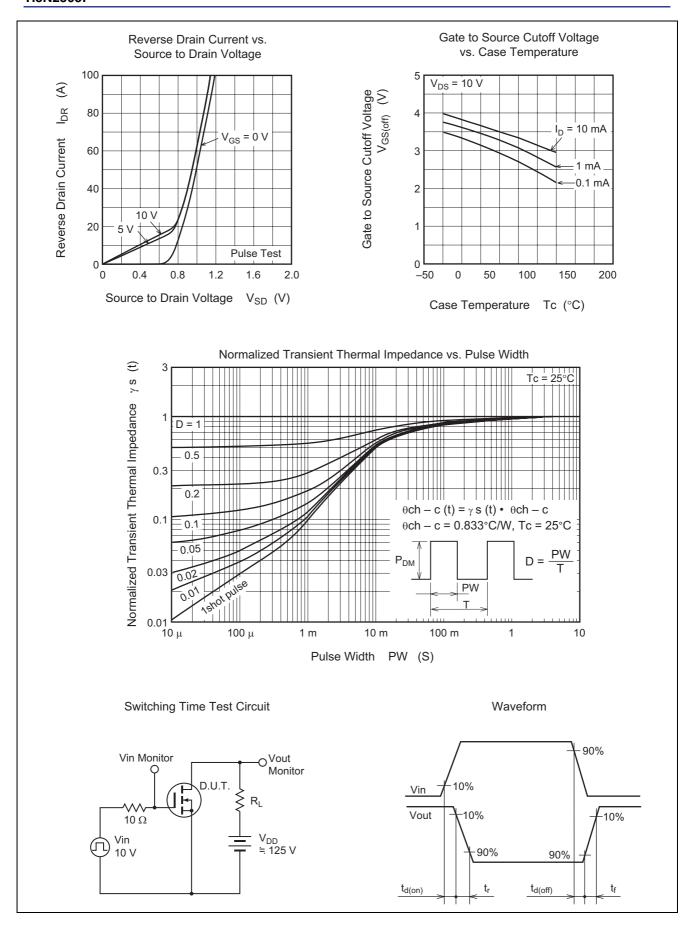
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	250	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 250 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V <sub>GS (off)</sub>	3.0	_	4.0	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state resistance	R <sub>DS (on)</sub>	_	0.040	0.055	Ω	$I_D = 25 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y <sub>fs</sub>	25	40	_	S	$I_D = 25 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	5150	_	pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	_	620	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	105	_	pF	f = 1 MHz
Turn-on delay time	t <sub>d (on)</sub>	_	58	_	ns	I <sub>D</sub> = 25 A
Rise time	t <sub>r</sub>	_	210	_	ns	V <sub>GS</sub> = 10 V
Turn-off delay time	t <sub>d (off)</sub>	_	220	_	ns	$R_L = 5 \Omega$
Fall time	t <sub>f</sub>	_	190	_	ns	$Rg = 10 \Omega$
Total gate charge	Qg	_	140	_	nC	V <sub>DD</sub> = 200 V
Gate to source charge	Qgs	_	25	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	60	_	nC	$I_D = 50 \text{ A}$
Body-drain diode forward voltage	$V_{DF}$	_	1.0	1.5	V	$I_F = 50 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	210	_	ns	$I_F = 50 \text{ A}, V_{GS} = 0$
Body-drain diode reverse recovery charge	$Q_{rr}$	_	1.8	_	μС	di <sub>F</sub> /dt = 100 A/μs

Note: 4. Pulse test

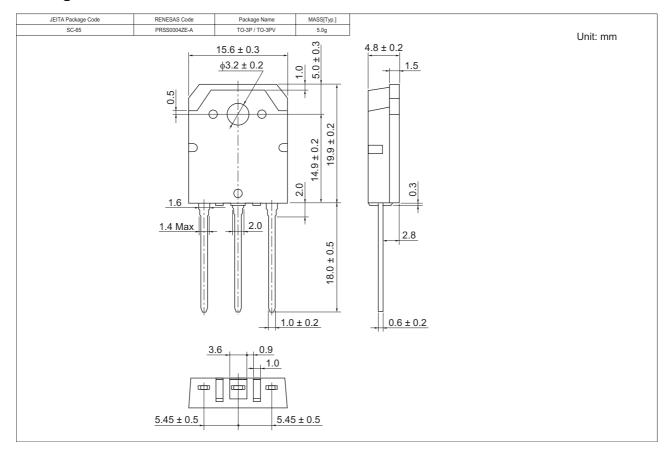
### **Main Characteristics**







## **Package Dimensions**



## **Ordering Information**

Part Name	Quantity	Shipping Container
H5N2503P-E	360 pcs	Box (Tube)

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