

DF2S6.2S

Product for Use Only as Protection against Electrostatic Discharge (ESD).

* This product is for protection against electrostatic discharge (ESD) only and is not intended for any other usage, including without limitation, the constant voltage diode application.

- 2 terminal ultra small package suitable for mounting on small space.

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Power dissipation	P*	150*	mW
Junction temperature	T _j	150	°C
Storage temperature range	T _{stg}	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

*: Mounted on a glass-epoxy circuit board of 20 × 20 mm, pad dimension of 4 × 4 mm.

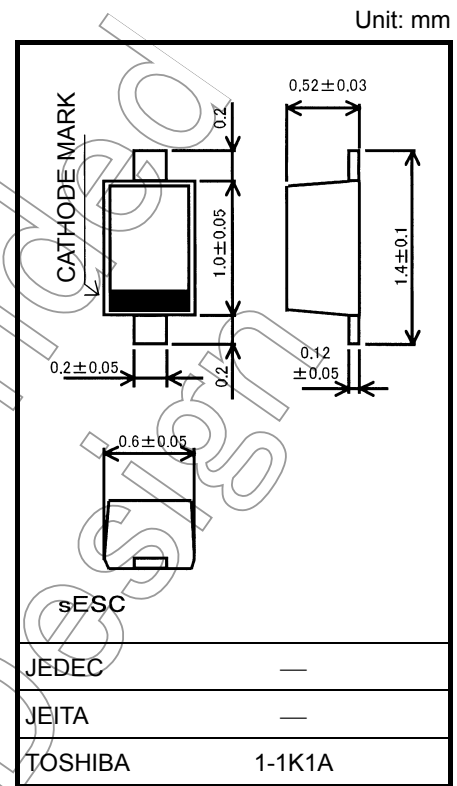
Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Typ.	Max	Unit
Zener voltage	V _Z	—	I _Z = 5 mA	5.8	6.2	6.6	V
Dynamic impedance	Z _Z	—	I _Z = 5 mA	—	—	30	Ω
Reverse current	I _R	—	V _R = 5 V	—	—	2.5	μA
Total capacitance	C _T	—	V _R = 0 V, f = 1 MHz	—	32	—	pF

Guaranteed Level of ESD Immunity

Test Condition	ESD Immunity Level
IEC61000-4-2 (contact discharge)	± 30 kV

Criterion: No damage to device elements

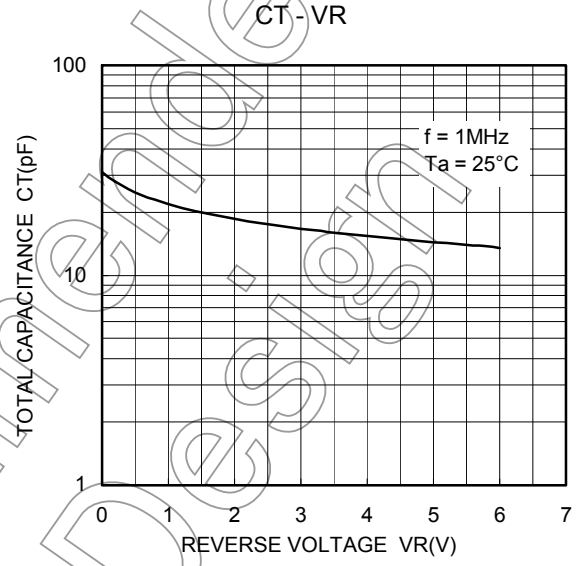
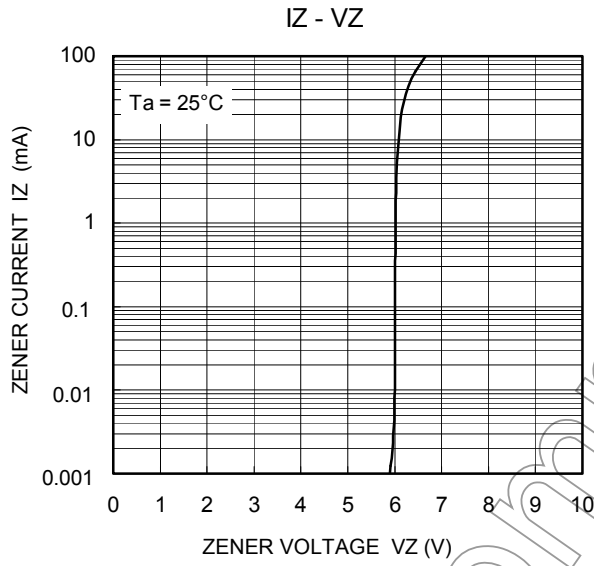
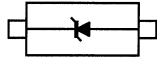
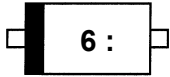


Weight: 0.0011 g (typ.)

JEDEC	—
JEITA	—
TOSHIBA	1-1K1A

Marking

Equivalent Circuit (Top View)



Not Recommended for New Design

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