TOSHIBA Field Effect Transistor Silicon N Channel Dual Gate MOS Type

# 3SK226

## TV Tuner, VHF RF Amplifier Applications **FM Tuner Applications**

- Superior cross modulation performance.
- Low reverse transfer capacitance:  $C_{rss} = 0.015 \text{ pF (typ.)}$
- Low noise figure: NF = 1.1dB (typ.)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	$V_{DS}$	13.5	V
Gate 1-source voltage	V <sub>G1S</sub>	±8	V
Gate 2-source voltage	V <sub>G2S</sub>	±8	V
Drain current	I <sub>D</sub>	30	mA
Drain power dissipation	P <sub>D</sub>	150	mW
Channel temperature	T <sub>ch</sub>	125	°C
Storage temperature range	T <sub>stg</sub>	-55~125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

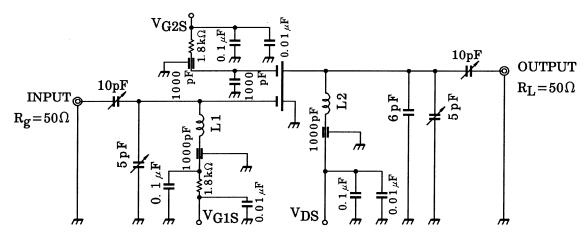
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

## Unit: mm + 0.2 2.9 – 0.3 2.9 ± 0.2 0.85 0.55 + 0.25 1.50 - 0.15 GATE 2 DRAIN SMQ 4. SOURCE JEDEC JEITA **TOSHIBA** 2-3J1A

Weight: 0.013 g (typ.)

### **Electrical Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate 1 leakage current	I <sub>G1SS</sub>	$V_{DS} = 0$ , $V_{G1S} = \pm 6$ V, $V_{G2S} = 0$	_	_	±50	nA
Gate 2 leakage current	I <sub>G2SS</sub>	$V_{DS} = 0$ , $V_{G1S} = 0$ , $V_{G2S} = \pm 6 \text{ V}$	_	_	±50	nA
Drain-source voltage	V <sub>(BR)DSX</sub>	$V_{G1S} = -4 \text{ V}, V_{G2S} = -4 \text{ V}, I_D = 100  \mu\text{A}$	13.5	_	_	V
Drain current	I <sub>DSS</sub>	$V_{DS} = 6 \text{ V}, V_{G1S} = 0, V_{G2S} = 4.5 \text{ V}$	0	_	0.1	mA
Gate 1-source cut-off voltage	V <sub>G1S</sub> (OFF)	$V_{DS} = 6 \text{ V}, V_{G2S} = 4.5 \text{ V}, I_D = 100 \mu A$	0	_	1.0	V
Gate 2-source cut-off voltage	V <sub>G2S</sub> (OFF)	$V_{DS} = 6 \text{ V}, V_{G1S} = 4 \text{ V}, I_D = 100 \mu A$	0.5	1.0	1.5	V
Forward transfer admittance	Y <sub>fs</sub>	$\begin{split} V_{DS} = 6 \text{ V}, & V_{G2S} = 4.5 \text{ V}, & I_D = 10 \text{ mA}, \\ f = 1 \text{ kHz} \end{split}$	_	13	_	mS
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 6 V, V <sub>G2S</sub> = 4.5 V, I <sub>D</sub> = 10 mA,	2.1	2.7	3.3	pF
Reverse transfer capacitance	C <sub>rss</sub>	f = 1 MHz	_	0.015	0.03	pF
Power gain	G <sub>ps</sub>	V <sub>DS</sub> = 6 V, V <sub>G2S</sub> = 4.5 V, I <sub>D</sub> = 10 mA,	23	27	_	dB
Noise figure	NF	f = 200 MHz (Figure 1)	_	1.1	2.2	dB

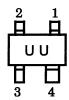


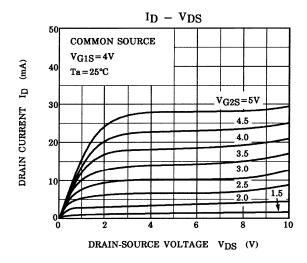
L1: 1 mm $\phi$  Ag plated copper wire, 2 turns, 8 mm ID

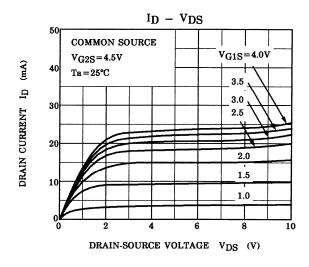
L2: 1 mm Ag plated copper wire, 2.5 turns, 8 mm ID

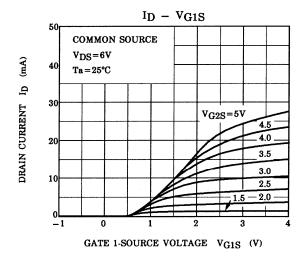
Figure 1 200 MHz, G<sub>ps</sub> NF Test Circuit

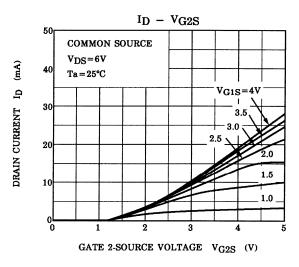
### Marking

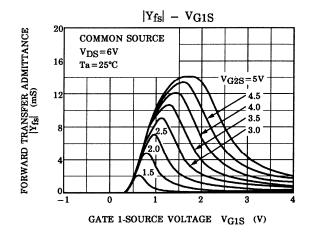


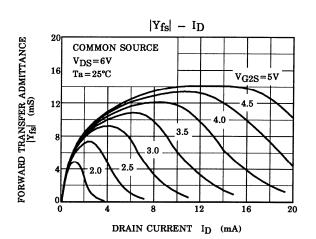




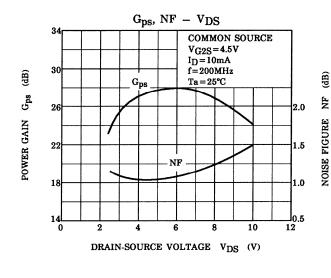


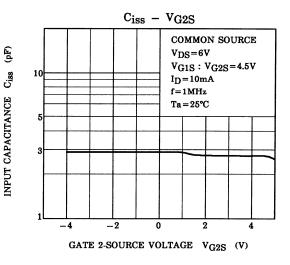


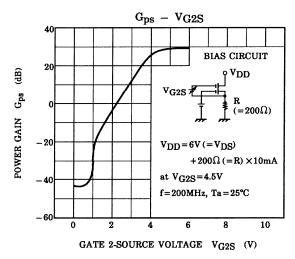


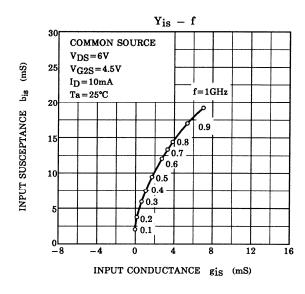


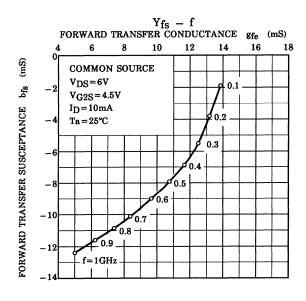
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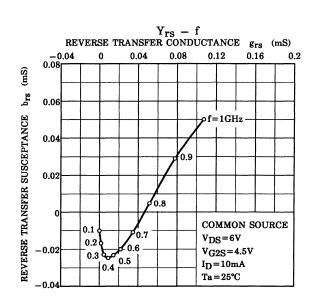


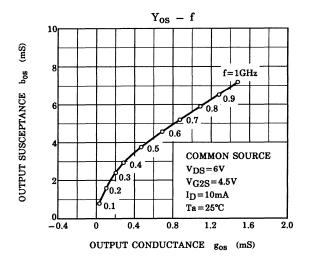


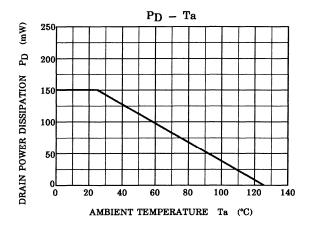












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