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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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Phase-out/Discontinued THYRISTORS 03P4MG,03P6MG

300 mA HIGH-WITHSTANDING-VOLTAGE MOLD SCR

DESCRIPTION

The 03P4MG and 03P6MG are P-gate fully diffused mold SCRs with an average on-state current of 300 mA. The repeat peak off-state voltages (and reverse voltages) are 400 and 600 V.

FEATURES

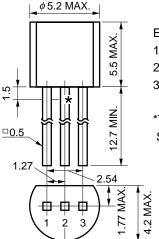
- 400 and 600 V high-withstanding-voltage series of products
- The non-repetitive withstanding voltage is a high 700 V, making it easy to harmonize the rise voltage of the surge absorber.
- High-sensitivity thyristor (I_{GT} = 3 to 50 μ A)
- Employs flame-retardant epoxy resin (UL94V-0)

APPLICATIONS

★

Leakage breakers, SSRs, various type of alarms, consumer electronic equipments and automobile electronic components

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)



Electrode connection

1: Gate

PACKAGE DRAWING (Unit: mm)

2: Anode

3: Cathode

*Tc test bench-mark Standard weight: 0.3 g

Parameter	Symbol	Ratings			Remarks
		03P4MG	03P6MG		
Non-repetitive Peak Reverse Voltage	Vrsm	700	700	V	R _G κ = 1 kΩ
Non-repetitive Peak Off-state Voltage	Vdsm	700	700	V	Rgκ = 1 kΩ
Repetitive Peak Reverse Voltage	Vrrm	400	600	V	Rgκ = 1 kΩ
Repetitive Peak Off-state Voltage	Vdrm	400	600	V	Rgκ = 1 kΩ
Average On-state Current	It(AV)	300 (T _A = 30°C, Single half-wave, θ = 180°)			Refer to Figure 10.
Effective On-state Current	IT(RMS)	470			-
Surge On-state Current	Ітѕм	8 (f = 50 Hz, Sine half-wave, 1 cycle)			Refer to Figure 2.
Fusing Current	∫ i⊤²dt	$0.15 (1 \text{ ms} \le t \le 10 \text{ ms})$			-
Critical Rate of On-state Current of Rise	dl⊤/dt	20			-
Peak Gate Power Dissipation	Рсм	100 (f ≥ 50 Hz, Duty ≤ 10%)			Refer to Figure 3.
Average Gate Power Dissipation	PG(AV)	10			Refer to Figure 3.
Peak Gate Forward Current	IFGM	100 (f ≥ 50 Hz, Duty ≤ 10%)			-
Peak Gate Reverse Voltage	Vrgm	6			_
Junction Temperature	Tj	-40 to +125			_
Storage Temperature	Tstg	-55 to +150			_

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Parameter	Symbol	Conditions		Specifications			Unit	Remarks
				MIN.	TYP.	MAX.		
Non-repetitive Peak Reverse	IRRM	VRM = VRRM T _j = 25°C		-	-	10	μA	_
Current			T _j = 125°C	-	-	100	μA	-
Non-repetitive Peak Off-state	DRM	Vdm = Vdrm	$T_j = 25^{\circ}C$	-	-	10	μA	-
Current			T _j = 125°C	-	-	100	μA	_
Critical Rate-of-rise of Off-state	dV⊳/dt	$T_j = 125^{\circ}C, V_{DM} =$	10	_	_	V/µs	_	
Voltage			5					
On-state Voltage	Vτ	I⊤ = 4 A	-	-	2.2	V	Refer to Figure 1.	
Gate Trigger Current	Іст	$V_{DM} = 6 V, R_{L} = 10$	3	-	50	μA	-	
Gate Trigger Voltage	Vgt	VDM = 6 V, RL = 10	-	-	0.8	V	_	
Gate Non-trigger Voltage	Vgd	$T_{j} = 125^{\circ}C, \ V_{DM} = \ \frac{V_{DRM}}{2}$		0.2	-	-	V	-
Holding Current	Η	Vdm = 24 V, Iтм = 4	_	_	5	mA	_	
Turn-off Time	tq	$T_j = 125^{\circ}C, \ I_T = 20^{\circ}$	-	60	_	μs	_	
		dlr/dt = 15 A/µs, Vr ≥ 25 V,						
		$V_{DM} = \frac{2}{3} V_{DRM}, dV_D/dt = 10 V/\mu s$						
Thermal Resistance	Rth(j-C)	Junction-to-case DC		_		50	°C/W	Refer to Figure 14.
	Rth(j-A)	Junction-to-ambier	-	_	230	°C/W	Refer to Figure 14.	

ELECTRICAL CHARACTERISTICS (T_j = 25°C, R_{GK} = 1 k Ω)

TYPICAL CHARACTERISTICS (T_A = 25°C)

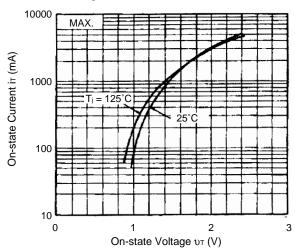


Figure 1. it vs. ut Characteristics

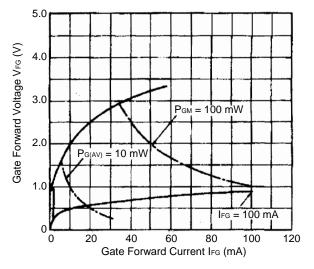
At initial, T_j = 125°C 14 Ітям [Surge On-state Current ITSM (A) 12 10 ms 20 ms 10 8 6 4 2 0 5 10 Cycles (N) 50 100 1

Figure 2. ITSM Rating

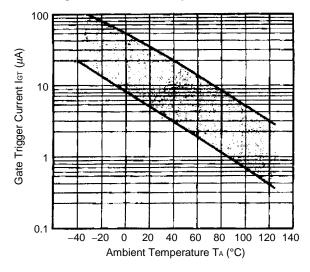
03P4MG,03P6MG



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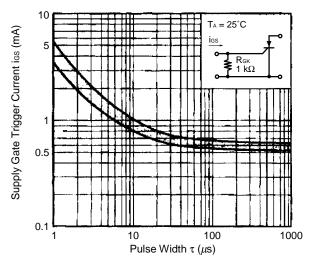


Figure 4. Example of Gate Characteristics

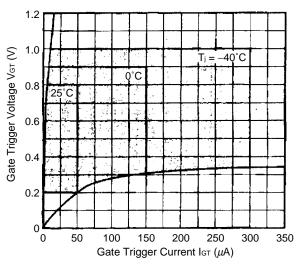
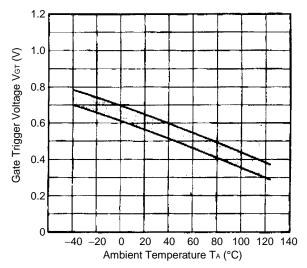
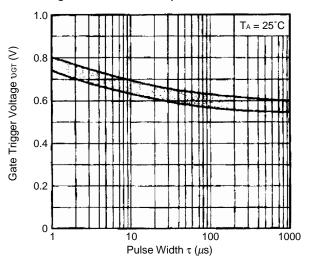


Figure 6. VGT vs. TA Example of Characteristics







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Figure 9. PT(AV) vs. IT(AV) Characteristics

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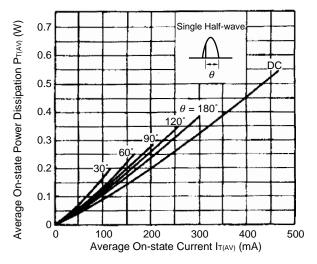
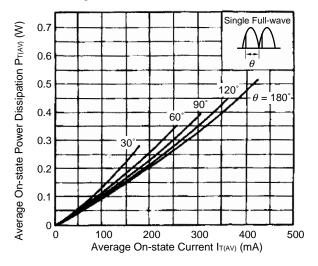
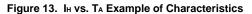


Figure 11. PT(AV) vs. IT(AV) Characteristics





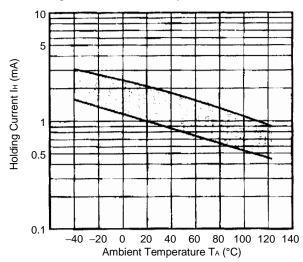
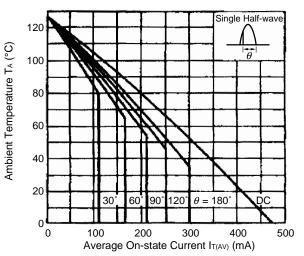


Figure 10. TA vs. IT(AV) Characteristics





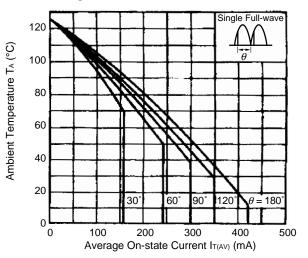
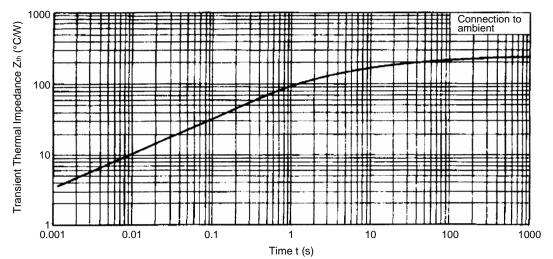


Figure 14. Zth Characteristics

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