

Multilayer Transient Voltage Surge Suppressors

ML Varistor Series

The ML Series family of Transient Voltage Surge Suppression devices is based on the Littelfuse Multilayer fabrication technology. These components are designed to suppress a variety of transient events, including those specified in IEC 61000-4-2 or other standards used for Electromagnetic Compliance (EMC). The ML Series is typically applied to protect integrated circuits and other components at the circuit board level.

The wide operating voltage and energy range make the ML Series suitable for numerous applications on power supply, control and signal lines.

The ML Series is manufactured from semiconducting ceramics, and is supplied in a leadless, surface mount package. The ML Series is compatible with modern reflow and wave soldering procedures.

It can operate over a wider temperature range than zener diodes, and has a much smaller footprint than plastic-housed components.

Littelfuse Inc. manufactures other Multilayer Series products. See the MLE Series data sheet for ESD applications, MHS Series data sheet for high-speed ESD applications, the MLN for multiline protection and the AUML Series for automotive applications.

Features

- Leadless 0402, 0603, 0805, 1206 and 1210 Chip Sizes
- Multilayer Ceramic Construction Technology
- --55°C to +125°C Operating Temperature Range
- Operating Voltage Range V_{M(DC)} = 5.5V to 120V
- Rated for Surge Current (8 x 20µs)
- Rated for Energy (10 x 1000µs)
- · Inherent Bi-directional Clamping
- No Plastic or Epoxy Packaging Assures Better than 94V-0 Flammability Rating
- Standard Low Capacitance Types Available
- 100% Lead-Free

Applications

- Suppression of Inductive Switching or Other Transient Events Such as EFT and Surge Voltage at the Circuit Board Level
- ESD Protection for Components Sensitive to IEC 61000-4-2, MIL-STD-883C Method 3015.7, and Other Industry Specifications (See Also the MLE or MLN Series)
- · Provides On-Board Transient Voltage Protection for ICs and Transistors
- · Used to Help Achieve Electromagnetic Compliance of End Products
- Replace Larger Surface Mount TVS Zeners in Many Applications



Size

Metric	EIA			
1005	0402			
1608	0603			
2012	0805			
3216	1206			
3225	1210			
4532	1812			
5650	2220			



Multilayer Transient Voltage Surge Suppressors

ML Varistor Series

Absolute Maximum Ratings For ratings of individual members of a series, see Device Ratings and Specifications table.

Continuous:	ML SERIES	UNITS
Steady State Applied Voltage:		UNITO
DC Voltage Range (V _{M/DC}))	3.5 to 120	V
DC Voltage Range (V _{M(DC)})	2.5 to 107	V
Transient:		
Non-Repetitive Surge Current, 8/20µs Waveform, (I _{TM})	4 to 500	А
Non-Repetitive Surge Energy, 10/1000µs Waveform, (W _{TM})	0.02 to 1.2	J
Operating Ambient Temperature Range (T _A)	55 to + 125	°C
Storage Temperature Range (T _{STG})		°C
Temperature Coefficient (V) of Clamping Voltage (V _C) at Specified Test Current		%/°C

Device Ratings and Specifications

				MAXIMUM RA	ATINGS (125°C)		SF	PECIFICATION	NS (25 ^o C)
		CONTI	IMUM NUOUS KING 'AGE	MAXIMUM NON- REPETITIVE SURGE CURRENT (8/20µs)	MAXIMUM NON- REPETITIVE SURGE ENERGY (10/1000µs)	MAXIMUM CLAMPING VOLTAGE AT 10A (OR AS NOTED) (8/20µs)	AT 1mA	VOLTAGE DC TEST RENT	TYPICAL CAPACITANCE AT f = 1MHz
	PART	V _{M(DC)}	V _{M(AC)}	Ітм	w _{TM}	٧ _C	V _{N(DC)} MIN	V _{N(DC)} MAX	с
	NUMBER	(V)	(V)	(A)	(J)	(V)	(V)	(V)	(pF)
	V3.5MLA0603	3.5	2.5	30	0.1	10 at 2A	3.7	7.0	1100
	V3.5MLA0805	3.5	2.5	120	0.3	10 at 5A	3.7	7.0	2200
	V3.5MLA0805L	3.5	2.5	40	0.1	10 at 2A	3.7	7.0	1200
	V3.5MLA1206	3.5	2.5	100	0.3	14	3.7	7.0	6000
NEW	V5.5MLA0402	5.5	4.0	20	0.050	19 at 1A	7.1	10.8	220
NEW	V5.5MLA0402L	5.5	4.0	20	0.050	38 at 1A	15.9	21.5	70
	V5.5MLA0603	5.5	4.0	30	0.1	19 at 2A	7.1	9.3	660
	V5.5MLA0805	5.5	4.0	120	0.3	15.5 at 5A	7.1	9.3	1600
	V5.5MLA0805L	5.5	4.0	40	0.1	15.5 at 2A	7.1	9.3	860
	V5.5MLA1206	5.5	4.0	150	0.4	15.5	7.1	9.3	4500
NEW	V9MLA0402	9	6.5	20	0.050	30 at 1A	11	16	120
NEW	V9MLA0402L	9	6.5	4	0.020	35 at 1A	11	16	33
	V9MLA0603	9.0	6.5	30	0.1	30 at 2A	11.0	16	420
	V9MLA0805L	9.0	6.5	40	0.1	20 at 2A	11	14	450
	V12MLA0805L	12	9.0	40	0.1	38 at 2A	14	18.5	350
NEW	V14MLA0402	14	10	20	0.050	38 at 1A	15.9	21.5	70
	V14MLA0603	14	10	30	0.1	38 at 2A	15.9	21.5	150
	V14MLA0805	14	10	120	0.3	30 at 5A	15.9	20.3	480
	V14MLA0805L	14	10	40	0.1	30 at 2A	15.9	20.3	270
	V14MLA1206	14	10	150	0.4	30	15.9	20.3	1600



Multilayer Transient Voltage Surge Suppressors

ML Varistor Series

Device Ratings and Specifications (Continued)

			MAXIMUM RA	ATINGS (125°C)		SPECIFICATIONS (25°C)			
	CONTI	IMUM NUOUS KING TAGE	MAXIMUM NON- REPETITIVE SURGE CURRENT (8/20µs)	MAXIMUM NON- REPETITIVE SURGE ENERGY (10/1000µs)	MAXIMUM CLAMPING VOLTAGE AT 10A (OR AS NOTED) (8/20µs)	NOMINAL AT 1mA I CURI		TYPICAL CAPACITANCI AT f = 1MHz	
DADT	V _{M(DC)}	V _{M(AC)}	I _{TM}	w _{TM}	v _c	V _{N(DC)} MIN	V _{N(DC)} MAX	с	
PART NUMBER	(V)	(V)	(A)	(J)	(V)	(V)	(V)	(pF)	
V18MLA0402	18	14	20	0.050	50 at 1A	22	28.0	40	
V18MLA0603	18	14	30	0.1	50 at 2A	22	28.0	100	
V18MLA0805	18	14	120	0.3	40 at 5A	22	28.0	450	
V18MLA0805L	18	14	40	0.1	40 at 2A	22	28.0	250	
V18MLA1206	18	14	150	0.4	40	22	28.0	1100	
V18MLA1210	18	14	500	2.5	40	22	28.0	1250	
V26MLA0603	26	20	30	0.1	58 at 2A	31	38	90	
V26MLA0805	26	20	100	0.3	58 at 5A	29.5	38.5	190	
V26MLA0805L	26	20	40	0.1	58 at 2A	29.5	38.5	115	
V26MLA1206	26	20	150	0.6	56	29.5	38.5	900	
V26MLA1210	26	20	300	1.2	54	29.5	38.5	1000	
V30MLA0603	30	25	30	0.1	65 at 2A	37	46	75	
V30MLA0805L	30	25	30	0.1	65 at 2A	37	46	80	
V30MLA1210	30	25	280	1.2	62	35	43	1575	
V30MLA1210L	30	25	220	0.9	62	35	43	1530	
V33MLA1206	33	26	180	0.8	72	38	49	550	
V42MLA1206	42	30	180	0.8	86	46	60	550	
V48MLA1210	48	40	250	1.2	100	54.5	66.5	450	
V48MLA1210L	48	40	220	0.9	100	54.5	66.5	430	
V56MLA1206	56	40	180	1.0	110	61	77	150	
V60MLA1210	60	50	250	1.5	120	67	83	375	
V68MLA1206	68	50	180	1.0	130	76	90	150	
V85MLA1210	85	67	250	2.5	160	95	115	225	
V120MLA1210	120	107	125	2.0	230	135	165	65	

NOTES:

1. L suffix is a low capacitance and energy version; Contact your Littelfuse Sales Representative for custom capacitance requirements.

2. Typical leakage at 25°C<25μA, maximum leakage 50μA at V_{M(DC)}; for 0402 size, typical leakage <5μA, maximum leakage <20μA at V_{M(DC)}.

3. Average power dissipation of transients for 0402, 0603, 0805, 1206 and 1210 sizes not to exceed 0.03W, 0.05W, 0.1W, 0.1W and 0.15W respectively.

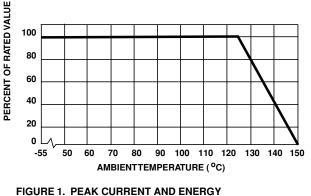


Multilayer Transient Voltage Surge Suppressors

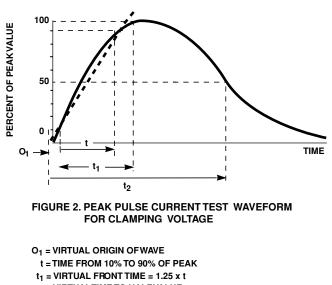
ML Varistor Series

Temperature De-rating

When transients occur in rapid succession, the average power dissipation is the energy (watt-seconds) per pulse times the number of pulses per second. The power so developed must be within the specifications shown on the Device Ratings and Specifications table for the specific device. For applications exceeding 125°C ambient temperature, the peak surge current and energy ratings must be derated as shown in Figure 1.



DERATING CURVE



t₂ = VIRTUALTIME TO HALF VALUE (IMPULSE DURATION)

EXAMPLE: FOR AN 8/20 μs CURRENT WAVEFORM 8 μs = t1 = VIRTUAL FRONT TIME 20 μs = t2 = VIRTUAL TIME TO HALFVALUE

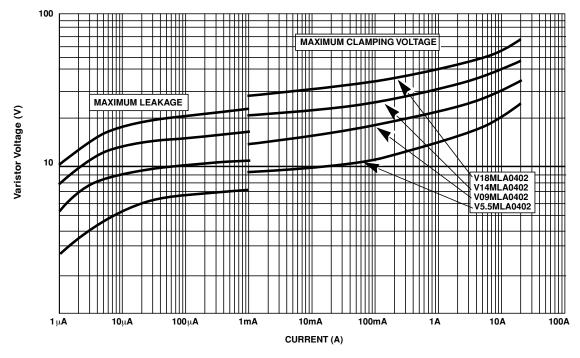


FIGURE 3. LIMIT V-I CHARACTERISTIC FOR V5.5MLA0402 TO V18MLA0402

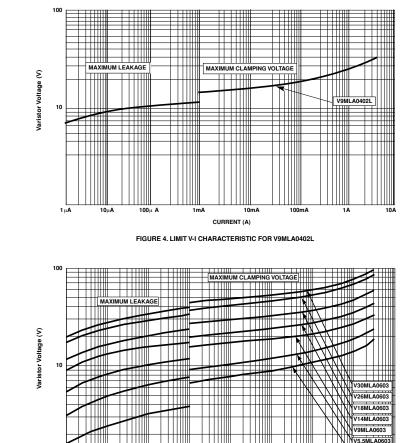


SURFACE MOUNT VARISTORS

Surface Mount Varistors

Multilayer Transient Voltage Surge Suppressors

ML Varistor Series



Maximum Transient V-I Characteristic Curves

FIGURE 5. LIMIT V-I CHARACTERISTIC FOR V3.5MLA0603 TO V30MLA0603

10mA

CURRENT (A)

100mA

1A

100μA

1μ**Α**

10μA

1mA

V3.5MLA0603

1004

10A

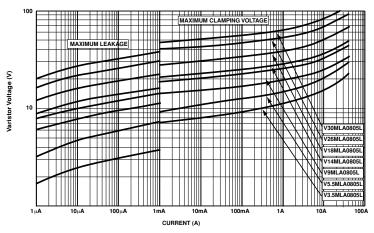
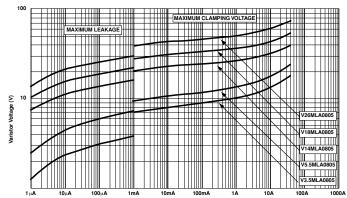


FIGURE 6. LIMIT V-I CHARACTERISTIC FOR V3.5MLA0805L TO V30MLA0805L



Multilayer Transient Voltage Surge Suppressors

ML Varistor Series



Maximum Transient V-I Characteristic Curves (Continued)

CURRENT (A)

FIGURE 7. LIMIT V-I CHARACTERISTIC FOR V3.5MLA0805 TO V26MLA0805

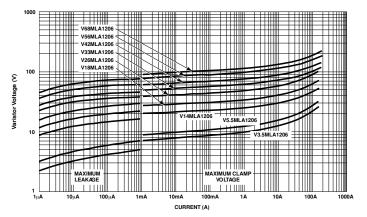


FIGURE 8. LIMIT V-I CHARACTERISTIC FOR V3.5MLA1206 TO V68MLA1206

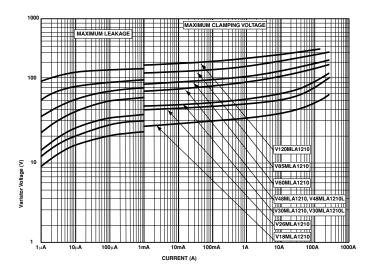


FIGURE 9. LIMIT V-I CHARACTERISTIC FOR V18MLA1210 TO V120MLA1210

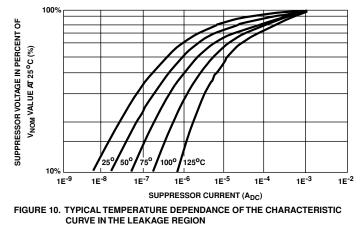


Multilayer Transient Voltage Surge Suppressors

ML Varistor Series

Device Characteristics

At low current levels, the V-I curve of the multilayer transient voltage suppressor approaches a linear (ohmic) relationship and shows a temperature dependent effect (Figure 10). At or below the maximum working voltage, the suppressor is in a high resistance mode (approaching 10⁶ Ω at its maximum rated working voltage). Leakage currents at maximum rated voltage are below 50µA, typically 25µA; for 0402 size below 10µA, typically 5µA.



Speed of Response

The Multilayer Suppressor is a leadless device. Its response time is not limited by the parasitic lead inductances found in other surface mount packages. The response time of the Zinc Oxide dielectric material is less than 1 nanosecond and the ML can clamp very fast dV/dT events such as ESD. Additionally, in "real world" applications, the associated circuit wiring is often the greatest factor effecting speed of response. Therefore, transient suppressor placement within a circuit can be considered important in certain instances.

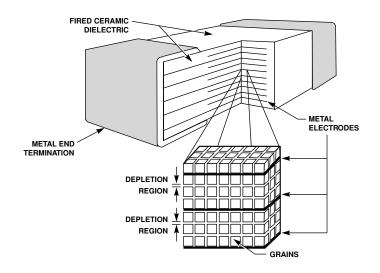
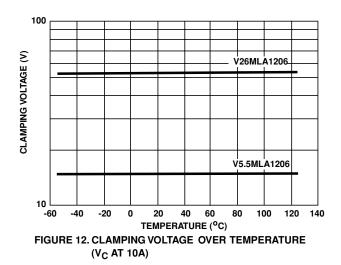


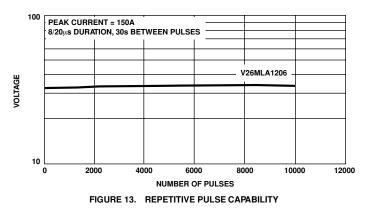
FIGURE 11. MULTILAYER INTERNAL CONSTRUCTION



Energy Absorption/Peak Current Capability

Energy dissipated within the ML is calculated by multiplying the clamping voltage, transient current and transient duration. An important advantage of the multilayer is its interdigitated electrode construction within the mass of dielectric material. This results in excellent current distribution and the peak temperature per energy absorbed is very low. The matrix of semiconducting grains combine to absorb and distribute transient energy (heat) (Figure 11). This dramatically reduces peak temperature; thermal stresses and enhances device reliability.

As a measure of the device capability in energy and peak current handling, the V26MLA1206A part was tested with multiple pulses at its peak current rating (150A, 8/20µs). At the end of the test, 10,000 pulses later, the device voltage characteristics are still well within specification (Figure 13).





Multilayer Transient Voltage Surge Suppressors

ML Varistor Series

Soldering Recommendations

The principal techniques used for the soldering of components in surface mount technology are Infrared (IR) re-flow, vapour phase re-flow and wave soldering. Typical profiles are shown in Figures 14, 15 and 16. When wave soldering, the ML suppressor is attached to the circuit board by means of an adhesive. The assembly is then placed on a conveyor and run through the soldering process to contact the wave. With IR and vapour phase reflow; the device is placed in a solder paste on the substrate. As the solder paste is heated, it re-flows and solders the unit to the board.

The recommended solder for the ML suppressor is a 62/36/2 (Sn/Pb/Ag), 60/40 (Sn/Pb) or 63/37 (Sn/Pb). Littelfuse also recommends an RMA solder flux.

Wave soldering is the most strenuous of the processes. To avoid the possibility of generating stresses due to thermal shock, a preheat stage in the soldering process is recommended, and the peak temperature of the solder process should be rigidly controlled.

When using a reflow process, care should be taken to ensure that the ML chip is not subjected to a thermal gradient steeper than 4 degrees per second; the ideal gradient being 2 degrees per second. During the soldering process, preheating to within 100 degrees of the solder's peak temperature is essential to minimize thermal shock. Examples of the soldering conditions for the ML suppressor are given in the tables below.

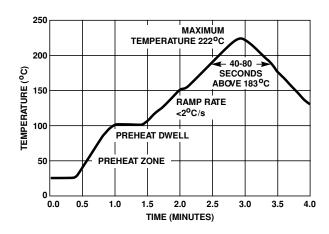
Once the soldering process has been completed, it is still necessary to ensure that any further thermal shocks are avoided. One possible cause of thermal shock is hot printed circuit boards being removed from the solder process and subjected to cleaning solvents at room temperature. The boards must be allowed to cool gradually to less than 50°C before cleaning.

Termination Options

Littelfuse offers three types of electrode termination finish for the Multilayer product series:

- 1. Silver/Platinum
- 2. Silver/Palladium
- 3. Nickel Barrier (Tin-on-Nickel-on-Silver, available for 0402-1210 package size)

(The ordering information section describes how to designate them.)





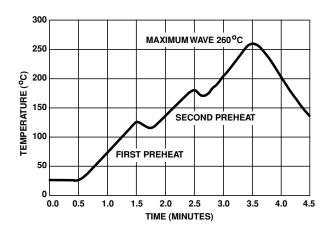


FIGURE 15. WAVE SOLDER PROFILE

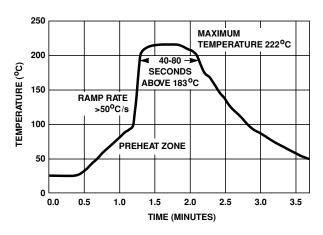


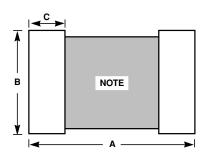
FIGURE 16. VAPOR PHASE SOLDER PROFILE



Multilayer Transient Voltage Surge Suppressors

ML Varistor Series

Recommended Pad Outline



NOTE: Avoid metal runs in this area.

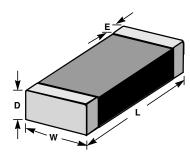
TABLE 1: PAD LAYOUT DIMENSIONS

	PAD SIZE									
	1210 SIZE DEVICE				0805 SIZE DEVICE		0603 SIZE DEVICE		0402 SIZE DEVICE	
DIMENSION	IN	ММ	IN	ММ	IN	ММ	IN	ММ	IN	ММ
A	0.160	4.06	0.160	4.06	0.120	3.05	0.100	2.54	0.067	1.70
В	0.100	2.54	0.065	1.65	0.050	1.27	0.030	0.76	0.020	0.51
С	0.040	1.02	0.040	1.02	0.040	1.02	0.035	0.89	0.024	0.61



ML Varistor Series

Mechanical Dimensions



	CHIP SIZE											
	1210		1210		1210 1206		0805		0603		0402	
DIMENSION	IN	ММ	IN	ММ	IN	ММ	IN	ММ	IN	ММ		
D Max.	0.113	2.87	0.071	1.80	0.043	1.10	0.035	0.90	0.024	0.90		
E	0.02 ±0.01	0.50 ±0.25	0.02 ±0.01	0.50 ±0.25	0.01 to 0.029	0.50 to 0.25	0.015 ±0.008	0.4 ±0.2	0.010 ±0.006	0.25 ±0.15		
L	0.125 ±0.012	3.20 ±0.30	0.125 ±0.012	3.20 ±0.30	0.079 ±0.008	2.01 ±0.20	0.063 ±0.006	1.6 ±0.15	0.039 ±0.004	1.0 ±0.1		
W	0.10 ±0.012	2.54 ±0.30	0.06 ±0.011	1.60 ±0.28	0.049 ±0.008	1.25 ±0.20	0.032 ±0.06	0.8 ±0.15	0.020 ±0.004	0.5 ±0.1		

Ordering Information

VXXML TYPES 18 MLA 1206 Х v Х Х DEVICE FAMILY PACKING OPTIONS Littelfuse TVSS Device T: 13in (330mm) Diameter Reel H: 7in (178mm) Diameter Reel (Note) MAXIMUM DC . A: Bulk Pack WORKING VOLTAGE END TERMINATION OPTION MULTILAYER SERIES No Letter: Ag/Pt DESIGNATOR W: Ag/Pd N: Nickel Barrier (Tin-on-Nickel-on-Silver, 0402 -1210 only) CAPACITANCE OPTION No Letter: Standard DEVICE SIZE: i.e 120 mil x 60 mil L: Low Capacitance Version (3MM X 1.5MM)

Standard Shipping Quantities

DEVICE SIZE	"13" INCH REEL ("T"OPTION)	"7"INCH REEL ("H"OPTION)	BULK PACK ("A"OPTION)
1210	8,000	2,000	2500
1206	10,000	2,500	2500
0805	10,000	2,500	2500
0603	10,000	2,500	2500
0402	N/A	10,000	N/A



Multilayer Transient Voltage Surge Suppressors

ML Varistor Series

Tape and Reel Specifications

Conforms to EIA - 481-1, Revision A

Can be supplied to IEC Publication 286 - 3

SYMBOL	DESCRIPTION	DIMENSIONS IN MILLIMETERS				
		0402 Size	0603, 0805, 1206 & 1210 Sizes			
A ₀	Width of Cavity	Dependent on Chip Size to Minimize Rotation				
B ₀	Length of Cavity	Dependent on Chip Size to Minimize Rotatio				
K ₀	Depth of Cavity	Dependent on Chip Size to Minimize Rotation.				
W	Width of Tape	8 ±0.2				
F	Distance Between Drive Hole Centers and Cavity Centers	3.5 ±0.05				
Е	Distance Between Drive Hole Centers and Tape Edge	1.75 ±0.1				
P ₁	Distance Between Cavity Centers	2±0.05 4 ±0.1				
P ₂	Axial Drive Distance Between Drive Hole Centers & Cavity Centers	2 ±0.1				
P ₀	Axial Drive Distance Between Drive Hole Centers	4 ±0.1				
D ₀	Drive Hole Diameter	1.55 ±0.05				
D ₁	Diameter of Cavity Piercing	N/A 1.05 ±0.05				
	Top Tape Thickness		0.1 Max			

