



NPN SILICON HIGH FREQUENCY TRANSISTOR

UPA811T

FEATURES

- **SMALL PACKAGE STYLE:**
2 NE680 Die in a 2 mm x 1.25 mm package
- **LOW NOISE FIGURE:**
NF = 1.9 dB TYP at 2 GHz
- **HIGH GAIN:**
 $IS_{21EI}^2 = 7.5$ dB TYP at 2 GHz
- **EXCELLENT LOW VOLTAGE, LOW CURRENT PERFORMANCE**

DESCRIPTION

NEC's UPA811T is two NPN high frequency silicon epitaxial transistors encapsulated in an ultra small 6 pin SMT package. Each transistor is independently mounted and easily configured for either dual transistor or cascode operation. The high ft, low voltage bias and small size make this device ideally suited for pager and other hand-held wireless applications.

ABSOLUTE MAXIMUM RATINGS¹ (T_A = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
V _{CB0}	Collector to Base Voltage	V	20
V _{CEO}	Collector to Emitter Voltage	V	10
V _{EBO}	Emitter to Base Voltage	V	1.5
I _C	Collector Current	mA	35
P _T	Total Power Dissipation		
	1 Die	mW	110
	2 Die	mW	200
T _J	Junction Temperature	°C	150
T _{STG}	Storage Temperature	°C	-65 to +150

Note:

1. Operation in excess of any one of these parameters may result in permanent damage.

ELECTRICAL CHARACTERISTICS (T_A = 25°C)

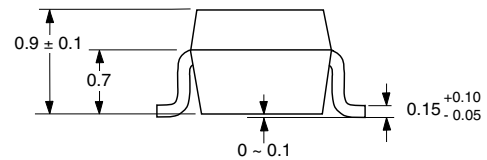
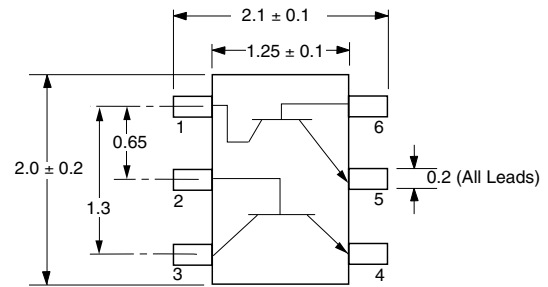
PART NUMBER PACKAGE OUTLINE			UPA811T S06		
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
I _{CBO}	Collector Cutoff Current at V _{CB} = 10 V, I _E = 0	μA			1.0
I _{EBO}	Emitter Cutoff Current at V _{EB} = 1 V, I _C = 0	μA			1.0
h _{FE} ¹	Forward Current Gain at V _{CE} = 3 V, I _C = 5 mA		80	120	200
f _T	Gain Bandwidth at V _{CE} = 3 V, I _C = 5 mA	GHz	5.5	8.0	
C _{re} ²	Feedback Capacitance at V _{CB} = 3 V, I _E = 0, f = 1 MHz	pF		0.3	0.7
IS _{21EI} ²	Insertion Power Gain at V _{CE} = 3 V, I _C = 5 mA, f = 2 GHz	dB	5.5	7.5	
NF	Noise Figure at V _{CE} = 3 V, I _C = 5 mA, f = 2 GHz	dB		1.9	3.2

Notes:

1. Pulsed measurement, pulse width ≤ 350 μs, duty cycle ≤ 2 %.
 2. The emitter terminal should be connected to the ground terminal of the 3 terminal capacitance bridge.
- For Tape and Reel version use part number UPA811T-T1, 3K per reel.

OUTLINE DIMENSIONS (Units in mm)

PACKAGE OUTLINE S06 (Top View)



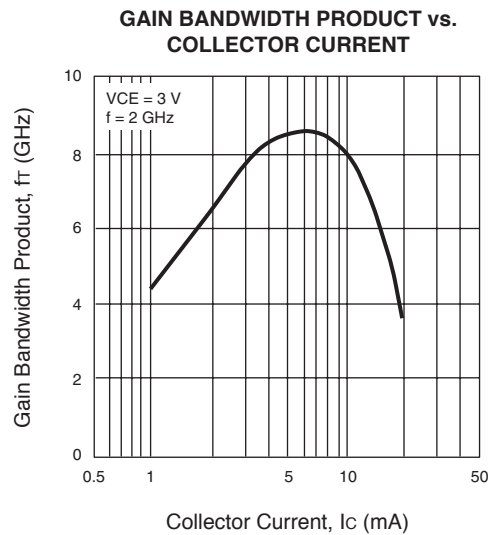
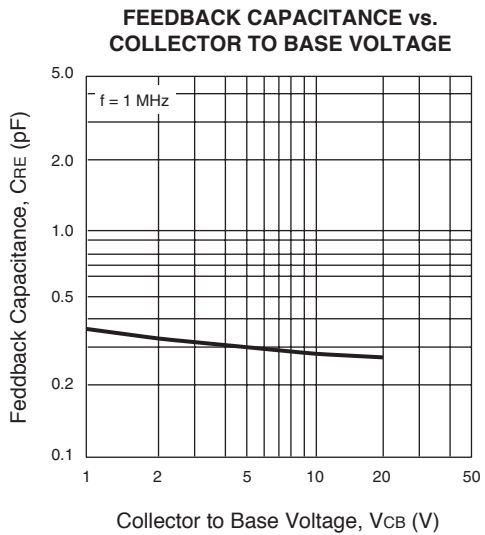
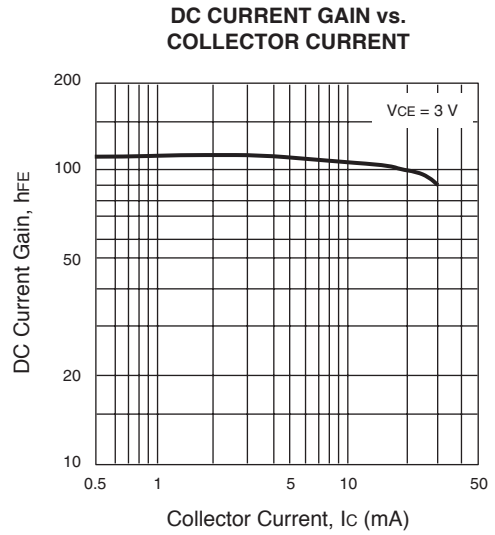
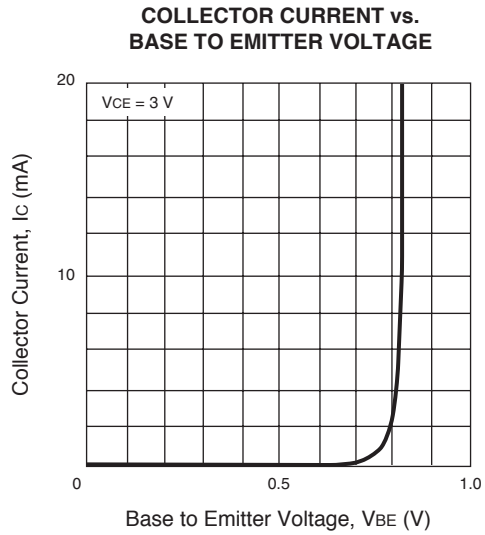
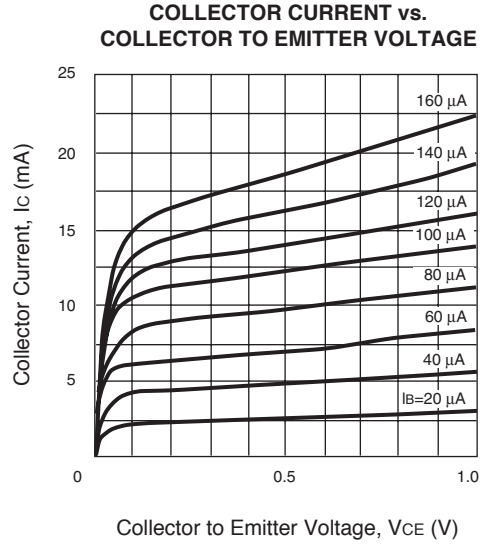
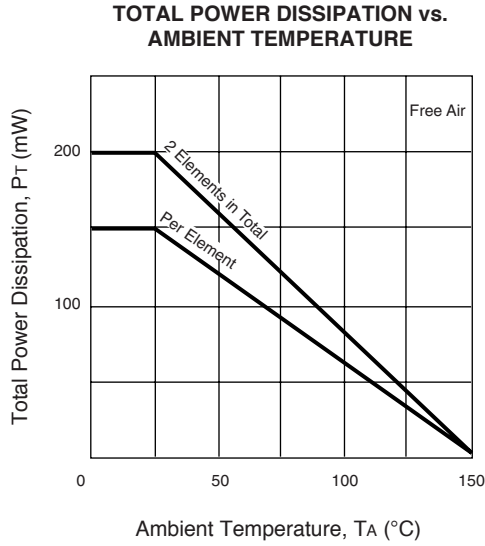
PIN OUT

1. Collector Transistor 1
2. Base Transistor 2
3. Collector Transistor 2
4. Emitter Transistor 2
5. Emitter Transistor 1
6. Base Transistor 1

Note:

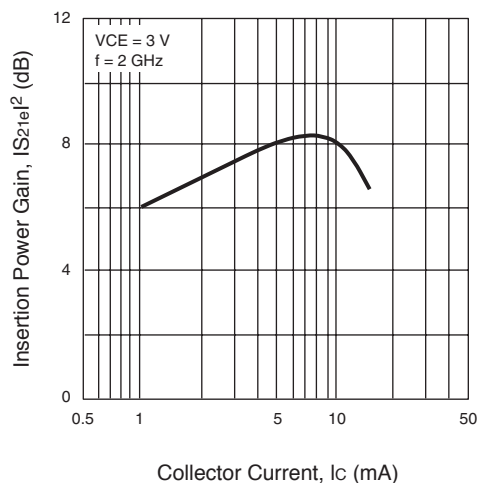
Pin 3 is identified with a circle on the bottom of the package.

TYPICAL PERFORMANCE CURVES (T_A = 25°C)

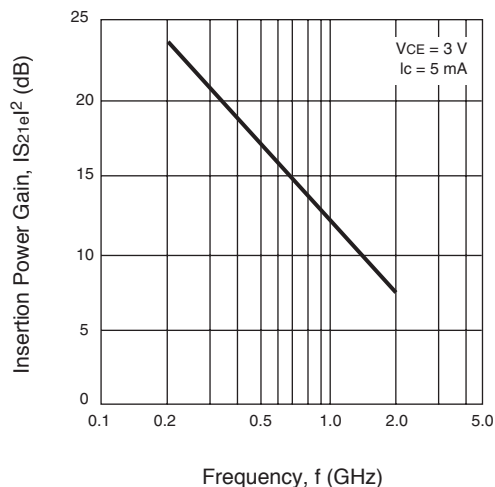


TYPICAL PERFORMANCE CURVES (T_A = 25°C)

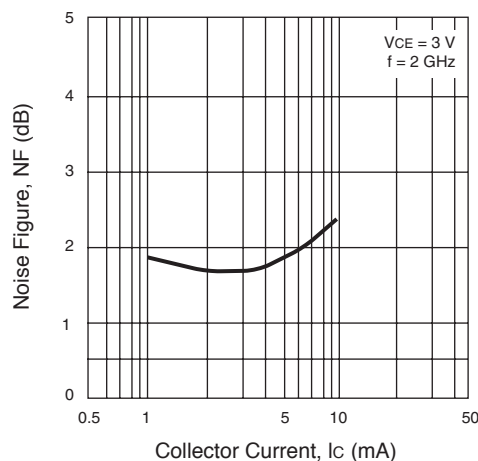
INSERTION POWER GAIN vs.
COLLECTOR CURRENT



INSERTION POWER GAIN vs.
FREQUENCY



NOISE FIGURE vs.
COLLECTOR CURRENT



ORDERING INFORMATION

PART NUMBER	QUANTITY	PACKAGING
UPA811T-T1-A	3000	Tape & Reel

Life Support Applications

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CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL’s understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices	
		-A	-AZ
Lead (Pb)	< 1000 PPM	Not Detected	(*)
Mercury	< 1000 PPM	Not Detected	
Cadmium	< 100 PPM	Not Detected	
Hexavalent Chromium	< 1000 PPM	Not Detected	
PBB	< 1000 PPM	Not Detected	
PBDE	< 1000 PPM	Not Detected	

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