

Small Plastic Package, Dual SPDT Analog Switch
Features

- CMOS Technology for Bus and Analog Applications
- Low On-Resistance: 0.6Ω
- Wide V_{DD} Range: 2.7V to 4.2V $\pm 10\%$
- Rail-to-Rail Signal Range
- High Off Isolation: -75dB @ 100kHz
- Crosstalk Rejection Reduces Signal Distortion: -77dB @ 100kHz
- Break-Before-Make Switching
- Extended Industrial Temperature Range: -40°C to 85°C
- Packaging (Pb-free & Green):
 - 10-contact TQFN (ZL10)
 - 10-contact TQFN (ZM10)

Applications

- Cell Phones
- PDAs
- MP3 players
- Portable Instrumentation
- Computer Peripherals
- Speaker Headset Switching
- Power Routing
- Relay Replacement
- Audio and Video Signal Routing
- PCMCIA Cards
- Modems

Pin Description

Pin #	Name	Description
2, 10	NO _X	Data Port (Normally open)
6	GND	Ground
5, 7	NC _X	Data Port (Normally closed)
3, 9	COM _X	Common Output / Data Port
1	V _{DD}	Positive Power Supply
4, 8	IN _X	Logic Control

Logic Function Table

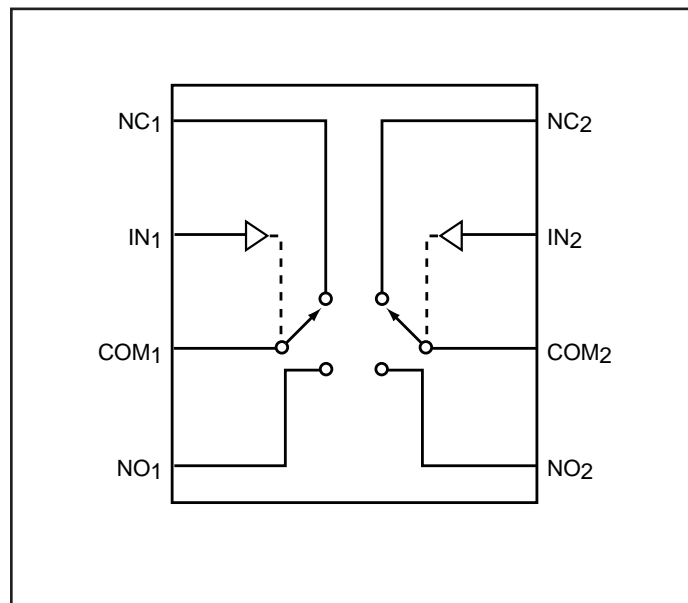
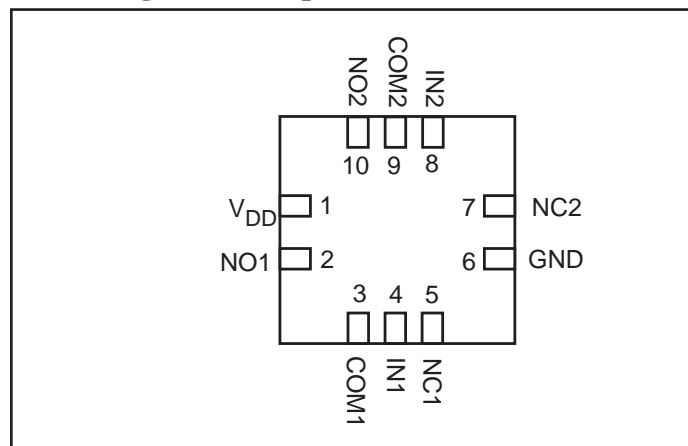
Logic Input (IN _X)	Function
0	NC _X Connected to COM _X
1	NO _X Connected to COM _X

Note: x = 1 or 2

Description

Pericom Semiconductor's PI3A223 is a dual, fast single-pole double throw (SPDT) CMOS switch. It can be used as an analog switch or as a low-delay bus switch. Specified over a wide operating power supply voltage, 2.7V to 4.2V, the PI3A223 has an On-Resistance of 0.6Ω at +2.7V.

Break-before-make switching prevents both switches being enabled simultaneously. This eliminates signal disruption during switching.

Functional Block Diagram

Pin Configuration (top view)


Absolute Maximum Ratings⁽¹⁾	Recommended Operating Conditions⁽³⁾
Supply Voltage V_{DD} -0.5V to 4.6V	Supply Voltage Operating (V_{DD}) 2.7V to 4.2V $\pm 10\%$
Control Input Voltage (V_{INx}) 0V to 5V	Control Input Voltage (V_{IN}) 0V to V_{DD}
DC Input Voltage (V_{INPUT}) ⁽²⁾ -0.5V to 4.6V	Switch Input Voltage (V_{INPUT}) -0.3V to V_{DD}
Continuous Current NO_NC_COM_ ± 300 mA	Operating Temperature (T_A) -40°C to +85°C
Peak Current NO_NC_COM_ ± 400 mA	Input Rise and Fall Time (t_r, t_f)
(pulsed at 1ms 50% duty cycle) ± 400 mA	Control Input $V_{DD} = 2.3V - 3.6V$ 0ns/V to 10ns/V
Peak Current NO_NC_COM_ ± 500 mA	Thermal Resistance (θ_{JA}) 350°C/W
(pulsed at 1ms 10% duty cycle) ± 500 mA	Lead Temperature (soldering 10s) +300°C
Storage Temperature Range (T_{STG}) -65°C to +150°C	Bump Temperature (soldering notes)
Junction Temperature under Bias (T_J) 150°C	Infared (15s) +220°C
Junction Lead Temperature (T_L)	Vapor Phase (60ns) +215°C
(Soldering, 10 seconds) 260°C	
Power Dissipation (P_D) @ +85°C 250mW	

Notes:

1. "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions beyond those indicated in the operational sections of this specification is not implied.
2. The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.
3. Control input must be held HIGH or LOW; it must not float.

DC Electrical Characteristics +3V Supply

($V_{DD} = 2.7V$ to $3.3V$, $T_A = -40^{\circ}C$ to $85^{\circ}C$, unless otherwise noted. Typical values are at $3V$ and $+25^{\circ}C$.)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Analog Switch						
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}		-0.3		V_{DD}	V
NC On-Resistance	$R_{ON(NC)}$	$V_{DD} = 2.7V, I_{COM} = 100mA,$ $V_{NC} = 0$ to V_{CC}		0.6	0.8	Ω
NO On-Resistance	$R_{ON(NO)}$	$V_{DD} = 2.7V, I_{COM} = 100mA,$ $V_{NO} = 0$ to V_{CC}		0.6	0.8	
On-Resistance Match Between Channels	ΔR_{ON}	$V_{DD} = 2.7V, I_{COM} = 100mA,$ V_{NO} or $V_{NC} = 1.5V$		0.01	0.06	
NC On-Resistance Flatness	$R_{ONF(NC)}$	$V_{DD} = 2.7V, I_{COM} = 100mA,$ $V_{NC} = 0$ to V_{CC}			0.2	
NO On-Resistance Flatness	$R_{ONF(NO)}$	$V_{DD} = 2.7V, I_{COM} = 100mA,$ $V_{NO} = 0$ to V_{CC}			0.2	
NO or NC Off Leakage Current	$I_{OFF(NO)}$ or $I_{OFF(NC)}$	$V_{DD} = 3.3V, V_{NO}$ or $V_{NC} = 3V, 0.3V, V_{COM} = 0.3V,$ $3V$	-400		400	
COM On Leakage Current	$I_{COM(ON)}$	$V_{DD} = 3.3V, V_{NO}$ or $V_{NC} = 3V, 0.3V, V_{COM} = 3V,$ $0.3V,$ or floating	-160		160	%
Total Harmonic Distortion	THD	Load = 16Ω , $V_{DD} = 2.7V, V_{input} = 1.5V_{pp}$, Fre- quency = 20Hz to 20KHz		0.03		
Total Harmonic Distortion	THD	Load = 8Ω , $V_{DD} = 2.7V, V_{input} = 1.5V_{pp}$, Frequen- cy = 20Hz to 20KHz		0.035		
Digital I/O						
Input Logic High	V_{IH}		1.3			V
Input Logic Low	V_{IL}				0.6	
Input Hysteresis	V_H	$V_{DD} = 3.3V$		100		mV
IN Input Leakage Current	I_{IN}	$V_{IN} = 0$ or V_{CC}	-0.5		0.5	μA
Supply Current	I_{CC}	$V_{DD} = 3.6V, V_{IN} = 0$ or V_{DD}		3	7	

DC Electrical Characteristics +4.2V Supply

 ($V_{DD} = 4.2V$, $T_A = -40^{\circ}C$ to $85^{\circ}C$, unless otherwise noted. Typical values are at $+25^{\circ}C$.)

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Analog Switch						
Analog Signal Range	V_{NO}, V_{NC}, V_{COM}		-0.3		V_{DD}	V
NC On-Resistance	$R_{ON(NC)}$	$V_{DD} = 4.2V, I_{COM} = 100mA, V_{NC} = 0$ to V_{DD}		0.6	0.8	Ω
NO On-Resistance	$R_{ON(NO)}$	$V_{DD} = 4.2V, I_{COM} = 100mA, V_{NO} = 0$ to V_{DD}		0.6	0.8	
On-Resistance Match Between Channels	ΔR_{ON}	$V_{DD} = 4.2V, I_{COM} = 100mA, V_{NO}$ or $V_{NC} = 1.5V$		0.01	0.06	
NC On-Resistance Flatness	$R_{ONF(NC)}$	$V_{DD} = 4.2V, I_{COM} = 100mA, V_{NC} = 0$ to V_{DD}			0.2	
NO On-Resistance Flatness	$R_{ONF(NO)}$	$V_{DD} = 4.2V, I_{COM} = 100mA, V_{NO} = 0$ to V_{DD}			0.2	
NO or NC Off Leakage Current	$I_{OFF(NO)}$ or $I_{OFF(NC)}$	$V_{DD} = 4.2V, V_{NO}$ or $V_{NC} = 3V, 0.3V, V_{COM} = 0.3V, 3V$	-400		400	
COM On Leakage Current	$I_{COM(ON)}$	$V_{DD} = 4.2V, V_{NO}$ or $V_{NC} = 3V, 0.3V, V_{COM} = 3V, 0.3V$, or floating	-160		160	
Total Harmonic Distortion	THD	Load = 16Ω , $V_{DD} = 4.2V, V_{input} = 2.0V_{pp}$, Frequency = 20Hz to 20KHz		0.06		%
Total Harmonic Distortion	THD	Load = 8Ω , $V_{DD} = 4.2V, V_{input} = 2.0V_{pp}$, Frequency = 20Hz to 20KHz		0.065		
Digital I/O						
Input Logic High	V_{IH}		1.3			V
Input Logic Low	V_{IL}				0.6	
Input Hysteresis	V_H	$V_{DD} = 4.2V$		100		mV
IN Input Leakage Current	I_{IN}	$V_{IN} = 0$ or V_{DD}	-0.5		0.5	μA
Supply Current	I_{CC}	$V_{DD} = 4.2V, V_{IN} = 0$ or V_{DD}		3.5	10	

Switch and AC Characteristics

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Turn-On Time	t _{ON}	V _{DD} = 2.7V, V _{NO} or V _{NC} = 1.5V, R _L = 50Ω, C _L = 35pF, See Test Circuit Figure 1 & 2.		15	25	ns
Turn-Off Time	t _{OFF}	V _{DD} = 2.7V, V _{NO} or V _{NC} = 1.5V, R _L = 50Ω, C _L = 35pF, See Test Circuit Figure 1 & 2.		4	10	
Break-Before-Make Delay	t _{BBM}	V _{DD} = 2.7V, V _{NO} or V _{NC} = 1.5V, R _L = 50Ω, C _L = 35pF, See Test Circuit Figure 3.			20	
Charge Injection	Q	COM = 0, R _S = 0, C _L = 1nF, V _{DD} = 3.3V or 4.2V See Test Circuit Figure 4.		55		pC
Off-Isolation	O _{IRR}	C _L = 5pF, R _L = 50Ω, f = 100kHz, V _{COM} = 1 V _{RMS} , V _{DD} = 3.3V See Test Circuit Figure 5.		-77		dB
Crosstalk	X _{TALK}	C _L = 5pF, R _L = 50Ω, f = 100kHz, V _{COM} = 1 V _{RMS} , V _{DD} = 3.3V See Test Circuit Figure 6.		-77		
3dB Bandwidth	f _{3dB}	See Test Circuit Figure 9., V _{DD} = 3.3V		65		MHz

Capacitance

Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
NC Off Capacitance	C _{NC (OFF)}	f = 1MHz, See Test Circuit Figure 7.		31		pF
NO Off Capacitance	C _{NO (OFF)}	f = 1MHz, See Test Circuit Figure 7.		31		
NC On Capacitance	C _{NC (ON)}	f = 1MHz, See Test Circuit Figure 8.		90		
NO On Capacitance	C _{NO (ON)}	f = 1MHz, See Test Circuit Figure 8.		90		

Test Circuits and Timing Diagrams

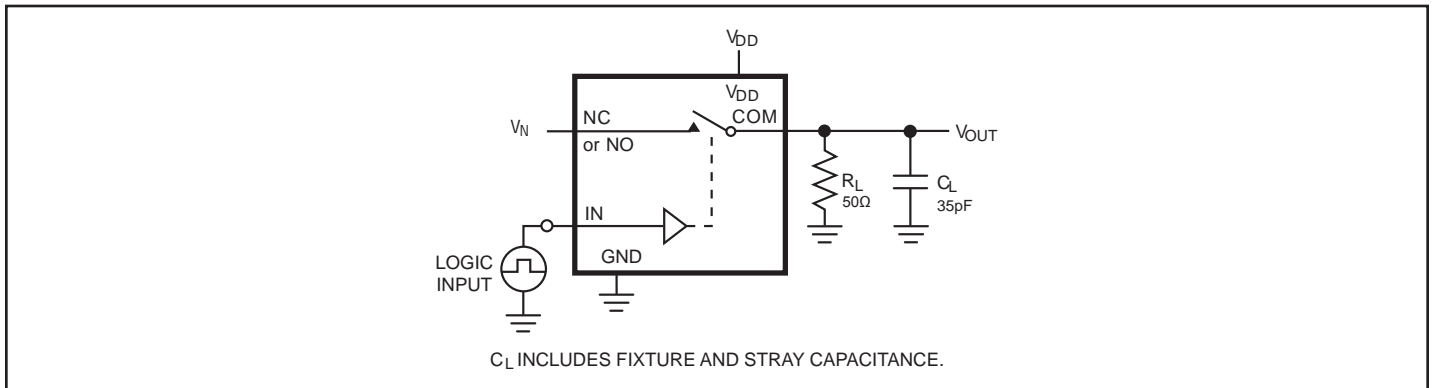


Figure 1. AC Test Circuit

Note:

1. Unused input (NC or NO) must be grounded.

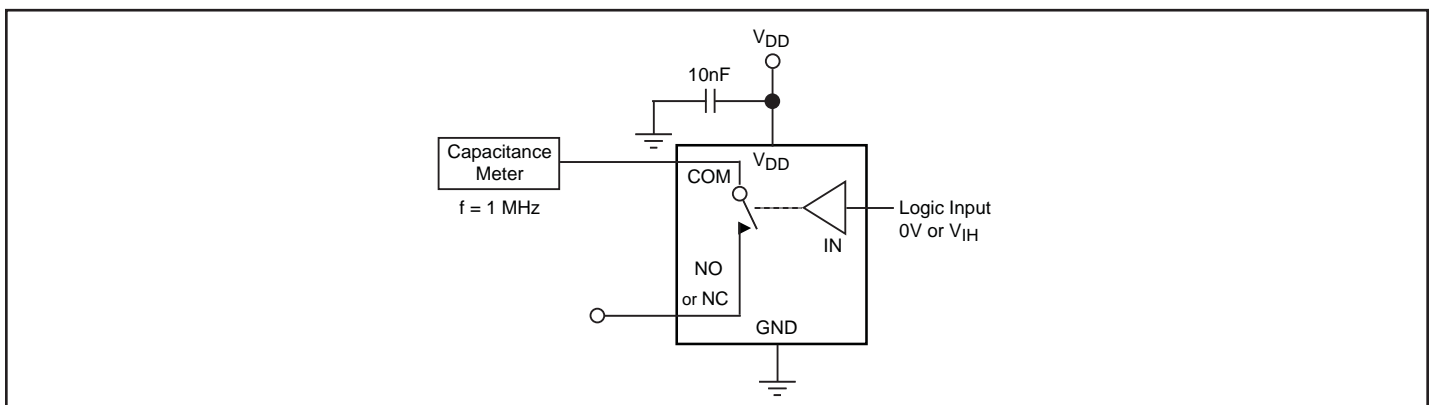


Figure 2. AC Waveforms

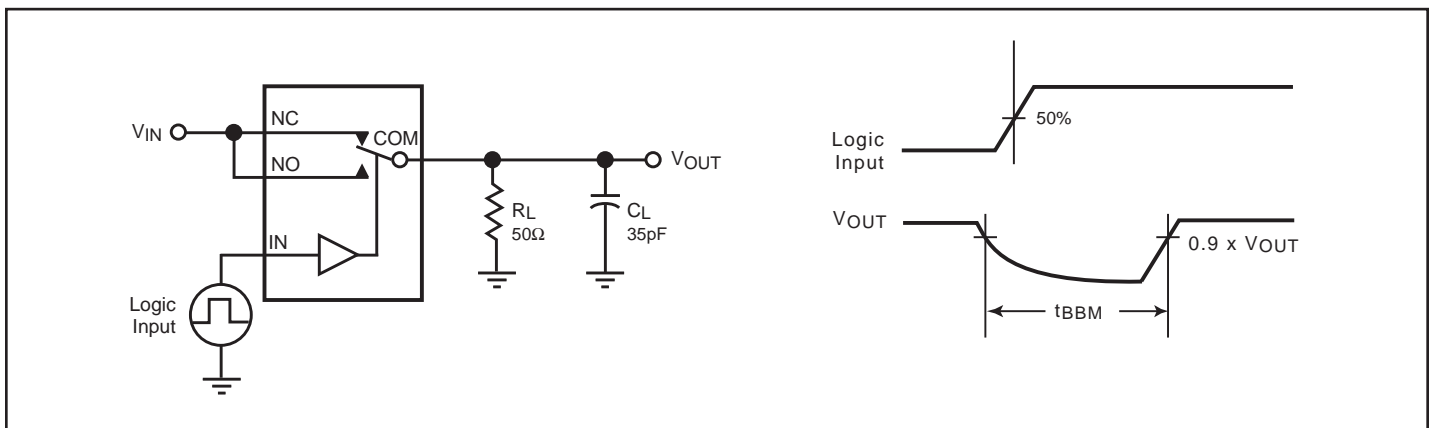


Figure 3. Break Before Make Interval Timing

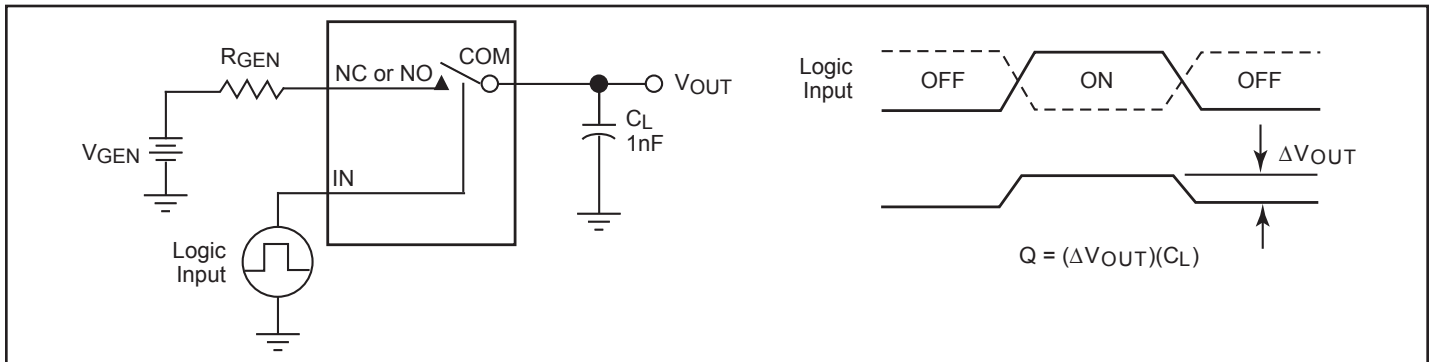


Figure 4. Charge Injection Test

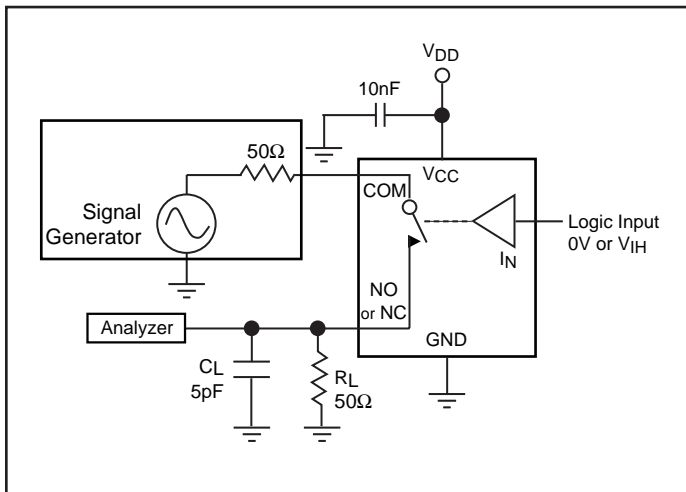


Figure 5. Off Isolation

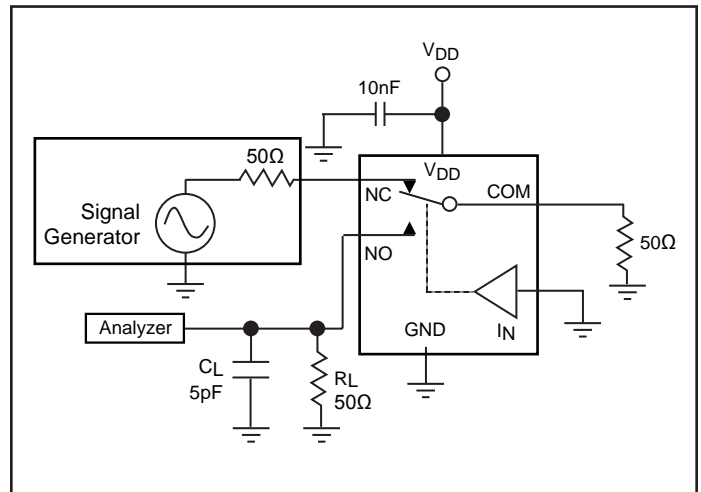


Figure 6. Crosstalk

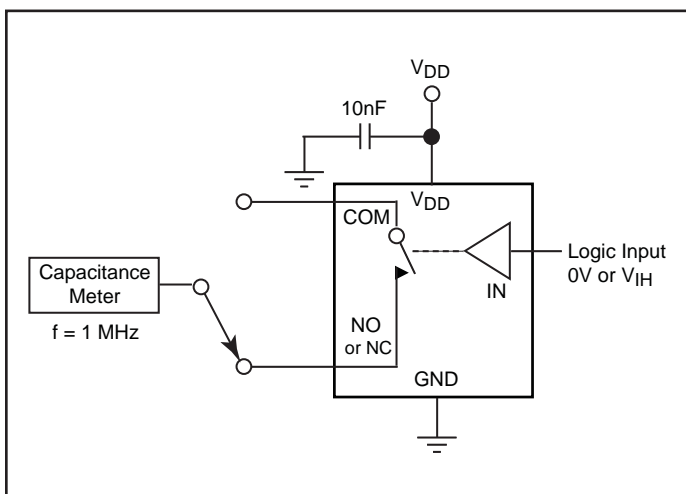


Figure 7. Channel Off Capacitance

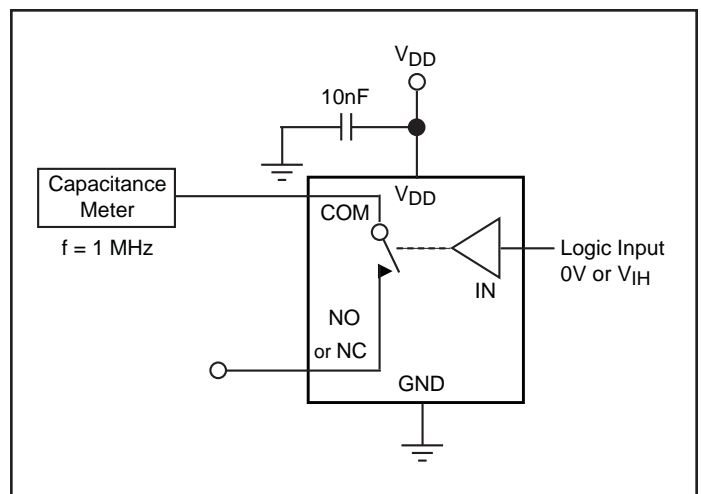


Figure 8. Channel On Capacitance

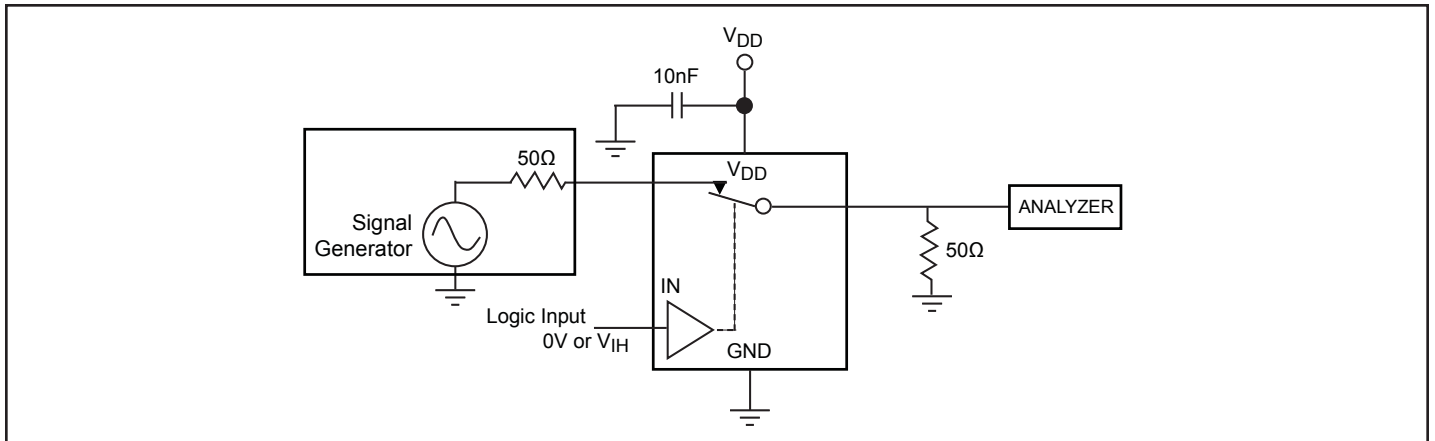
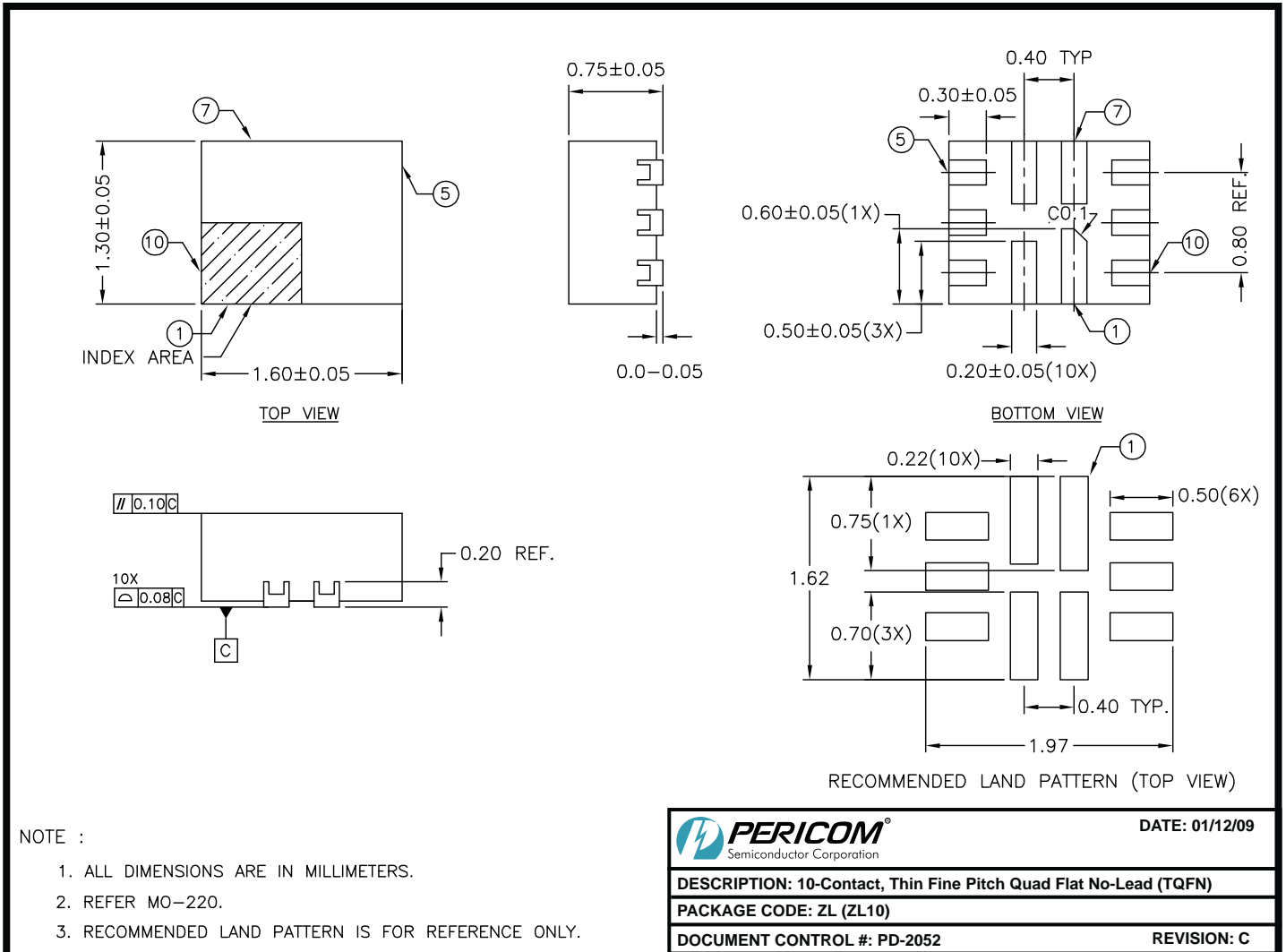
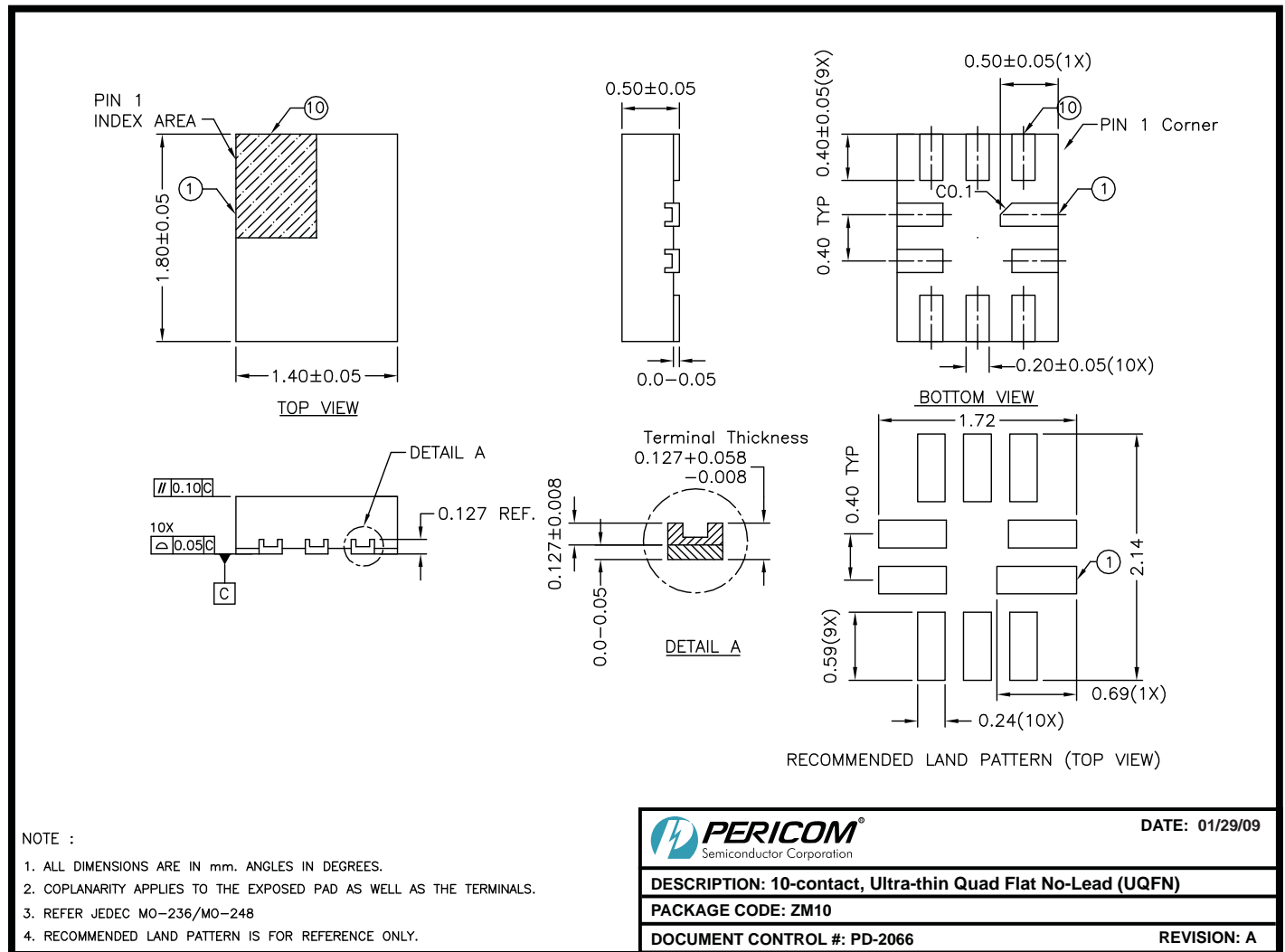


Figure 9. Bandwidth

Packaging Mechanical: 10-pin TQFN (ZL)


09-0031

Packaging Mechanical: 10-pin TQFN (ZM10)


09-0072

Ordering Information

Ordering Code	Packaging Code	Package Type	Top Mark
PI3A223ZLE	ZL	1.3x1.6M Pb-Free & Green, 10-contact TQFN	GD
PI3A223ZME	ZM	1.4 X 1.8, Pb-Free & Green, 10-contact TQFN	GD

Notes:

- Thermal characteristics can be found on the company web site at www.pericom.com/packaging/
- E = Pb-free & Green
- X suffix = Tape/Reel