TOSHIBA Power MOS FET Module Silicon P Channel MOS Type (Four L<sup>2</sup>-π-MOSV inOne)

# **MP4211**

High Power, High Speed Switching Applications For Printer Head Pin Driver and Pulse Motor Driver For Solenoid Driver

- 4-V gate drivability •
- Small package by full molding (SIP 10 pin)
- High drain power dissipation (4 devices operation)  $: P_{T} = 4 W (Ta = 25^{\circ}C)$
- Low drain-source ON resistance: RDS (ON) =  $0.16 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 4.0 \text{ S}$  (typ.)
- Low leakage current: IGSS =  $\pm 10 \mu A (max) (VGS = \pm 16 V)$ 
  - $I_{DSS} = -100 \ \mu A \ (max) \ (V_{DS} = -60 \ V)$
- Enhancement-mode:  $V_{th} = -0.8$  to -2.0 V ( $V_{DS} = -10$  V,  $I_D = -1$  mA)

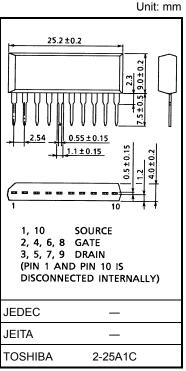
#### Characteristics Symbol Rating Unit Drain-source voltage -60 V VDSS Drain-gate voltage (R<sub>GS</sub> = 20 kΩ) -60 V VDGR V Gate-source voltage ±20 V<sub>GSS</sub> DC $I_D$ -5 Drain current А Pulse -20 IDP Drain power dissipation PD 2.0 W (1-device operation, Ta = 25°C) Drain power dissipation W PDT 4.0 (- device operation, Ta = 25°C) Single pulse avalanche energy EAS 273 mJ (Note 1) Avalanche current $I_{AR}$ -5 А 1-device 0.2 $\mathsf{E}_{\mathsf{AR}}$ operation Repetitive avalanche mJ energy (Note 2) 4-device EART 0.4 operation Channel temperature 150 °C T<sub>ch</sub> Storage temperature range Tstg -55 to 150 °C

#### Maximum Ratings ( $Ta = 25^{\circ}C$ )

Note 1:	Condition for avalanche energy (single pulse) measurement
	$V_{DD}$ = -25 V, starting T <sub>ch</sub> = 25°C, L = 14.84 mH, R <sub>G</sub> = 25 $\Omega$ , I <sub>AR</sub> = -5 A

Note 2: Repetitive rating; pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.

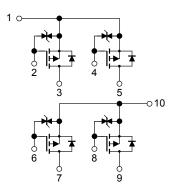


Industrial Applications

Weight: 2.1 g (typ.)

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### Array Configuration



#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit	
Thermal resistance from channel to ambient	ΣR <sub>th (ch-a)</sub>	31.2	°C/W	
(4-device operation, Ta = 25°C)	- ( /			
Maximum lead temperature for soldering purposes	ΤL	260	°C	
(3.2 mm from case for t = 10 s)				

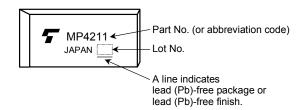
## Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Мах	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	—	±10	μA
Drain cut-off current		I <sub>DSS</sub>	$V_{DS}$ = -60 V, $V_{GS}$ = 0 V	_	_	-100	μA
Drain-source breakdown voltage		V (BR) DSS	I <sub>D</sub> = -10 mA, V <sub>GS</sub> = 0 V	-60	_	_	V
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-0.8	-	-2.0	V
Drain-source ON resistance		D	$V_{GS}$ = -4 V, I <sub>D</sub> = -2.5 A	-	0.24	0.28	Ω
		R <sub>DS (ON)</sub>	$V_{GS}$ = -10 V, I <sub>D</sub> = -2.5 A	-	0.16	0.19	
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS}$ = -10 V, I <sub>D</sub> = -2.5 A	2.0	4.0	_	S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = -10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	630	-	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	95	-	pF
Output capacitance		C <sub>oss</sub>		_	290	-	pF
Switching time	Rise time	tr	$V_{GS}$ $-10 V$ $V_{DD} \approx -30 V$ $I_{D} = -2.5 A$ $V_{OUT}$ $C$	_	25	_	
	Turn-on time	t <sub>on</sub>		-	45	_	20
	Fall time	t <sub>f</sub>		_	55	_	- ns
	Turn-off time	t <sub>off</sub>	$V_{IN}$ : t <sub>r</sub> , t <sub>f</sub> < 5 ns, duty ≤ 1%, t <sub>w</sub> = 10 µs		200	_	
Total gate charge (Gate-source plus gate-drain)		Qg	— V <sub>DD</sub> ≈ −48 V, V <sub>GS</sub> = −10 V, I <sub>D</sub> = −5 A	_	22	_	nC
Gate-source charge		Q <sub>gs</sub>		_	16	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>	]	_	6	_	nC

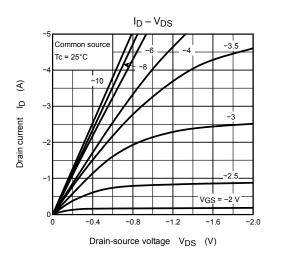
### Source-Drain Diode Ratings and Characteristics (Ta = 25°C)

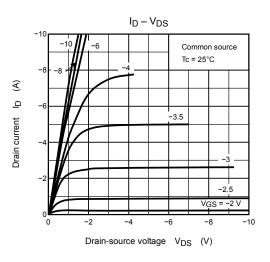
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current	I <sub>DR</sub>	—	—	_	-5	А
Pulse drain reverse current	I <sub>DRP</sub>	—	_	_	-20	А
Diode forward voltage	V <sub>DSF</sub>	$I_{DR}$ = -5 A, $V_{GS}$ = 0 V	_	_	1.7	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = -5 A, V <sub>GS</sub> = 0 V	_	80	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 50 A/µs		0.1	_	μC

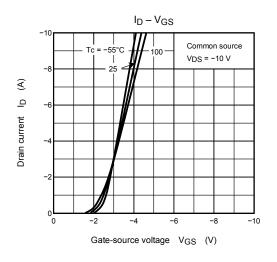
#### Marking

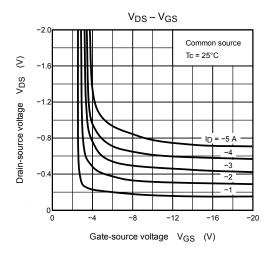


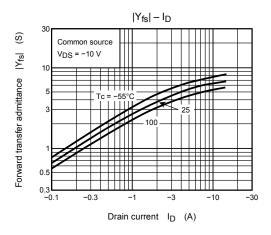
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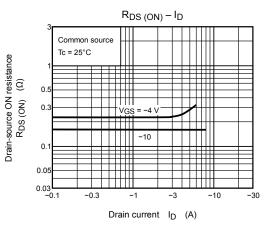




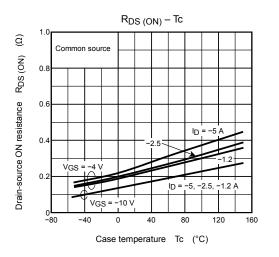


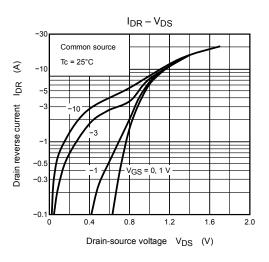


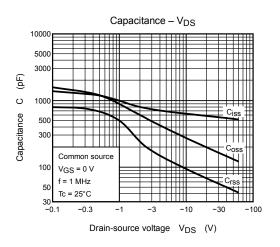


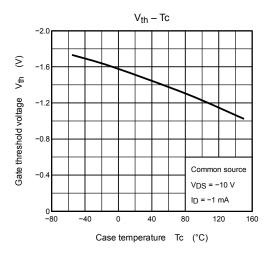


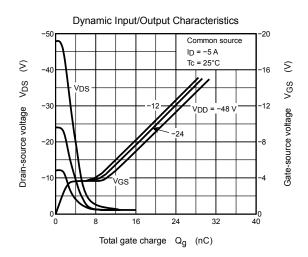
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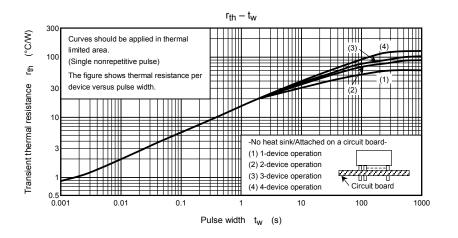


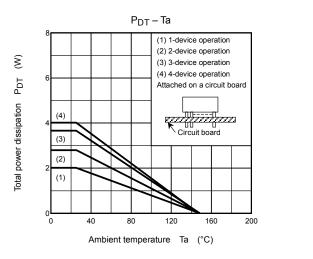


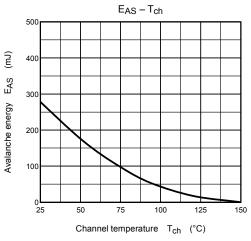


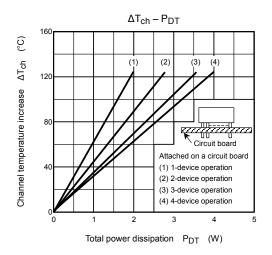


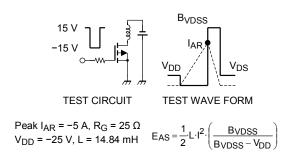
Safe Operating Area -3 IDP max 100 µs\* -10 E ID ma \_ Drain current ++++ 10 ms 100 ms Single nonrepetitive pulse Tc = 25°C -0.3 Curves must be derated linearly with increase in temperature -0.1 -1 -3 -10 -30 -100 -300 Drain-source voltage  $V_{DS}$  (V)











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