## **DZ2W082**

## Silicon epitaxial planar type

For constant voltage / waveform clipper and surge absorption circuit Capability of withstanding a high surge type DZ24082 in Mini2 type package

#### ■ Features

- Excellent rising characteristics of zener current IZ
- Low zener operating resistance R<sub>Z</sub>
- Contributes to miniaturization of sets, reduction of component count.
- Eco-friendly Halogen-free package

#### Packaging

Embossed type (Thermo-compression sealing): 3000 pcs / reel (standard)

## ■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Repetitive peak forward current	$I_{FRM}$	500	mA	
Total power dissipation *1	$P_{T}$	1	W	
Junction temperature	T <sub>j</sub>	150	°C	
Storage temperature	T <sub>stg</sub>	-55 to +150	°C	
Non-repetitive reverse surge power dissipation *2	P <sub>ZSM</sub>	100	W	

Note) \*1: Mounted on ceramics print circuit board.

Board size: 50 mm × 50 mm, Board thickness: 0.8 mm, Soldering size: 2 mm × 2 mm

### ■ Electrical Characteristics $T_a = 25$ °C±3°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Forward voltage	$V_{\mathrm{F}}$	$I_F = 200 \text{ mA}$			1.2	V
Zener voltage *1,2	V <sub>Z</sub>	$I_Z = 10 \text{ mA}$	7.79	8.20	8.61	V
Zener operating resistance	$R_Z$	$I_Z = 10 \text{ mA}$			30	Ω
Reverse current	$I_R$	$V_R = 4.0 \text{ V}$			10	μΑ
Temperature coefficient of zener voltage *3	$S_Z$	$I_Z = 10 \text{ mA}$		4.7		mV/°C

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7031 measuring methods for diodes.

- 2. Absolute frequency of input and output is 5 MHz.
- 3. \*1: The temperature must be controlled 25°C for  $V_Z$  measurement.  $V_Z$  value measured at other temperature must be adjusted to  $V_Z$  (25°C)
  - \*2: V<sub>Z</sub> guaranteed 20 ms after current flow.
  - \*3:  $T_i = 25^{\circ}C$  to  $150^{\circ}C$

#### ■ Package

Code

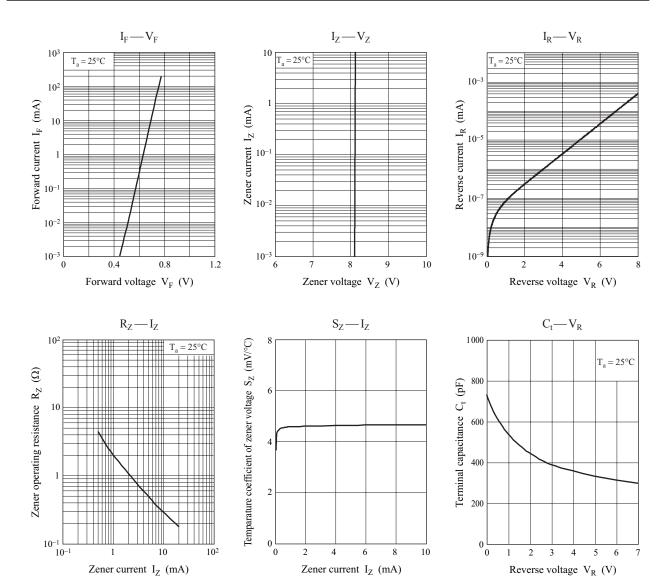
Mini2-F3-B

- Pin Name
  - 1. Cathode
  - 2. Anode

■ Marking Symbol: JJ

<sup>\*2:</sup> t = 0.1 ms

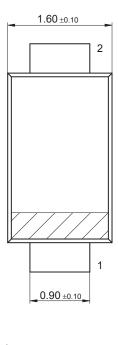
DZ2W082 Panasonic

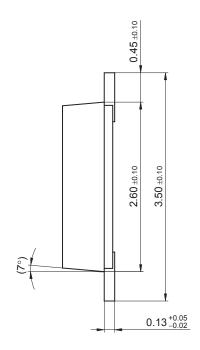


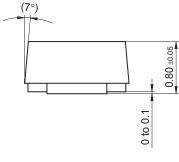
2 Ver. BED

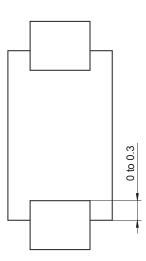
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