

CR08AS-12

Thyristor

Low Power Use

REJ03G0349-0400 Rev.4.00 Dec 10, 2008

Features

 $\begin{array}{ll} \bullet & I_{T\,(AV)}: 0.8\;A \\ \bullet & V_{DRM}: 600\;V \\ \bullet & I_{GT}: 100\;\mu A \end{array}$

Non-Insulated Type

• Glass Passivation Type

• Completed Pb Free

Outline

RENESAS Package code: PLZZ0004CA-A (Package name: UPAK)



RENESAS Package code: PLZZ0004CB-A (Package name: SOT-89)





1. Cathode

Anode
 Gate

4. Anode

Applications

Solid state relay, strobe flasher, igniter, and hybrid IC

Maximum Ratings

Parameter	Symbol	Voltage class	Unit	
Farameter	Symbol	12 (Mark AF)	Oilit	
Repetitive peak reverse voltage	V_{RRM}	600	V	
Non-repetitive peak reverse voltage	V_{RSM}	720	V	
DC reverse voltage	V _{R (DC)}	480	V	
Repetitive peak off-state voltage ^{Note1}	V_{DRM}	600	V	
DC off-state voltage ^{Note1}	V _{D (DC)}	480	V	

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	1.26	Α	
Average on-state current	I _{T (AV)}	0.8	А	Commercial frequency, sine half wave 180° conduction, Ta = 51°C ^{Note2}
Surge on-state current	I _{TSM}	10	А	60Hz sine half wave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.42	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P_{GM}	0.5	W	
Average gate power dissipation	P _{G (AV)}	0.1	W	
Peak gate forward voltage	V_{FGM}	6	V	
Peak gate reverse voltage	V_{RGM}	6	V	
Peak gate forward current	I _{FGM}	0.3	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	50	mg	Typical value

Notes: 1. With gate to cathode resistance R_{GK} = 1 $k\Omega$.

Electrical Characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I _{RRM}	_	_	0.5	mA	$Tj = 125$ °C, V_{RRM} applied, $R_{GK} = 1 \text{ k}\Omega$
Repetitive peak off-state current	I _{DRM}	_	_	0.5	mA	$Tj = 125$ °C, V_{DRM} applied, $R_{GK} = 1 \text{ k}\Omega$
On-state voltage	V_{TM}	_	_	1.5	V	Ta = 25°C, I_{TM} = 2.5 A, instantaneous value
Gate trigger voltage	V _{GT}	_	_	0.8	٧	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1 \text{ A}^{Note4}$
Gate non-trigger voltage	V_{GD}	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$, $R_{GK} = 1 k\Omega$
Gate trigger current	I _{GT}	20	_	100 ^{Note3}	μΑ	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1 \text{ A}^{Note4}$
Holding current	I _H	_	1.5	3	mA	$Tj = 25$ °C, $V_D = 12$ V, $R_{GK} = 1$ k Ω
Thermal resistance	R _{th (j-a)}	_	_	65	°C/W	Junction to ambient ^{Note2}

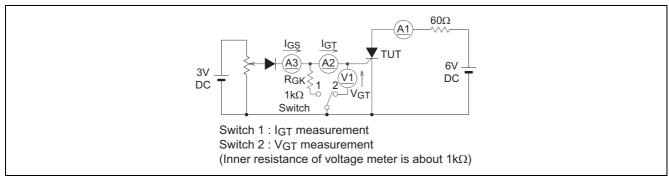
Notes: 2. Soldering with ceramic plate (25 mm \times 25 mm \times t0.7 mm).

3. If special values of I_{GT} are required, choose item E from those listed in the table below if possible.

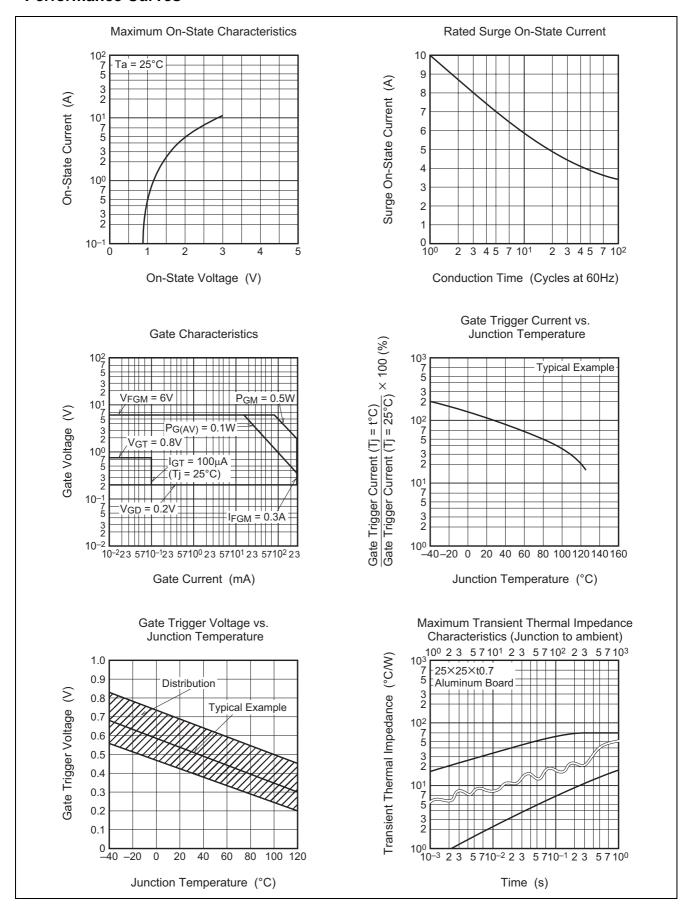
Item	Item B	
I _{GT} (μA)	20 to 50	20 to 100

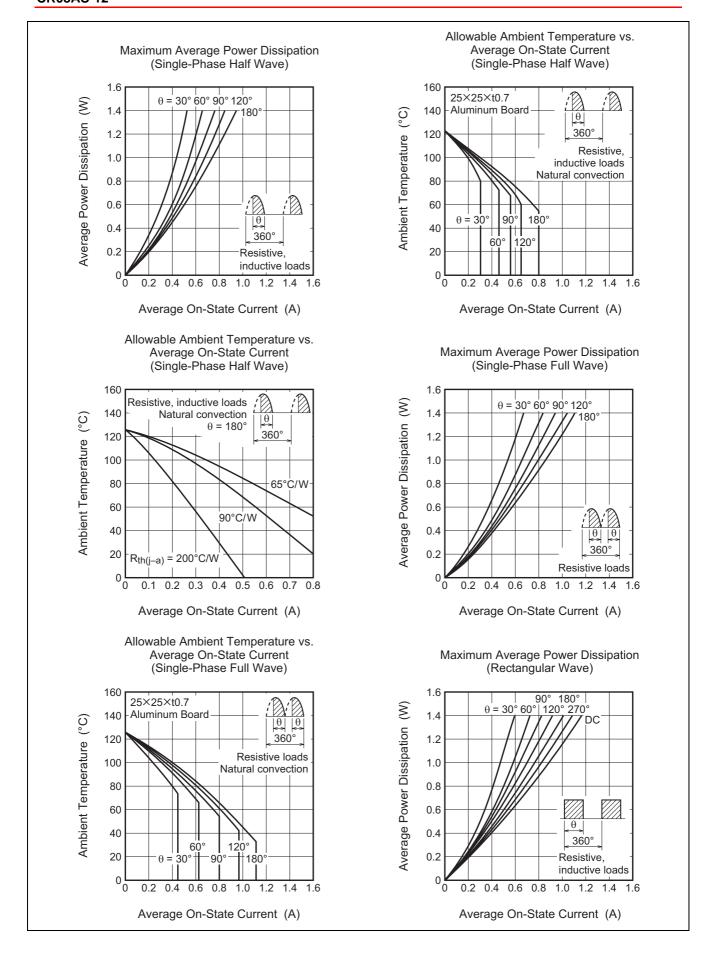
The above values do not include the current flowing through the 1 $k\Omega$ resistance between the gate and cathode.

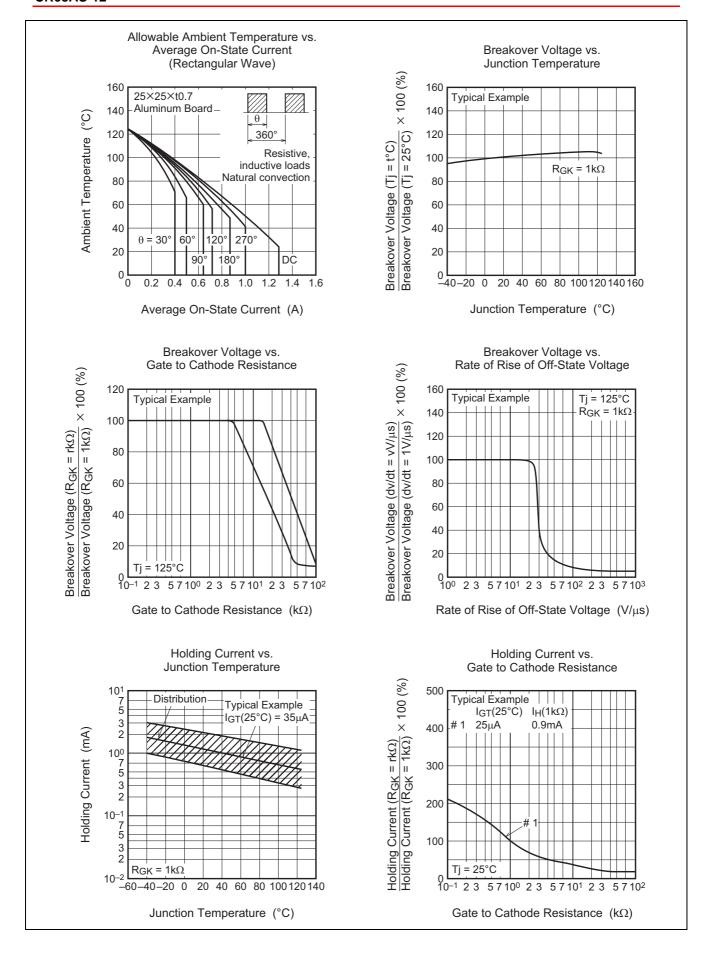
4. I_{GT} , V_{GT} measurement circuit.

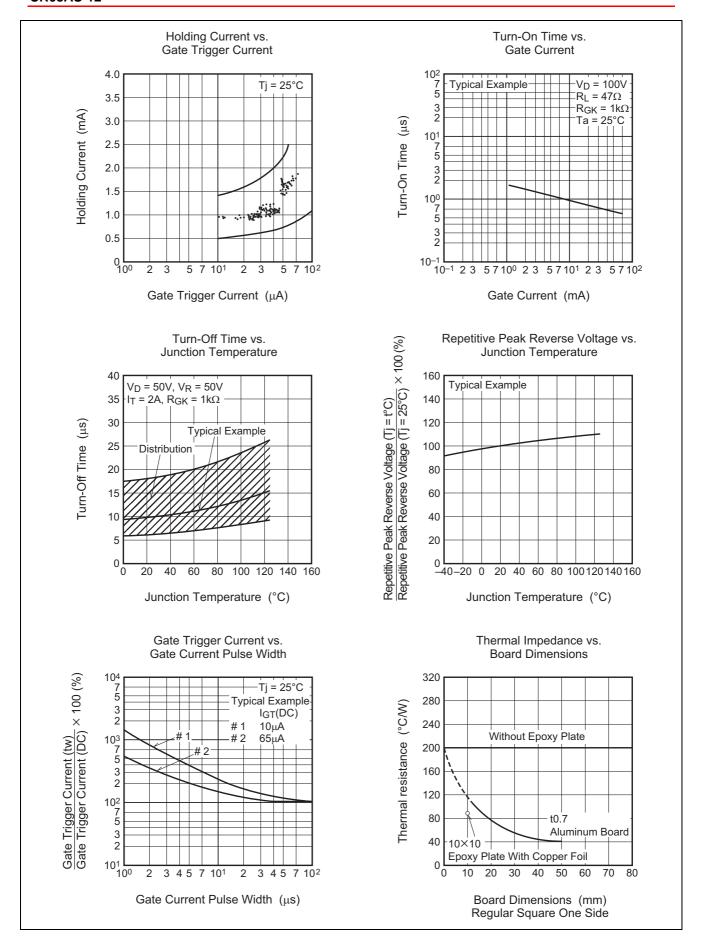


Performance Curves

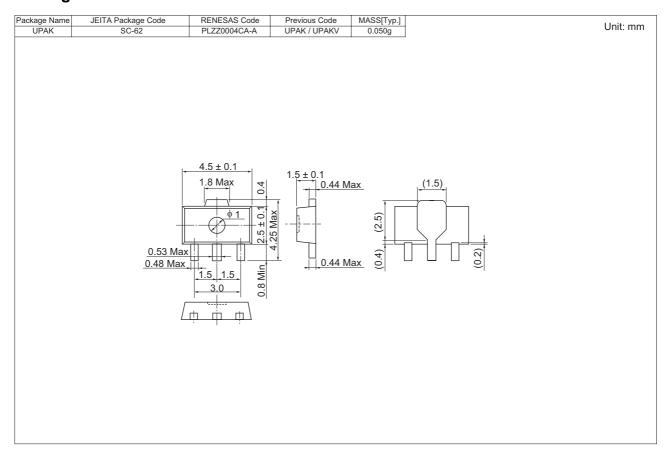


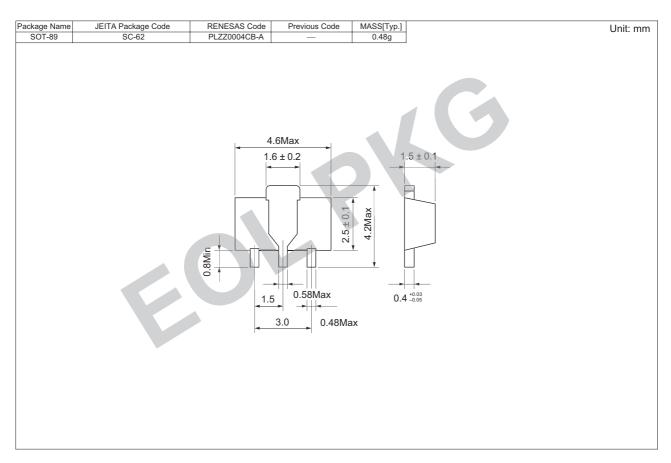






Package Dimensions





Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	4000	Type name – ET +Direction (1 or 2) +4	CR08AS-12-ET14

Note: Please confirm the specification about the shipping in detail.

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- Renesas lechnology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan Notes:

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