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April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

THYRISTORS 2S2M, 2S4M

2 A HIGH-SPEED SWITCHING SCR

The 2S2M and 2S4M are P-gate fully diffused mold SCRs with an average on-current of 2 A. The repeat peak off-voltages (and reverse voltages) are 200 V and 400 V.

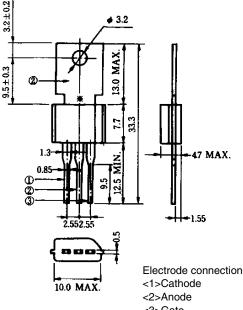
FEATURES

- · This transistor is designed for high-speed switching and is deal for use in commercial frequencies, high-frequency pulse applications, and inverter applications.
- · This transistor features a small and lightweight package and is easy to handle even on the mounting surface due to its TO-202AA dimensions. Processing of lead wires and heatsink (tablet) using jigs is also possible.
- Employs flame-retardant epoxy resin (UL94V-0).

ABSOLUTE MAXIMUM RATINGS (Ta = 25° C)

APPLICATIONS

Consumer electronic euipments, ignitors of devices for light indutry, inverter, and solenoid valve drives



<3>Gate Standard weight: 1.4

*TC test bench-mark

Parameter	Symbol	2S2M	2S4M	Ratings	Unit
Non-repetitive peak reverse voltage	VRSM	300	300 500		Rgк = 1 k Ω
Non-repetitive peak off-state voltage	VDSM	300 500		V	Rgк = 1 k Ω
Repetitive peak reverse voltage	VRRM	200	400	V	Rgк = 1 k Ω
Repetitive peak off-voltage	VDRM	200	400	V	R _{GK} = 1 k Ω
Average on-state current	IT(AV)	2 (Tc = 77°C, Single	Α	Refer to Figure 6 snd 7.	
Surge on-state current	Ітѕм	20 (f = 50 Hz, Sine	Α	Refer to Figure 2.	
High-frequency peak on-state current	ITRM	15 (Tc = 65°C, f = 1	Α	-	
Fusing current	∫iťdt	1.6 (1 ms:	A ² s	-	
Critical rate of rise of on-state current	dl⊤/dt	5	A/μs	-	
Peak gate power dissipation	Рсм	0.5 (f≥50 Hz	W	-	
Average gate power dissipation	PG(AV)	0.	W		
Peak gate forward current	IFGM	0.2 (f≥50 Hz	Α	-	
Peak gate reverse voltage	VRGM	e	V	_	
Junction temperature	Tj	–40 to	°C	_	
Storage temperature	Tstg	–55 tp	°C	_	

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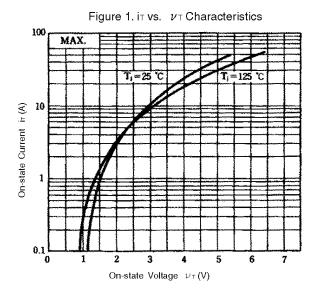
availability and additional information.

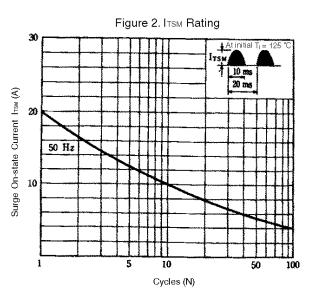
PACKAGE DRAWING (UNIT: mm)

ELECTRICAL CHARACTERISTICS (T_j = 25°C, R_{GK} = 1 k Ω)

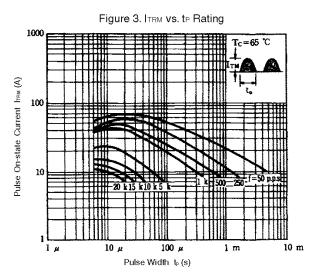
Parameter	Symbol	Conditions		Specifications			Unit	Remarks
				MIN.	TYP.	MAX.		
Repeat peak off-state	Idrm	Vdm = Vdrm	$T_j = 25^{\circ}C$			10	μA	-
current			T _j = 125°C			200		-
Repetitive peak reverse current	Irrm	Vrm = Vrrm	$T_j = 25^{\circ}C$			10	μA	-
			T _j = 125°C			200	V	Refer to Figure 1.
On voltage	Vтм	Тј = 25°С, Ітм = 4 А		_	_	2.2	V	Refer to Figure 9.
Gate trigger voltage	Vgt	$V_{DM} = 6 V, R_L = 100 \Omega$		-	_	0.8	μA	Refer to Figure 8.
Gate trigger current	Ідт	$V_{DM} = 6 V, R_L = 100 \Omega$		-	_	300	V	-
Gate non-trigger voltage	Vgd	$T_j = 125^{\circ}C, V_{DM} = \frac{1}{2}V_{DRM}$		0.2	-	-	V	-
Critical rate of-rise of off- state voltage	dv/dt	$T_{j} = 125^{\circ}C, V_{DM} = \frac{2}{3}V_{DRM}$		10	-	_	V/µs	-
Holding current	Ін	$T_j = 25^{\circ}C, V_D = 24 V$		_	-	10	mA	_
Commutating turn-off time	Tq	$T_j = 125^{\circ}C, I_T = 2 A$ $V_{DM} = \frac{2}{3}V_{DRM}, V_R = 50 V$		_		15	μs	
		dv/dt = 10 V/µs						
Turn-on time T _g		$T_j = 125^{\circ}C, V_{DM} = \frac{2}{3}V_{DRM}$		-	-	2	μs	-
		Iтм = 30 A Ig = 5 mA, tıg =	= 5 μs					
Thermal resistance	Thermal resistance Rth(j-c) Junction-to-case DC		se DC	-	-	10	°C/W	Refer to Figure 13.
R _{th(j-a)} Junctio		Junction-to-am	nction-to-ambient DC		-	75		

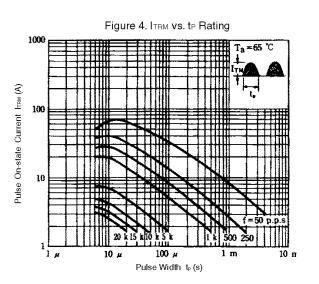
TYPICAL CHARACTERISTICS (Ta = 25°C)

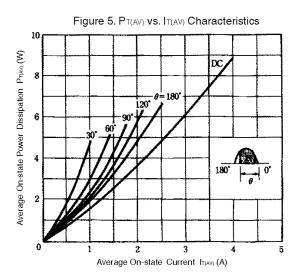


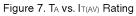


Data Sheet D13535EJ2V0DS









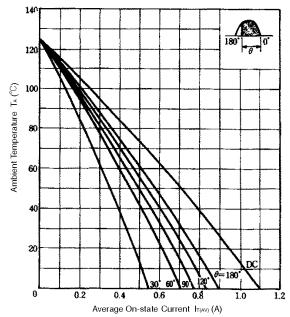


Figure 6. To vs. IT(AV) Rating

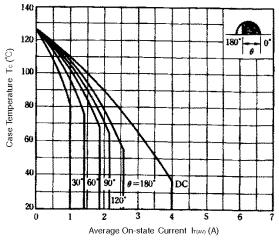
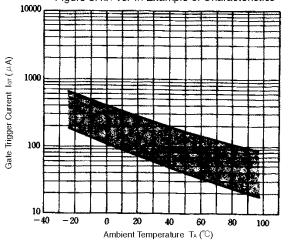
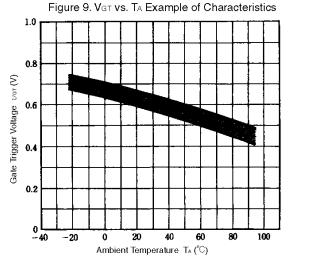
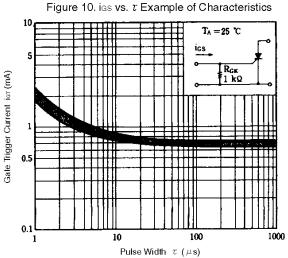


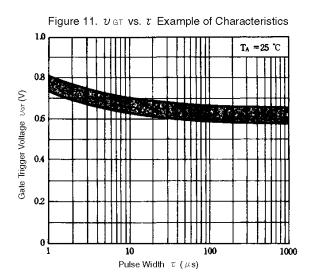
Figure 8. Igt vs. TA Example of Characteristics

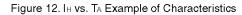


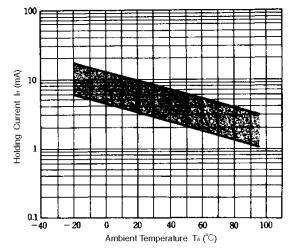
Data Sheet D13535EJ2V0DS

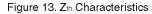


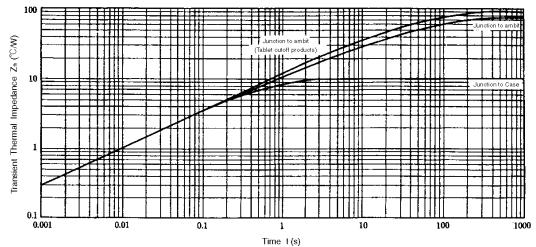












[MEMO]

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