TOSHIBA THYRISTOR SILICON PLANAR TYPE

S6370

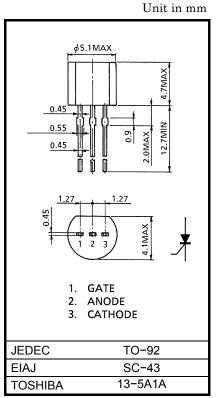
LOW POWER SWITCHING APPLICATIONS (STROBE TRIGGER)

• Repetitive Peak Off-State Voltage $: V_{DRM} = 400V$ • Repetitive Peak Reverse Voltage $: V_{RRM} = 400V$ • Fast Turn On Time $: t_{gt} = 1.5 \mu s$

• Plastic Mold Package (TO-92)

ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT	
Repetitive Peak Off–State Voltage and Repetitive Peak Reverse Voltage $(R_{GK}=1k\Omega)$	V _{DRM} V _{RRM}	400	٧	
Non-Repetitive Peak Reverse Voltage (Non-rep<5ms, R_{GK} =1k Ω , T_j =0~125°C)	V _{RSM}	450	V	
Average On-State Current (Half Sine Waveform Ta=45°C)	I _{T (AV)} 300		mA	
R.M.S. On-State Current	I _{T (RMS)}	450	mW	
Peak One Cycle Surge On-State		9 (50Hz)	Α	
Current (Non-Repetitive)	ITSM	9.9 (60Hz)		
Peak Gate Power Dissipation	P _{GM}	0.1	W	
Average Gate Power Dissipation	P _{G (AV)}	0.01	W	
Peak Reverse Gate Voltage	V_{RGM}	-5	V	
Peak Forward Gate Current	I _{GM}	125	mA	
Junction Temperature	Tj	-40~125	°C	
Storage Temperature Range	T _{stg}	-40~125	°C	

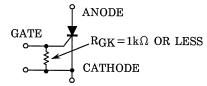


Weight: 0.2 g

Note1: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

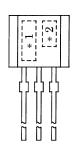
Note 2: Use with gate resistance by all means



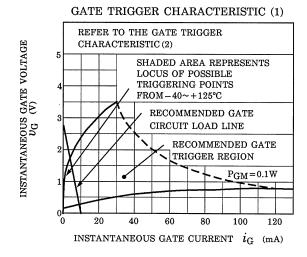
ELECTRICAL CHARACTERISTICS (Ta = 25°C)

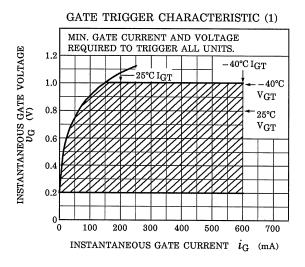
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Repetitive Peak Off-State Current and Repetitive Peak Reverse Current	I _{DRM} I _{RRM}	$V_{DRM} = V_{RRM} = 400V$ $R_{GK} = 1k\Omega$	_	_	10	μА
Peak On-State Voltage	V _{TM}	I _{TM} = 2A	_	_	2.0	V
Gate Trigger Voltage	V _{GT}	$V_D = 6V$, $R_L = 100Ω$, $R_{GK} = 1kΩ$	_	_	0.8	٧
Gate Trigger Current	I _{GT}	VD - 0V, NL - 10052, NGK - 1852	_	_	200	μА
Turn On Time	t _{gt}	V _D = 400V, i _G = 5mA	_	_	1.5	μS
Gate Non-Trigger Voltage	V_{GD}	$V_D = 6V$, $R_{GK} = 1k\Omega$	0.2	_	_	V
Holding Current	lΗ	$R_L = 100\Omega$, $R_{GK} = 1k\Omega$	_	4	_	mA
Thermal Resistance	R _{th (j−a)}	Junction to Ambient	_	_	250	°C / W

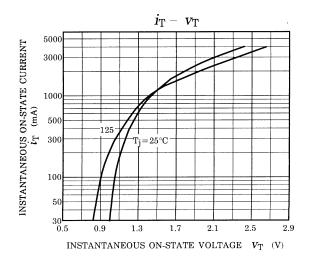
MARKING

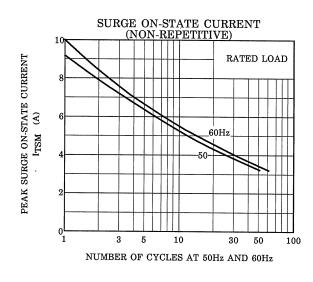


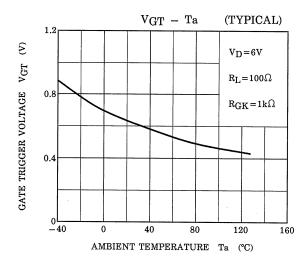
NUMBER	SYMBOL		MARK
*1	TYPE	S6370	S6370
*2	Lot Number Month (Starting from Alphabet A) Year (Last Decimal Digit of the Year of Manufacture)		Example 8A : January 1998 8B : February 1998 8L : December 1998

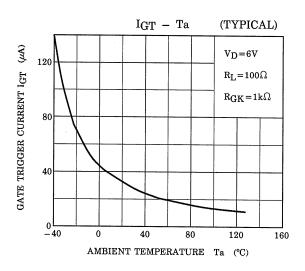


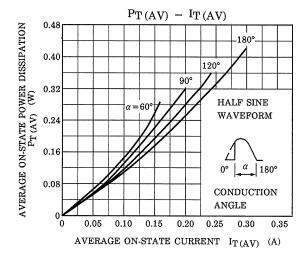


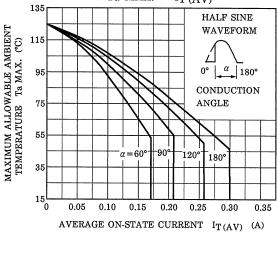




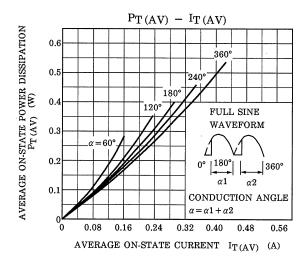


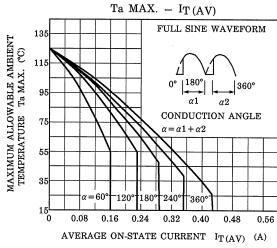


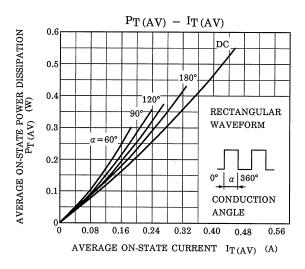


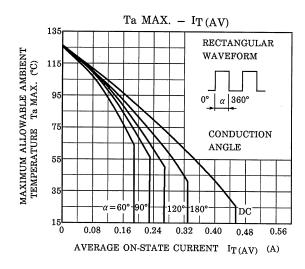


Ta MAX. - IT (AV)

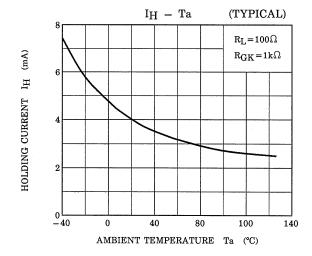


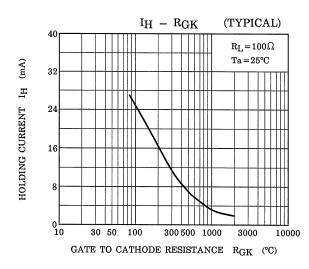


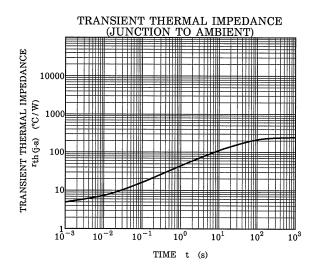




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20070701-EN

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