Unit: mm

TOSHIBA Diodes for Protecting against ESD Epitaxial Planar Type

DF2S16S

Product for Use Only as Protection against Electrostatic Discharge (ESD).

- * This product is for protection against electrostatic discharge (ESD) only and is not intended for any other usage, including without limitation, the constant voltage diode application.
- 2terminal ultra small package suitable for mounting on small space.
- Low total capacitance: C_T = 10 pF (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating
Power dissipation	P*	150 mW
Junction temperature	Tj	1,50 °C
Storage temperature range	T _{stg}	_55~150 °C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the

Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

*: Mounted on a glass-epoxy circuit board of 20 × 20 mm, pad dimensions of 4 × 4 mm.

SESC JEDEC JEITA TOSHIBA 1-1K1A Weight: 0.0011 g (typ.)

Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Zener voltage	V _Z		I _Z = 5 mA	15.3	16.0	17.1	V
Dynamic impedance	∠Z _Z	_	I _Z = 5 mA	_	_	35	Ω
Reverse current	↓R	_	V _R = 12 V	_	_	0.5	μA
Total capacitance	$\begin{pmatrix} c_{\dagger} \end{pmatrix}$	_	V _R = 0 V, f = 1 MHz	1	10	-	pF

Guaranteed Level of ESD Immunity

Test Condition	ESD Immunity Level
IEC61000-4-2 (contact discharge)	± 12 kV

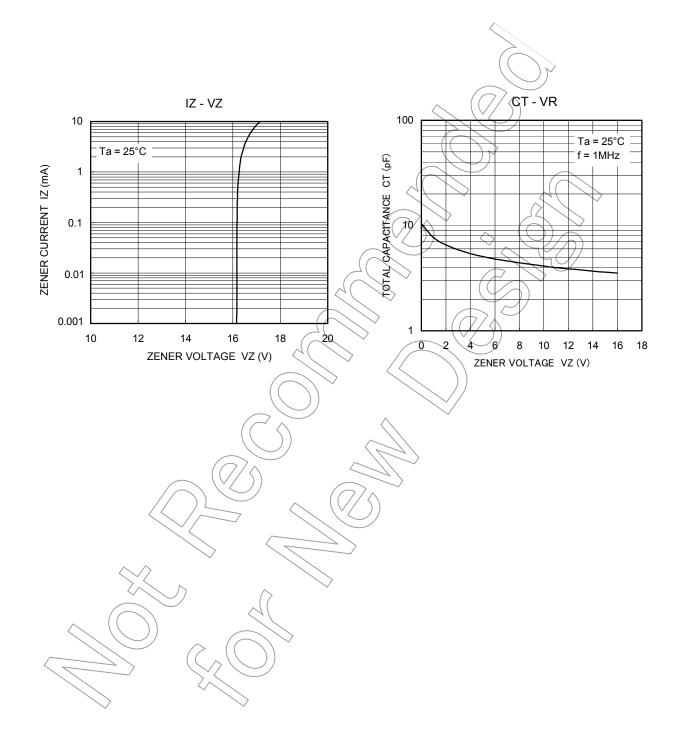
Judgment contents: No element destruction

Marking

Equivalent Circuit (Top View)







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